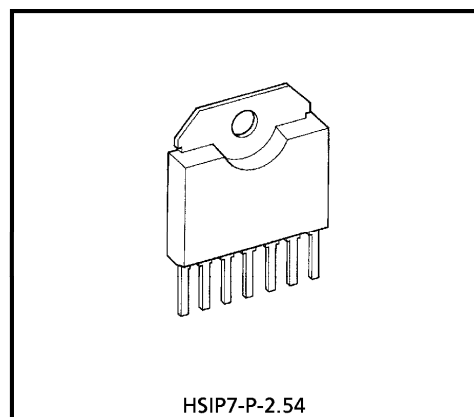


TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# TA8050P

## 1.5A MOTOR DRIVER WITH BRAKE FUNCTION

The TA8050P is a 1.5A motor driver which directly drives a bidirectional DC motor. Inputs DI1 and DI2 are combined to select one of forward, reverse, stop, and brake modes. Since the inputs are TTL-compatible, this IC can be controlled directly from a CPU or other control system. The IC also has various protective functions.

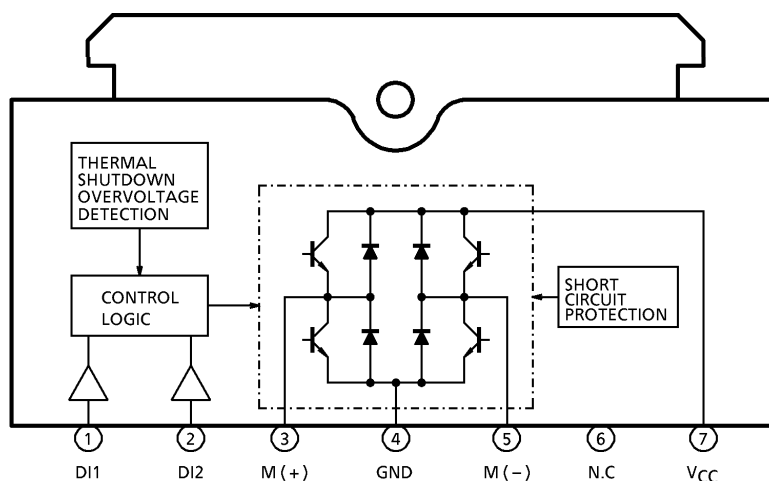


Weight : 1.9g (Typ.)

### FEATURES

- Bidirectional DC motor driver
- Current capacity : 1.5A
- Four modes : Forward, Reverse, Stop, and Brake
- Protective functions : Thermal Shutdown, Short Circuit Protection, and Overvoltage Shutdown
- Built-in diode for counteracting counter electromotive force
- Plastic HSIP-7 pin

### BLOCK DIAGRAM AND PIN LAYOUT



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**PIN DESCRIPTION**

PIN No.	SYMBOL	DESCRIPTION
1	DI1	Output status control pin.
2	DI2	Connects to a PNP-type voltage comparator.
3	M (+)	Connects to the DC motor. Both the sink and the source have a current capacity of 1.5A. Diodes for absorbing counter electromotive force are contained on the V <sub>CC</sub> and GND sides.
4	GND	Grounded
5	M (-)	Connects to the DC motor together with pin 3 and has the same function as pin 3. This pin is controlled by the inputs from pins 1 and 2.
6	(N.C)	Not connected
7	V <sub>CC</sub>	Power supply pin. This pin has a function to turn off the output when the applied voltage exceeds 27.5V, thus protecting the IC and the load.

**TRUTH TABLE**

Input		Output	
DI1	DI2	M (+)	M (-)
H	H	L	L
L	H	L	H
H	L	H	L
L	L	OFF (high impedance)	

(Note)

(Note)

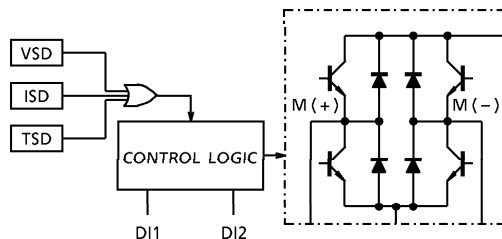
Note : Brake mode comes into effect when both M (+) and M (-) go low ; stop mode comes into effect when both M (+) and M (-) turn OFF.

**DESCRIPTION OF MULTI-PROTECTIVE OPERATION**

The TA8050P has functions for protection from overvoltage (V<sub>SD</sub>), overcurrent (I<sub>SD</sub>), and overheat (T<sub>SD</sub>). These functions protect the IC (and the motor load in some cases) from deterioration or destruction due to power-related overstress.

The three functions work independently.

Each function is explained below.



1. Overvoltage protection ( $V_{SD}$ )

## ● Basic operation

When the voltage supplied to the  $V_{CC}$  pin is up to the  $V_{SD}$  detection voltage, the output is controlled by the input signals. However, when the  $V_{CC}$  voltage exceeds the detection voltage, the output enters high-impedance state regardless of the input signals.

## ● Detailed explanation

The  $V_{SD}$  voltage is detected by comparing the Zener voltage with the voltage obtained by dividing  $V_{CC}$  with a resistor. When the center voltage of the resistor is higher than the Zener voltage, a transistor-off instruction is issued to the control logic. When it is lower than the Zener voltage, the logic is controlled by the input signals from pins 1 and 2.

2. Overheat protection ( $T_{SD}$ )

## ● Basic operation

When the junction (chip) temperature is up to the  $T_{SD}$  detection temperature, the output is controlled by the input signals. When it exceeds the  $T_{SD}$  detection temperature, the output enters high-impedance state regardless of the input signals.

## ● Detailed explanation

The temperature is detected by monitoring  $V_F$  of a diode on the chip. When the diode  $V_F$  is lower than the internal reference voltage, an output transistor-off instruction is issued to the control logic. When it is higher than the internal reference voltage, the logic is controlled by the input signals from pins 1 and 2.

3. Overcurrent protections ( $I_{SD}$ )

## ● Basic operation

When the output current (pin 3 or 5,  $I$  sink or  $I$  source) is up to the  $I_{SD}$  detection current, the output is controlled by the input signals. When it exceeds the detection current, the output assumes a switching waveform as shown in Fig.1.

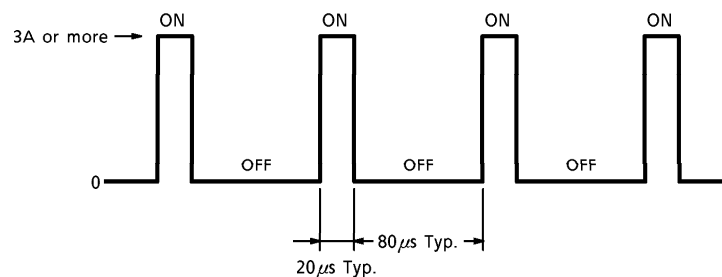


Fig.1 Basic Operation

## ● Detailed explanation

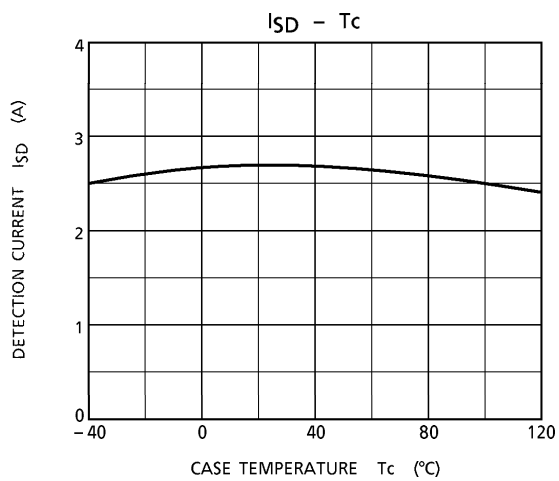
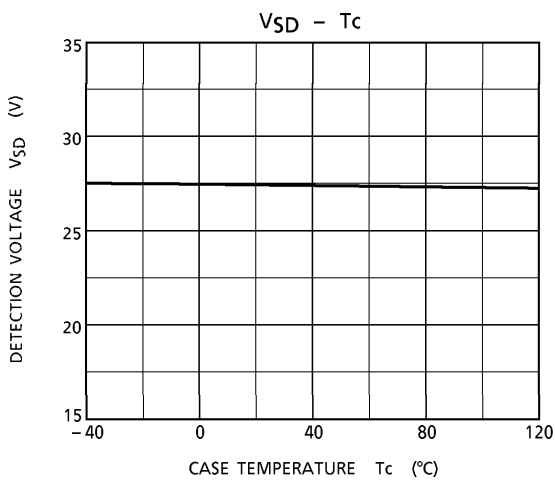
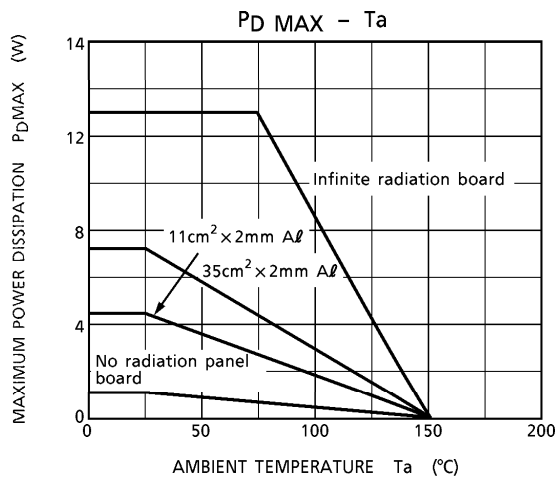
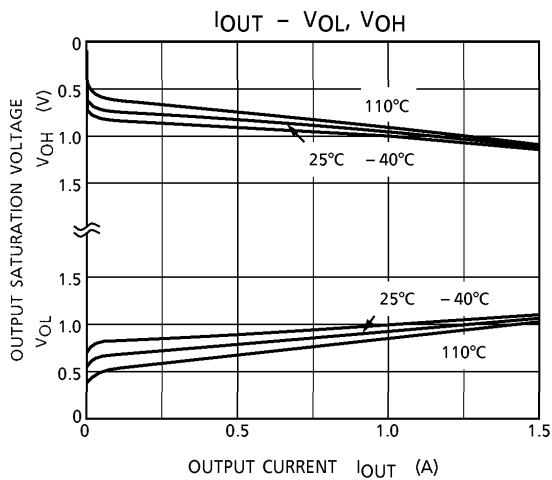
The output current is detected by monitoring the  $V_{BE}$  from each output transistor. One detection circuit connects to one of the output transistors and leads to the short-circuit protection circuit. When a current exceeding the  $I_{SD}$  detection current flows through one of the four output transistors, the short-circuit protection circuit is activated. This circuit contains a timer. When overcurrent condition continues for 20µs (typically), the protection circuit places the output in high-impedance mode and, 80µs (typically) later, returns the IC to ON mode. The switching-waveform output is repeated until overcurrent condition is no longer present.

## MAXIMUM RATINGS (Ta = 25°C)

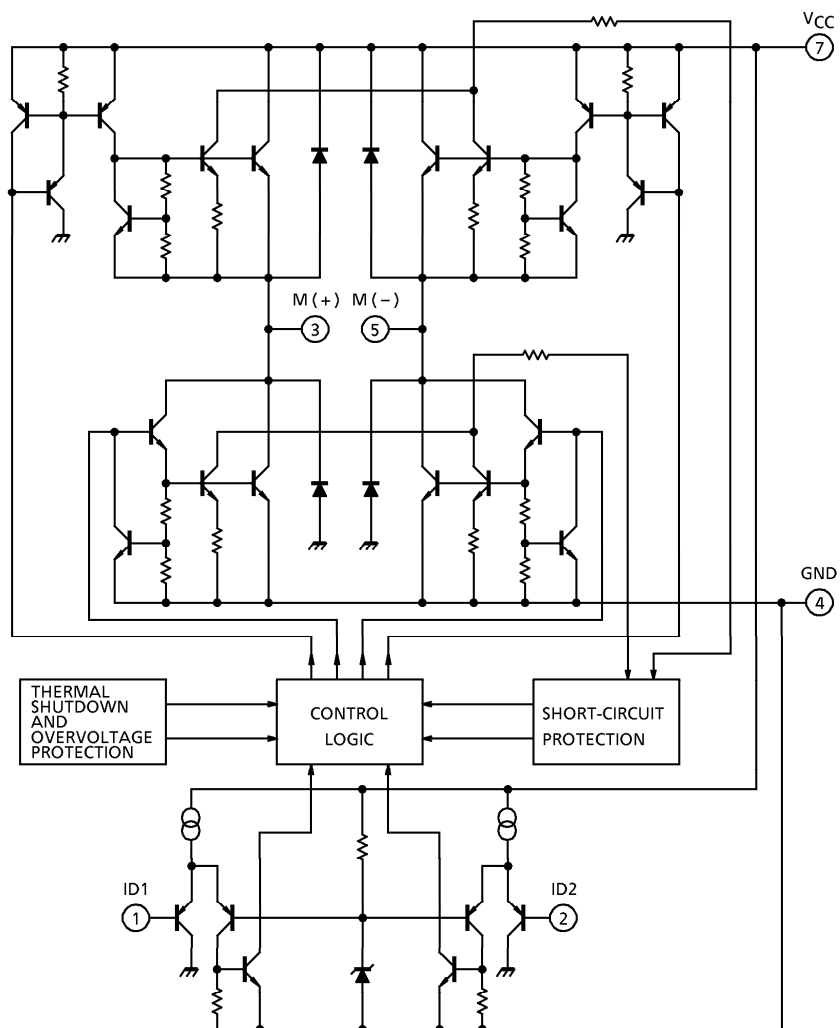
CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V <sub>CC</sub>	30	V
	V <sub>CC</sub>	60 (1s)	
Input Voltage	V <sub>IN</sub>	-0.3 to V <sub>CC</sub>	V
Output Current	I <sub>O·AVE</sub>	1.5	A
Operation Temperature	T <sub>opr</sub>	-40 to 110	°C
Storage Temperature	T <sub>stg</sub>	-55 to 150	°C
Power Dissipation	P <sub>D</sub>	12.5	W
Lead Temperature-time	T <sub>sol</sub>	260 (10s)	°C

ELECTRICAL CHARACTERISTICS (V<sub>CC</sub> = 6 to 16V, T<sub>c</sub> = -40 to 110°C)

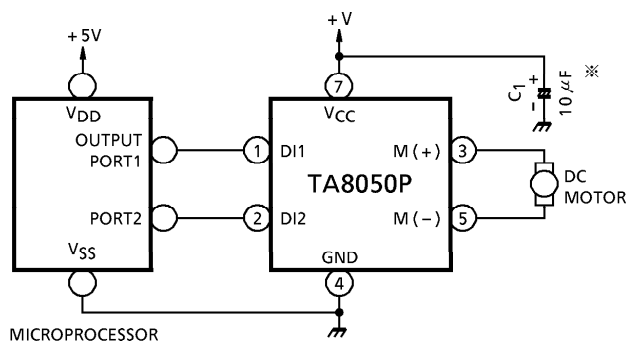
CHARACTERISTIC	SYMBOL	PIN	TEST CIRCUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Current Consumption	I <sub>CC1</sub>	V <sub>CC</sub>	—	Stop	—	8	15	mA
	I <sub>CC2</sub>		—	Forward / Reverse	—	27	50	
	I <sub>CC3</sub>		—	Brake	—	16	30	
Input Voltage	V <sub>IL</sub>	DI1	—	—	—	—	0.8	V
	V <sub>IH</sub>	/ DI2	—	—	2.0	—	—	
Input Current	I <sub>IL</sub>	DI1	—	V <sub>IN</sub> = 0.4V	—	—	-100	μA
	I <sub>IH</sub>	/ DI2	—	V <sub>IN</sub> = V <sub>CC</sub>	—	—	100	
Output Saturation Voltage	V <sub>sat</sub> (total)	M (+)	—	I <sub>O</sub> = 1.5A, T <sub>c</sub> = 25°C	—	2.2	2.9	V
		/ M (-)	—	I <sub>O</sub> = 1.5A, T <sub>c</sub> = 110°C	—	2.2	2.8	
Output Leakage Current	I <sub>LEAK·U</sub>	M (+)	—	V <sub>O</sub> = 0V	—	—	-100	μA
	I <sub>LEAK·L</sub>	/ M (-)	—	V <sub>O</sub> = V <sub>CC</sub>	—	—	100	
Diode Forward Voltage	V <sub>F·U</sub>	M (+)	—	I <sub>F</sub> = 1.5A	—	2.6	—	V
	V <sub>F·L</sub>	/ M (-)	—		—	1.5	—	
Overcurrent Detection	I <sub>SD</sub>	—	—	—	1.8	3	4	A
Shutdown Temperature	T <sub>SD</sub>	—	—	—	—	150	—	°C
Overvoltage Detection	V <sub>SD</sub>	—	—	—	25	27.5	30	V
Thermal Resistance	R <sub>θj-c</sub>	—	—	—	—	4	—	°C/W
Transfer Delay Time	t <sub>pLH</sub>	—	—	—	—	1	10	μs
	t <sub>pHL</sub>	—	—	—	—	1	10	



I/O EQUIVALENT CIRCUIT



EXAMPLE OF APPLICATION CIRCUIT



※ Connect this capacitor as close to the IC as possible.

