

No.2550

N-Channel Enhancement MOS Silicon FET

Analog Switch Applications

Features

Capacitance ON Resistance

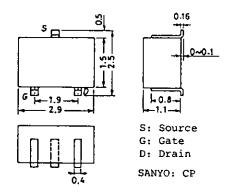
- . Large $|Y_{fs}|$. Enhancement type
- . Small ON resistance

Absolute Maximum Ratings a Drain to Source Voltage Gate to Source Voltage Drain Current Drain Current (Pulse) Allowable Power Dissipat Channel Temperature Storage Temperature	V _{DS} V _{GS} I _D I _{DP}		50 ±12 100 300 200 125 +125	unit V V mA mA oC oC		
Electrical Characteristics Drain to Source Breakdown Voltage		°C I _D =10uA,V _{GS} =0V	min 50	typ	max	unit V
Gate Cutoff Current Cutoff Voltage Drain Current Forward Transfer Admittance	IGS(off)	$V_{GS}=10V, V_{DS}=0V$ $V_{DS}=10V, I_{D}=100 \mu A$ $V_{DS}=20V, V_{GS}=0V$ $V_{DS}=10V, I_{D}=50 \mu A, f=1 kHz$	0.3 25	0.01 0.9 40	10 1.5 1	nΑ V μΑ mS
Output Capacitance	c _{iss} c _{oss} c _{rss}	$V_{DS}=10V, V_{GS}=0, f=1MHz$ $V_{DS}=10V, V_{GS}=0, f=1MHz$ $V_{DS}=10V, V_{GS}=0, f=1MHz$		15 6 0.5		pF pF pF

 $V_{GS} = 10V, I_D = 10mA$

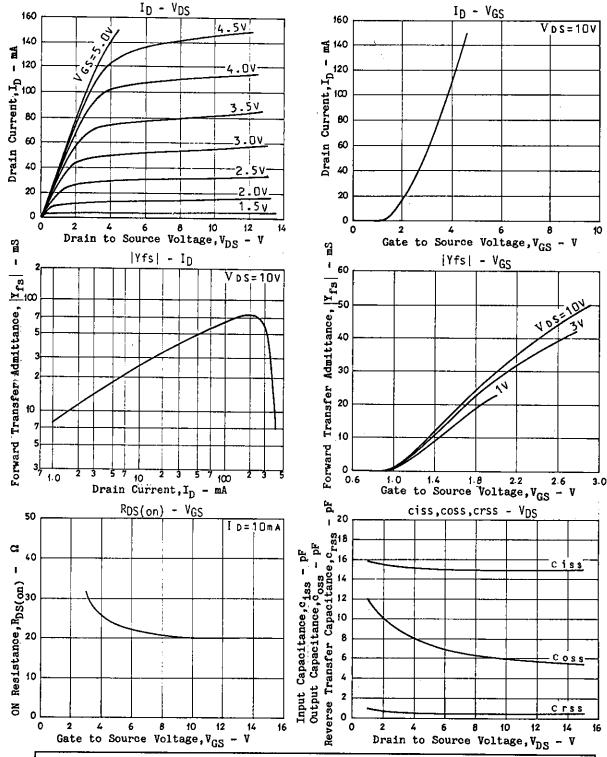
R_{DS}(on)

Package Dimensions 2024A (unit:mm)



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