

No.2563C

2SK669

N-Channel Enhancement MOS Silicon FET

Very High-Speed Switch, Analog Switch Applications

Applications

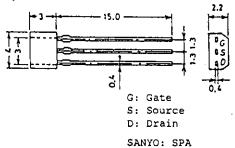
· Analog switches, low-pass filters, very high-speed switches

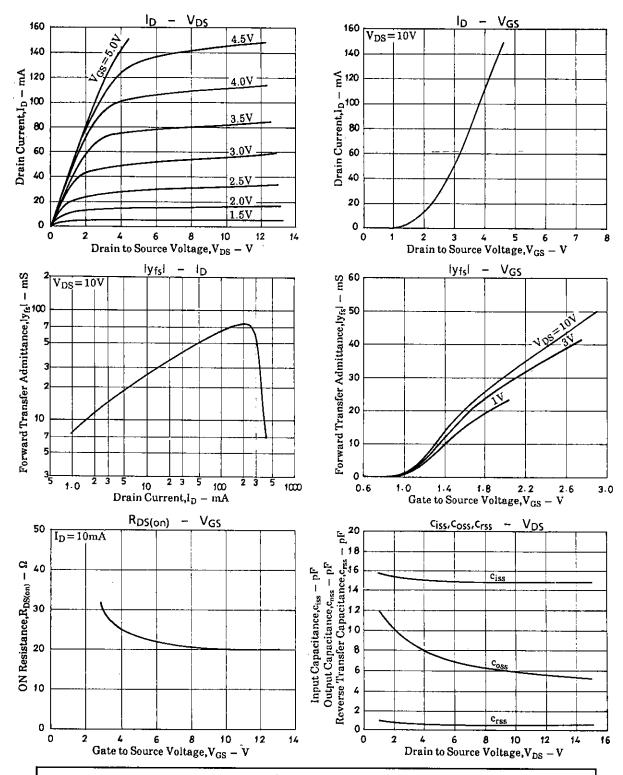
Features

- · Large lyfs
- · Enhancement type
- · Small ON resistance

Absolute Maximum Ratings at Ta = 25°C					unit	
Drain to Source Voltage	$ m V_{DS}$			50	V	
Gate to Source Voltage	V_{GS}	•		±12	V	
Drain Current	$I_{\mathbf{D}}$			100	mΑ	
Drain Current (Pulse)	I_{DP}			300	mΑ	
Allowable Power Dissipation	P_{D}			200	mW	
Channel Temperature	Tch			125	°C	
Storage Temperature	Tstg		-55 to +	125	°C	
Electrical Characteristics at Ta = 25°C			min	typ	max	unit
Drain to Source	$V_{(BR)DS}$	$I_D = 10 \mu A, V_{GS} = 0$	50			V
Breakdown Voltage	,,_ _					
Gate Cutoff Current	I_{GSS}	$V_{GS} = 10V, V_{DS} = 0$		0.01	10	nΑ
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = 10V, I_{D} = 100 \mu A$	0.3	0.9	1.5	V
Drain Current	I_{DSS}	$V_{DS} = 20V, V_{GS} = 0V$			1.0	μA
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = 10V, I_D = 50mA, f = 1kHz$	z 25	40		$\overline{\text{mS}}$
Input Capacitance	c_{iss}	$V_{DS} = 10V, V_{GS} = 0, f = 1MHz$		15		pF
Output Capacitance	c_{oss}	$V_{DS} = 10V, V_{GS} = 0, f = 1MHz$		6		рF
Reverse Transfer Capacitance	c_{rss}	$V_{DS} = 10V, V_{GS} = 0, f = 1MHz$		0.5		pF
ON Resistance	$R_{DS(on)}$	$V_{DS} = 10V, I_D = 10mA$		20		Ω

Package Dimensions 2040 (unit: mm)





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