

# MOS FIELD EFFECT TRANSISTOR $\mu PA602T$

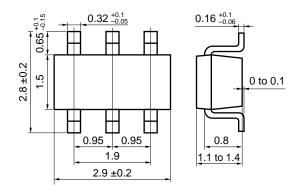
# N-CHANNEL MOS FET (6-PIN 2 CIRCUITS)

The  $\mu$ PA602T is a mini-mold device provided with two MOS FET circuits. It achieves high-density mounting and saves mounting costs.

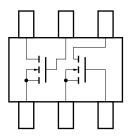
### **FEATURES**

- Two MOS FET circuits in package the same size as SC-59
- Complement to μPA603T
- · Automatic mounting supported

## **PACKAGE DIMENSIONS (in millimeters)**



### PIN CONNECTION (Top view)



### ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain to Source Voltage	VDSS	50	V	
Gate to Source Voltage	Vgss	±20	V	
Drain Current (DC)	I <sub>D(DC)</sub>	100	mA	
Drain Current (pulse)	I <sub>D(pulse)</sub> *	200	mA	
Total Power Dissipation	Рт	300 (Total)	mW	
Channel Temperature	Tch	150	°C	
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C	

<sup>\*</sup> PW  $\leq$  10 ms, Duty Cycle  $\leq$  50 %

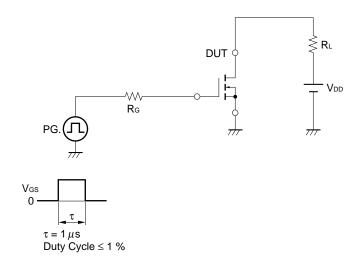


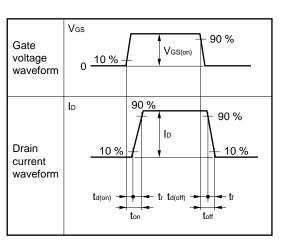
# ELECTRICAL CHARACTERISTICS (TA = 25 °C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-off Current	Ipss	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0			1.0	μΑ
Gate Leakage Current	Igss	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$			±1.0	μΑ
Gate Cut-off Voltage	V <sub>GS(off)</sub>	$V_{DS} = 5.0 \text{ V}, \text{ ID} = 1.0 \ \mu\text{A}$	0.8	1.4	1.8	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> = 5.0 V, I <sub>D</sub> = 10 mA	20			mS
Drain to Source On-State Resistance	RDS(on)1	V <sub>G</sub> S = 4.0 V, I <sub>D</sub> = 10 mA		19	30	Ω
Drain to Source On-State Resistance	RDS(on)2	Vgs = 10 V, Ip = 10 mA		15	25	Ω
Input Capacitance	Ciss	V <sub>DS</sub> = 5.0 V, V <sub>GS</sub> = 0, f = 1.0 MHz		16		pF
Output Capacitance	Coss			12		pF
Reverse Transfer Capacitance	Crss			3		pF
Turn-On Delay Time	td(on)	$V_{GS(on)} = 5.0 \text{ V, } R_G = 10 \Omega, \text{ V}_{DD} = 5.0 \text{ V,}$ $I_D = 10 \text{ mA, } R_L = 500 \Omega$		17		ns
Rise Time	tr			10		ns
Turn-Off Delay Time	td(off)			68		ns
Fall Time	<b>t</b> f			38		ns

Marking: IA

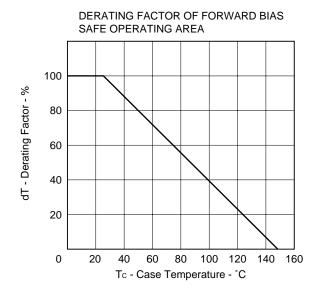
# SWITCHING TIME MEASUREMENT CIRCUIT AND CONDITIONS (RESISTANCE LOADED)

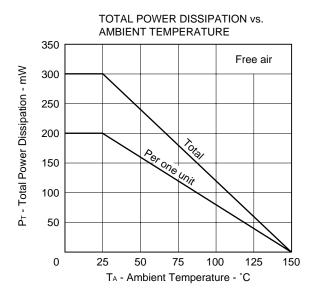


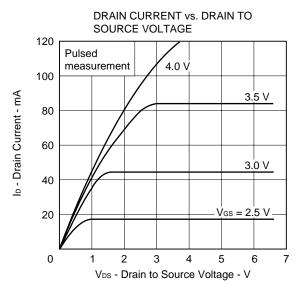


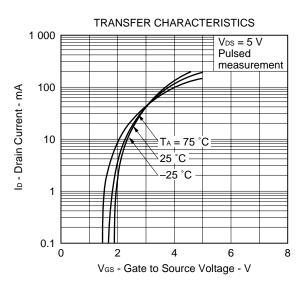


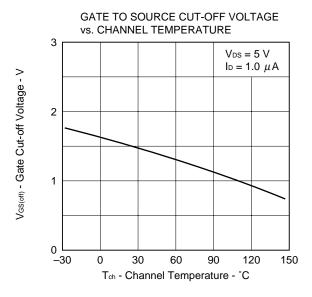
### TYPICAL CHARACTERISTICS (TA = 25 °C)

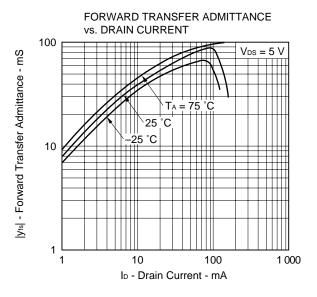




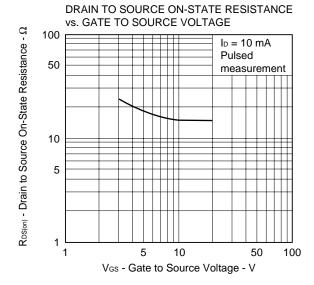


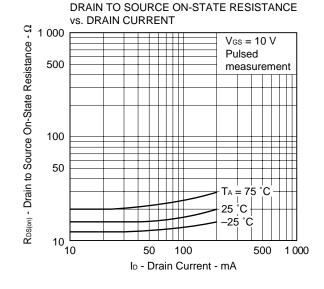


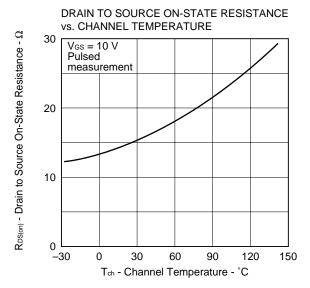


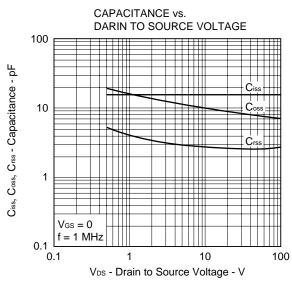


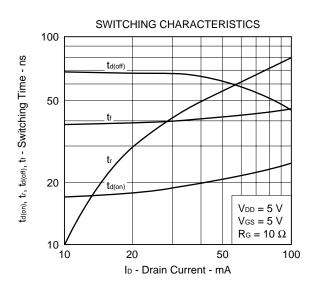


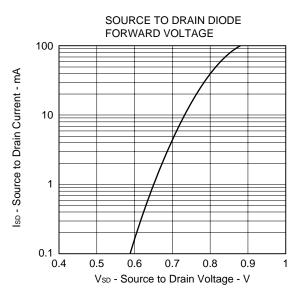














# REFERENCE

Document Name	Document No.	
NEC semiconductor device reliability/quality control system	TEI-1202	
Quality grade on NEC semiconductor devices	IEI-1209	
Semiconductor device mounting technology manual	C10535E	
Guide to quality assurance for semiconductor devices	MEI-1202	
Semiconductor selection guide	X10679E	

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Anti-radioactive design is not implemented in this product.

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