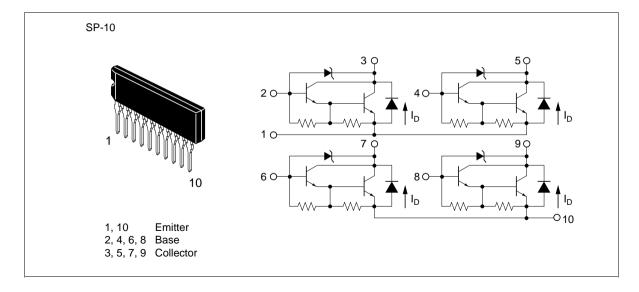
### Silicon NPN Triple Diffused

# HITACHI

#### Application

Low frequency power amplifier

#### Outline





#### **Absolute Maximum Ratings** (for each device, $Ta = 25^{\circ}C$ )

Item	Symbol	Ratings	Unit V	
Collector to base voltage	V <sub>CBO</sub>	150		
Collector to emitter voltage	V <sub>CEO</sub>	150	V	
Emitter to base voltage	V <sub>EBO</sub>	7	V	
Collector current	Ι <sub>c</sub>	5	А	
Collector peak current	I <sub>C(peak)</sub>	10	А	
Diode current	I <sub>D</sub>	5	А	
Collector power dissipation	P <sub>c</sub> * <sup>1</sup>	4	W	
	$P_{c}^{*1} (T_{c} = 25^{\circ}C)$	28		
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

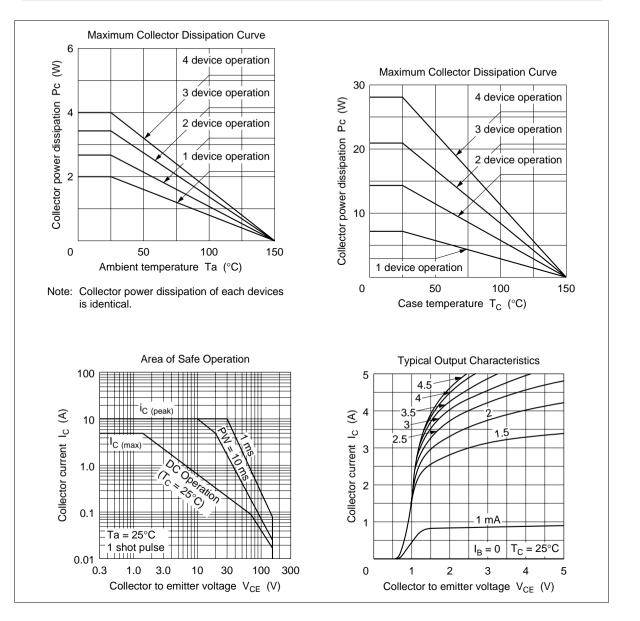
Note: 1. 4 devices operation.

#### **Electrical Characteristics** (for each device, $Ta = 25^{\circ}C$ )

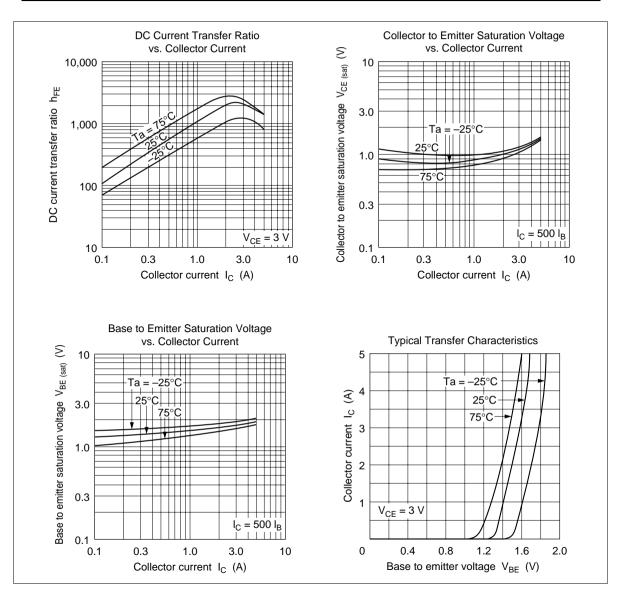
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to emitter breakdown voltage	$V_{\rm (BR)CBO}$	150	_	_	V	$I_{c} = 0.1 \text{ mA}, I_{E} = 0$
Collector to emitter sustain voltage	$V_{\text{CEO}(\text{SUS})}$	150	_	_	V	$I_{c}$ = 0.2 A, L = 20 mHz, $R_{BE}$ = $\infty$
Emitter to base breakdown voltage	$V_{(BR)EBO}$	7	—	—	V	$I_{\rm E} = 50  {\rm mA},  I_{\rm C} = 0$
Collector cutoff current	I <sub>CBO</sub>	_	_	10	μΑ	$V_{CB} = 120 \text{ V}, \text{ I}_{E} = 0$
	I <sub>CEO</sub>	—	—	10	_	$V_{ce}$ = 120 V, $R_{be}$ = $\infty$
DC current transfer ratio	$\mathbf{h}_{\text{FE}}$	1000	—	20000		$V_{ce} = 3 \text{ V}, \text{ I}_{c} = 3 \text{ A}^{*1}$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	_	_	1.5	V	$I_{\rm c} = 3 \text{ A}, I_{\rm B} = 6 \text{ mA}^{*1}$
Base to emitter saturation voltage	$V_{\text{BE(sat)}}$	_	—	2.0	V	$I_{\rm c} = 3 \text{ A}, I_{\rm B} = 6 \text{ mA}^{*1}$
C to E diode forward current	V <sub>D</sub>	_	_	3.5	V	I <sub>D</sub> = 5 A
Noto: 1 Dulas test						

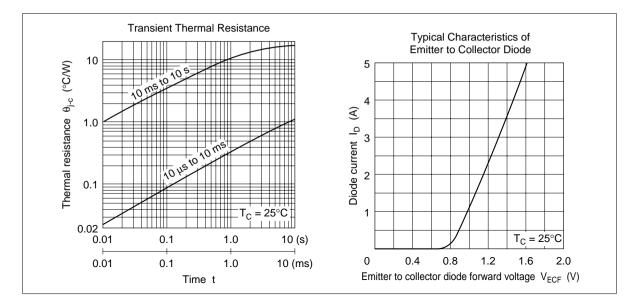
Note: 1. Pulse test.

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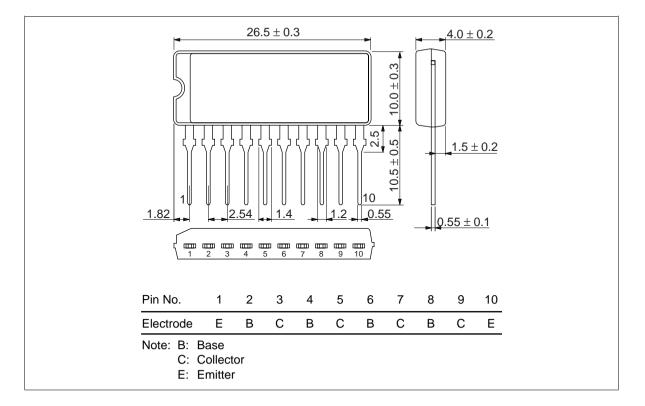


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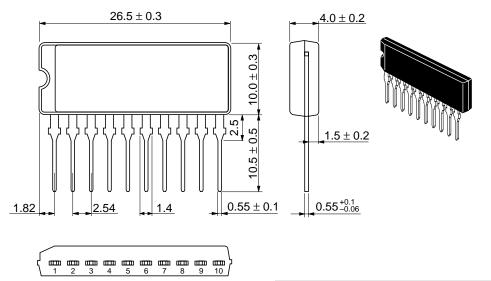




Unit: mm



#### Unit: mm



Hitachi Code	SP-10
JEDEC	—
EIAJ	—
Weight (reference value)	2.9 g

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