

SILICON TRANSISTOR 2SC3809

NPN SILICON EPITAXIAL TRANSISTOR FOR MICROWAVE AMPLIFIERS AND ULTRA HIGH SPEED SWITCHINGS INDUSTRIAL USE

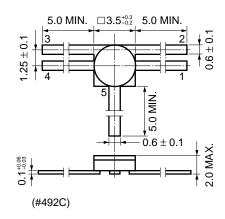
FEATURES

- The 2SC3809 is an NPN silicon epitaxial dual transistor having a large-gain-bandwidth product performance in a wide operating current range.
- Dual chips in one package can achieve high performance for differential amplifiers and current mode logic (CML) circuits.

ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

PARAMETER	SYMBOL	RATINGS	UNIT	
Collector to Base Voltage	Vсво	20	٧	
Collector to Emitter Voltage	Vceo	12	٧	
Emitter to Base Voltage	VEBO	3	٧	
Collector Current	Ic	100/unit	mA	
Total Power Dissipation	Рт	300/unit	mW	
Thermal Resistance (junction to case)	Rth (j-c)	90/unit	°C/W	
Junction Temperature	Tj	200	°C	
Storage Temperature	T _{stg}	-65 to +200	°C	

PACKAGE DIMENSIONS (in millimeters)



PIN CONNECTIONS $C_1 \stackrel{3}{\circ} C_2$ $C_1 \stackrel{2}{\circ} C_2$ $G_1 \stackrel{3}{\circ} C_2$ $G_2 \stackrel{4}{\circ} G_2$ $G_3 \stackrel{2}{\circ} C_2$ $G_4 \stackrel{3}{\circ} C_2$ $G_5 \stackrel{5}{\circ} C_2$

ELECTRICAL CHARACTERISTICS (TA = 25 °C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Collector to Base Breakdown Voltage	ВУсво	Ic = 100 μA	20			V
Emitter to Base Breakdown Voltage	BVEBO	$I_E = 100 \ \mu A, \ I_C = 0$	3			٧
Collector to Emitter Breakdown Voltage	BVcEo	Ic = 1 mA, R _{BE} = ∞	12			V
Collector Cut-off Current	Ісво	Vcb = 10 V, Ic = 0			1.0	μΑ
Emitter Cut-off Current	ІЕВО	VEB = 1 V, IE = 0			1.0	μΑ
DC Current Gain	hfe	VcE = 10 V, Ic = 20 mA	50		250	
h _{FE} Ratio	hFE1/hFE2 Note 1	Vce = 10 V, Ic = 20 mA	0.6		1.0	
Difference of Base to Emitter Voltage	VBE (on)	Vce = 10 V, Ic = 20 mA			30	mV
Gain Bandwidth Product	f⊤ Note 2	Vce = 10 V, Ic = 20 mA	6	7		GHz
Feedback Capacitance	Cre Note 3	Vcb = 10 V, IE = 0, f = 1.0 MHz		0.75	1.0	pF

Notes 1. hfe1 is the smaller hfe value of the 2 transistors.

- 2. Measured using a single-type device (equivalent to the 2SC3603).
- **3.** Measured with a 3-terminal bridge, terminals other than the collector and base of the device under test should be connected to the guard terminal of the bridge.

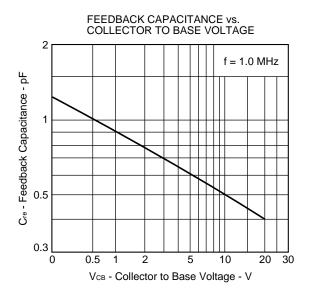


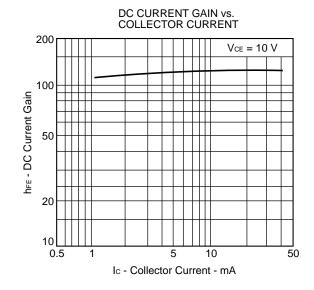
REGARDING CLEANSING

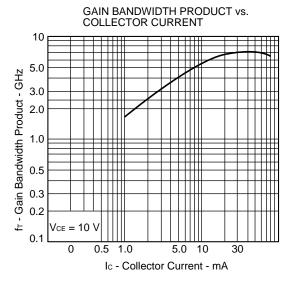
Cleanse the flux after soldering. Particularly, cleanse the bottom surface of the transistor so that flux does not remain. If any flux remains on the bottom surface, it may absorb moisture, resulting in short circuit among pins due to metal-migration at the metalized area of the transistor. You can use **alcohol** as a solvent.

Do not apply ultra-sonic-cleaning on this product.

TYPICAL CHARACTERISTICS (TA = 25 °C)







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[MEMO]

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Anti-radioactive design is not implemented in this product.

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