



**Weltrend Semiconductor, Inc.**

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**WT6802**

**Monitor OSD with Auto-calibration**

**Data Sheet**

**REV. 1.0**

**April, 9, 2001**

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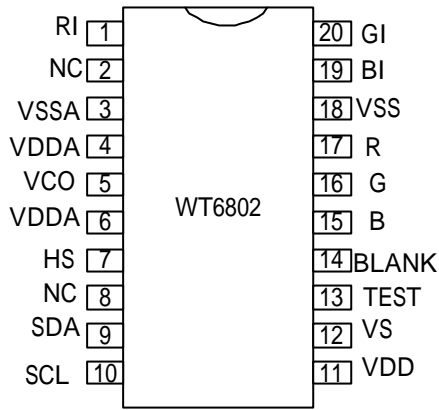




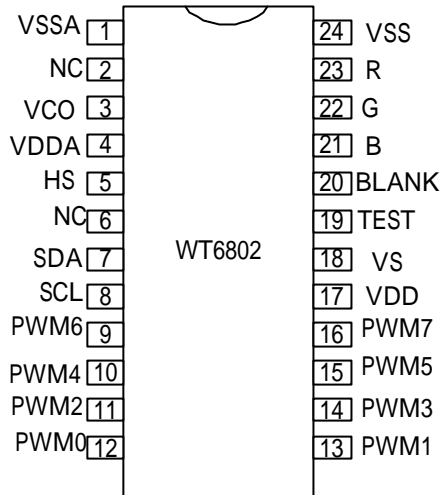
# WT6802

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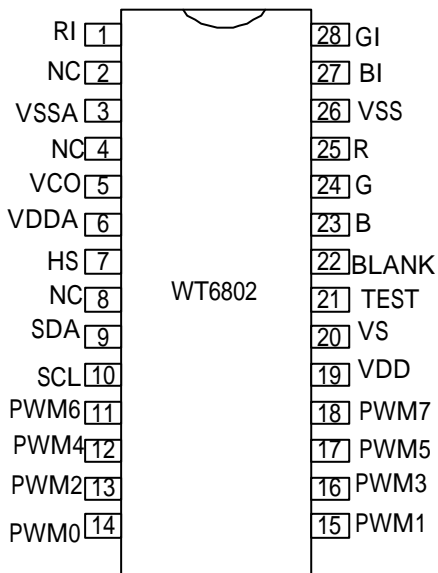
### PIN CONFIGURATION



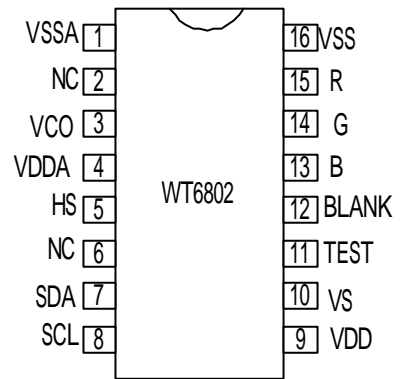
WT6802 (DIP-20)



WT6802 (DIP-24, 300mil)



WT6802 (DIP-28, 300mil)



WT6802 (DIP-16)



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**PIN DESCRIPTION**

PIN No				Symbol	Pin Name	Input/ output	Description
28 pin	24 pin	20 pin	16 pin				
1	--	1	--	RI	Red Input	input	Video Red input from Pre-AMP
2	--	2	--	NC			
3	1	3	1	VSSA	Analog ground	---	
4	2	4	2	NC	No connection	---	
5	3	5	3	VCO	VCO	---	Connected to external loop filter circuit
6	4	6	4	VDDA	Analog power	---	+5 V
7	5	7	5	HS	Horizontal SYNC	Input	Horizontal synchronous input signal
8	6	8	6	NC	No connection	---	
9	7	9	7	SDA	Serial data	Input/output	I <sup>2</sup> C data
10	8	10	8	SCL	Serial clock	input	I <sup>2</sup> C clock
11~18	9~16	--	--	PWM0~ PWM7	PWM DACs	output	8 channels of 8-bit PWM DAC. Refer to pin assignment in page1 for each PWM DAC's pin assignment.
19	17	11	9	VDD	Digital power	---	+5 V
20	18	12	10	VS	Vertical SYNC	input	Vertical synchronous input signal
21	19	13	11	TEST	TEST	output	Output test only
22	20	14	12	BLANK	Blank	output	Blank signal output



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23	21	15	13	B	Blue output	output	Blue signal output
24	22	16	14	G	Green output	output	Green signal output
25	23	17	15	R	Red output	output	Red signal output
26	24	18	16	VSS	Digital Ground		
27	--	19	--	BI	Blue input	input	Video Blue input from Pre-AMP
28	--	20	--	GI	Green input	input	Video Green input from Pre-AMP