# VCXO-7050/7050L

## Miniature VCXO Geared to Broadband

#### **■**Features

- "Small Size" as small as 0.063cc. (5×7×1.8mm)
- Using 1 chip HCMOS IC.
- 3-state devices are available.
- APR (Absolute Pull Range) of up to ±50ppm or ±100ppm\*2.

## **■**Typical Applications

- Digital switching system
- ATM SDH
- SONET
- XDSL

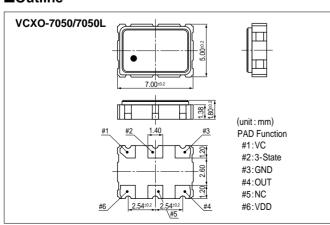


## **■**Specifications

Туре	VCXO-7050	VCXO-7050L	
Frequency range	12.2880MHz to	12.2880MHz to 80.0000MHz *1	
APR (Absolute Pull Range)	±50×10 <sup>-6</sup> or ±100×10 <sup>-6</sup> *2 (Vc=0.5~4.5V)	$\pm 50 \times 10^{-6}$ or $\pm 100 \times 10^{-6}$ *2 (Vc=0.3~3.0V)	
Operable temperature range	-40°C to +85°C		
Storage temperature	-40°C to +90°C		
Aging	APR is tolerable with 20years aging		
Power supply voltage	V <sub>DD</sub> =+5.0 V±5%	V <sub>DD</sub> =+3.3V±5%	
Power supply current	15mA max.		
Output load	15pF±10%		
Output level	Voн: +0.9Vcc min. /	Voh: +0.9Vcc min. / Vol: +0.1Vcc max.	
Output symmetry	40% to 60% (at +2.50V DC)	40% to 60% (at +1.65V DC)	
Rise time/Fall time	10ns max.	10ns max. / 10ns max.	
Vibration	MIL-STD-883 Method 2007		
	IEC68-2-6 test Fc		
	MIL-STD-202-Method 204		
Shock	MIL-STD-883 Method 2002		
	IEC68-2-27 test Ea		
	MIL-STD-202-Method 213		
Humidity	EIA / JES	EIA / JESD22-A101	
Weight	0.17gram		

- APR=Absolute Pull Range is the minimum guaranteed (voltage contorolled) frequency shift (△f/f NOM) over all conditions (temperature, aging, power supply and load)
  133MHz to 80MHz are underdevelopment.
- \*2 For 100×10<sup>-6</sup>, ask our sales representative

## **■**Outline



## **■**Land Pattern(reference)

