

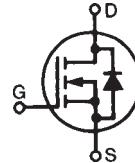
PolarHT™ Power MOSFET

**IXTQ 82N25P
IXTT 82N25P
IXTK 82N25P**

**V_{DSS} = 250 V
 I_{D25} = 82 A
 $R_{DS(on)}$ = 35 mΩ**

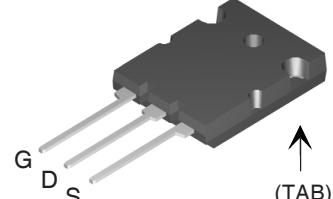
N-Channel Enhancement Mode

Preliminary Data Sheet

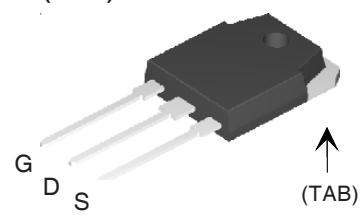


Symbol	Test Conditions	Maximum Ratings		
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	250		V
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C ; $R_{GS} = 1 \text{ M}\Omega$	250		V
V_{GSM}		± 20		V
I_{D25}	$T_c = 25^\circ\text{C}$	82	A	
$I_{D(\text{RMS})}$	External lead current limit	75	A	
I_{DM}	$T_c = 25^\circ\text{C}$, pulse width limited by T_{JM}	250	A	
I_{AR}	$T_c = 25^\circ\text{C}$	60	A	
E_{AR}	$T_c = 25^\circ\text{C}$	40	mJ	
E_{AS}	$T_c = 25^\circ\text{C}$	1.0	J	
dv/dt	$I_s \leq I_{DM}$, $di/dt \leq 100 \text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$, $R_G = 4 \Omega$	10	V/ns	
P_D	$T_c = 25^\circ\text{C}$	500		W
T_J		-55 ... +150		$^\circ\text{C}$
T_{JM}		150		$^\circ\text{C}$
T_{stg}		-55 ... +150		$^\circ\text{C}$
T_L	1.6 mm (0.062 in.) from case for 10 s	300		$^\circ\text{C}$
M_d	Mounting torque	1.13/10	Nm/lb.in.	
Weight	TO-3P	5.5		g
	TO-264	10		g
	TO-268	5.0		g

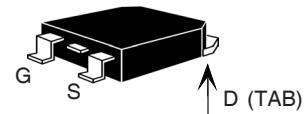
TO-264 (IXTK)



TO-3P (IXTQ)



TO-268 (IXTT)



G = Gate
S = Source

D = Drain
TAB = Drain

Features

- International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
 - easy to drive and to protect

Advantages

- Easy to mount
- Space savings
- High power density

PolarHT™ DMOS transistors utilize proprietary designs and process. US patent is pending.

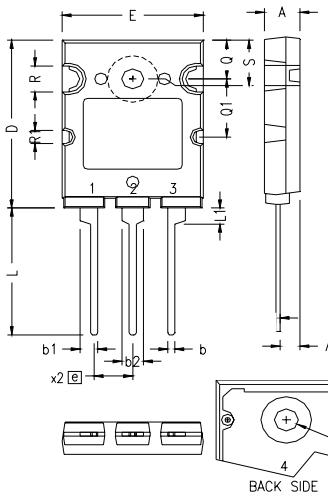
Symbol	Test Conditions	Characteristic Values		
	($T_J = 25^\circ\text{C}$, unless otherwise specified)	Min.	Typ.	Max.
V_{DSS}	$V_{GS} = 0 \text{ V}$, $I_D = 250 \mu\text{A}$	250		V
$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$	2.5		5.0 V
I_{GSS}	$V_{GS} = \pm 20 \text{ V}_{DC}$, $V_{DS} = 0$		± 100	nA
I_{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V}$		25 250	μA
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}$, $I_D = 0.5 I_{D25}$ Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2 \%$		35	$\text{m}\Omega$

Symbol **Test Conditions**
Characteristic Values
 $(T_J = 25^\circ\text{C}$, unless otherwise specified)

		Min.	Typ.	Max.
g_{fs}	$V_{DS} = 10 \text{ V}; I_D = 0.5 I_{D25}$, pulse test	30	52	S
C_{iss} C_{oss} C_{rss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	4800	pF	
		900	pF	
		210	pF	
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = 60 \text{ A}$ $R_G = 4 \Omega$ (External)	29	ns	
		20	ns	
		78	ns	
		22	ns	
$Q_{g(on)}$ Q_{gs} Q_{gd}	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}$	142	nC	
		32	nC	
		74	nC	
R_{thJC}			0.25 K/W	
R_{thCH}	TO-3P	0.21	K/W	
	TO-264	0.15	K/W	

Source-Drain Diode
Characteristic Values
 $(T_J = 25^\circ\text{C}$, unless otherwise specified)

		Min.	typ.	Max.
I_s	$V_{GS} = 0 \text{ V}$			82 A
I_{SM}	Repetitive			250 A
V_{SD}	$I_F = I_s, V_{GS} = 0 \text{ V}$, Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2 \%$			1.5 V
t_{rr} Q_{RM}	$I_F = 25 \text{ A}$ $-di/dt = 100 \text{ A}/\mu\text{s}$ $V_R = 100 \text{ V}$	200	ns	
			2.0	μC

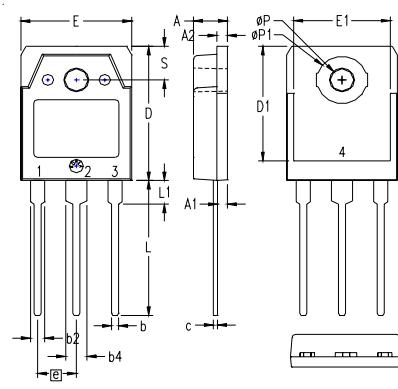
TO-264 AA Outline


SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.185	.209	4.70	5.30
A1	.102	.118	2.60	3.00
b	.035	.049	0.90	1.25
b1	.091	.106	2.30	2.70
b2	.110	.126	2.80	3.20
c	.020	.033	0.50	0.85
D	1.012	1.035	25.70	26.30
E	.776	.799	19.70	20.30
e	.215BSC		5.46 BSC	
L	.768	.807	19.50	20.50
L1	.091	.106	2.30	2.70
$\emptyset P$.122	.138	3.10	3.50
Q	.228	.244	5.80	6.20
Q1	.346	.362	8.80	9.20
$\emptyset R$.150	.165	3.80	4.20
$\emptyset R1$.071	.087	1.80	2.20
S	.228	.244	5.80	6.20

IXYS reserves the right to change limits, test conditions, and dimensions.

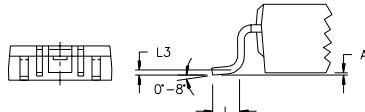
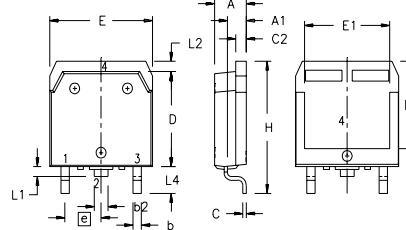
IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:

 4,850,072 4,931,844 5,034,796 5,063,307 5,237,481 5,381,025 6,404,065B1 6,162,665 6,534,343 6,583,505
 4,835,592 4,881,106 5,017,508 5,049,961 5,187,117 5,486,715 6,306,728B1 6,259,123B1 6,306,728B1 6,683,344

TO-3P Outline

 1 - GATE
 2 - DRAIN (COLLECTOR)
 3 - SOURCE (EMITTER)
 4 - DRAIN (COLLECTOR)

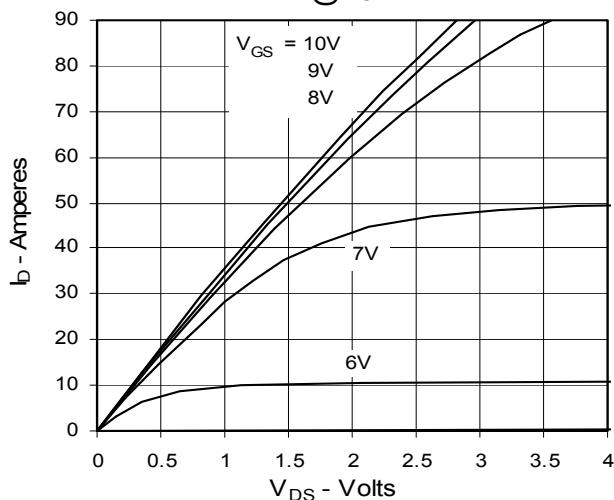
SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.185	.193	4.70	4.90
A1	.051	.059	1.30	1.50
A2	.057	.065	1.45	1.65
b	.035	.045	0.90	1.15
b2	.075	.087	1.90	2.20
b4	.114	.126	2.90	3.20
c	.022	.031	0.55	0.80
D	.780	.791	19.80	20.10
D1	.665	.677	16.90	17.20
E	.610	.622	15.50	15.80
E1	.531	.539	13.50	13.70
e	.215 BSC		5.45 BSC	
L	.779	.795	19.80	20.20
L1	.134	.142	3.40	3.60
$\emptyset P$.126	.134	3.20	3.40
$\emptyset P1$.272	.280	6.90	7.10
S	.193	.201	4.90	5.10

All metal areas are tin plated.

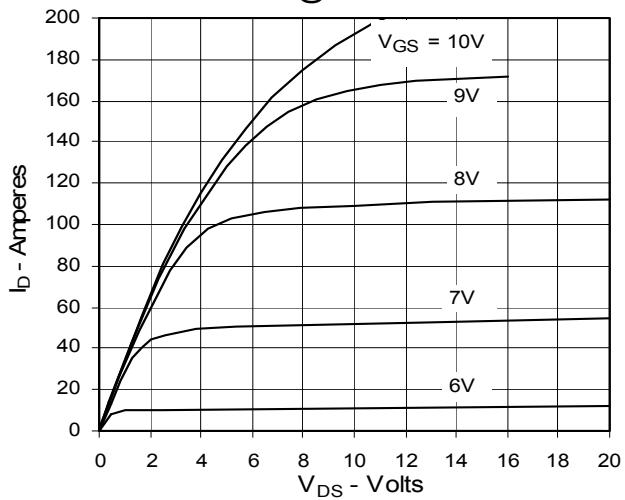
TO-268 Outline


SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.193	.201	4.90	5.10
A1	.106	.114	2.70	2.90
A2	.001	.010	0.02	0.25
b	.045	.057	1.15	1.45
b2	.075	.083	1.90	2.10
C	.016	.026	0.40	0.65
C2	.057	.063	1.45	1.60
D	.543	.551	13.80	14.00
D1	.488	.500	12.40	12.70
E	.624	.632	15.85	16.05
E1	.524	.535	13.30	13.60
e	.215 BSC		5.45 BSC	
H	.736	.752	18.70	19.10
L	.094	.106	2.40	2.70
L1	.047	.055	1.20	1.40
L2	.039	.045	1.00	1.15
L3	.010 BSC		0.25 BSC	
L4	.150	.161	3.80	4.10

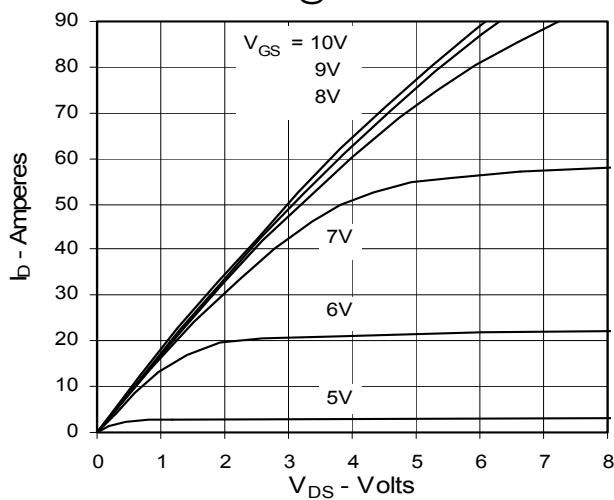
**Fig. 1. Output Characteristics
@ 25°C**



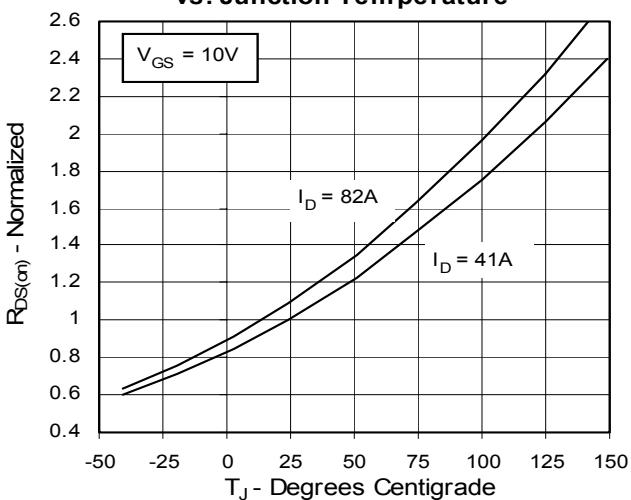
**Fig. 2. Extended Output Characteristics
@ 25°C**



**Fig. 3. Output Characteristics
@ 125°C**



**Fig. 4. $R_{DS(on)}$ Normalized to I_{D25} Value
vs. Junction Temperature**



**Fig. 5. $R_{DS(on)}$ Normalized to
 I_{D25} Value vs. I_D**

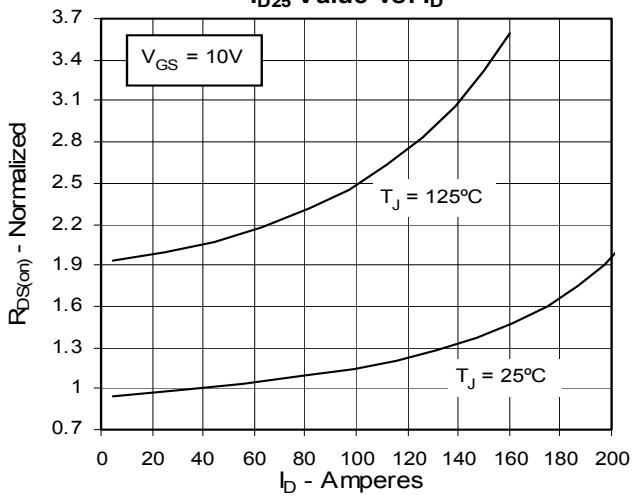


Fig. 6. Drain Current vs. Case Temperature

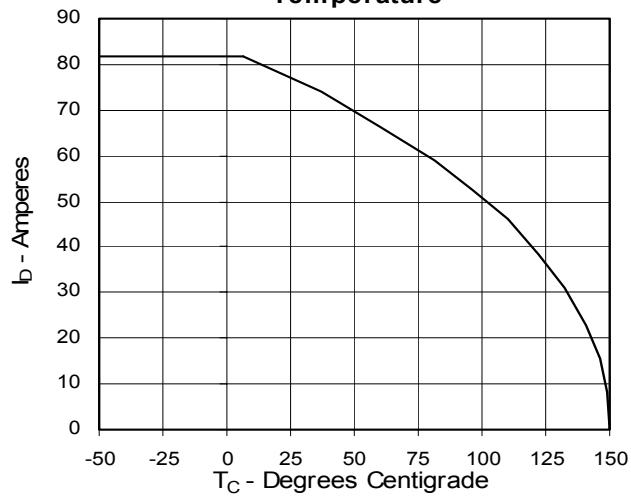
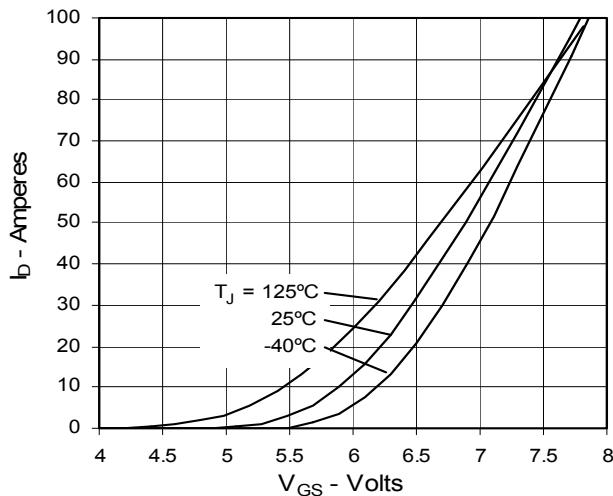
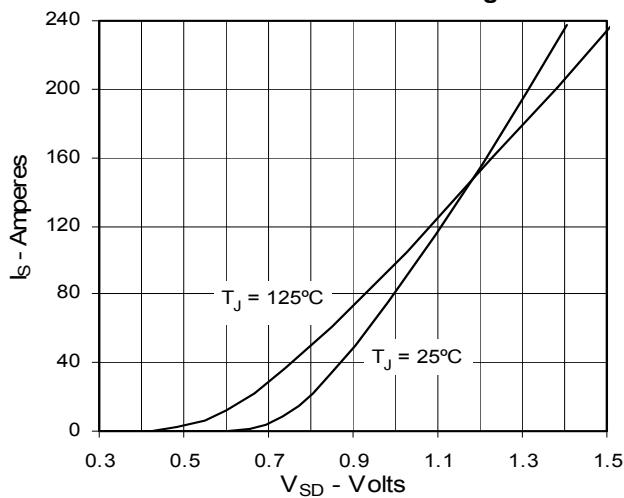
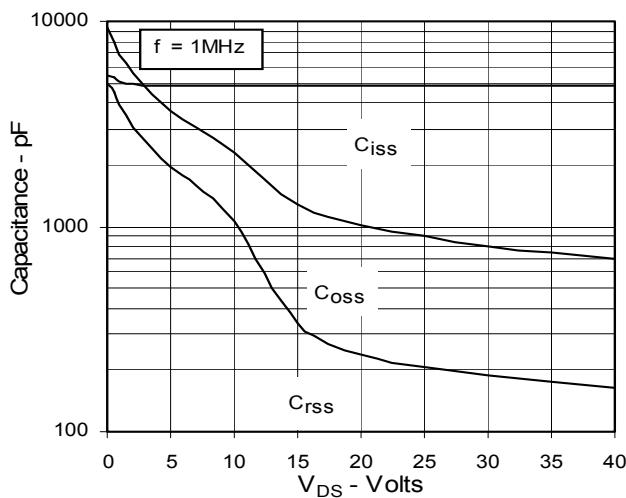
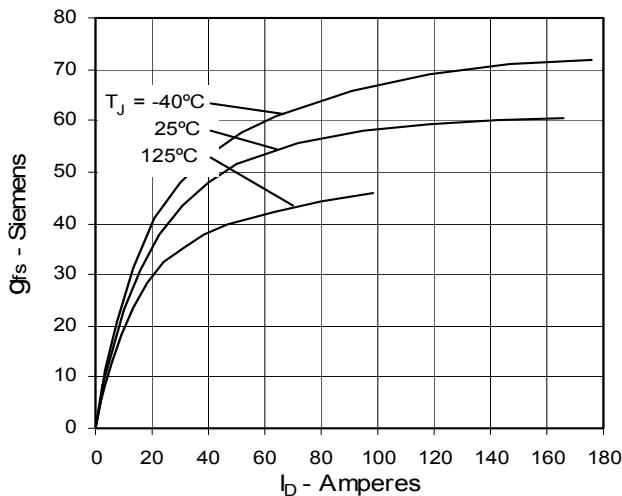
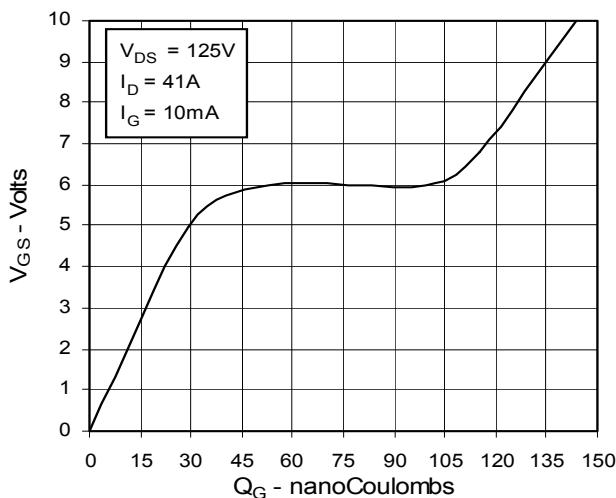
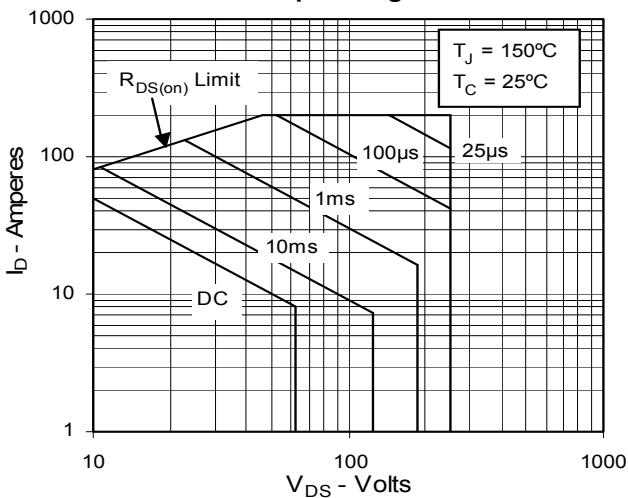


Fig. 7. Input Admittance

**Fig. 9. Source Current vs.
Source-To-Drain Voltage**

Fig. 11. Capacitance

Fig. 8. Transconductance

Fig. 10. Gate Charge

**Fig. 12. Forward-Bias
Safe Operating Area**


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Fig. 13. Maximum Transient Thermal Resistance