TOSHIBA TD6127BP

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

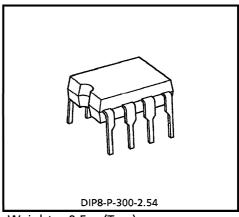
TD6127BP

ECL PRESCALLER FOR COMMUNICATIONS RADIO

TD6127BP is a 2 modulus prescaller developed for communications radio of PLL frequency synthesizer type. This is suitable for mobile radio telephone and personal communications radio etc.

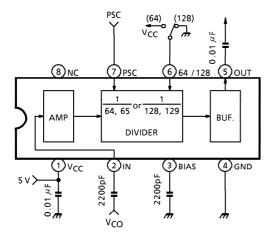
FEATURES

- Maximum operating frequency is 1 GHz.
- 2 modulus prescaller : N = 64/65 or N = 128/129
- Input voltage sensitivity is 50 mV_{rms}.
- The package is DIP 8 pins.

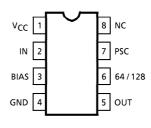


Weight: 0.5 g (Typ.)

BLOCK DIAGRAM



PIN CONNECTION (TOP VIEW)



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PIN FUNCTION

PIN No.	SYMBOL	FUNCTION	REMARKS		
1	Vcc	Power supply terminal	_		
2	IN	Input terminal of local oscillator	_		
3	BIAS	Bias capacitance terminal	_		
4	GND	Earth terminal	_		
5	OUT	Output terminal	_		
6	64 / 128	Dividing mode selection terminal "H" level : 64, 65 "L" level : 128, 129	_		
7	PSC	2 modulus control terminal "H" level : N "L" level : N + 1	_		
8	NC	Not connected	_		

MAXIMUM RATINGS (Ta = 25°C)

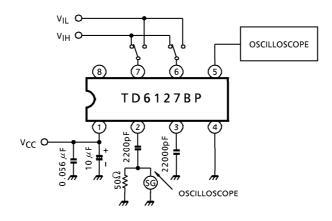
CHARACTERISTIC	SYMBOL	RATING	UNIT
Power Supply Voltage	VCC	6.5	V
Power Dissipation	PD	450	mW
Input Voltage	V _{in}	-0.3~V _{CC} + 0.3	V
Operating Temperature	T _{opr}	T _{opr} - 30~85	
Storage Temperature	T _{stq}	- 55~150	°C

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, $V_{CC} = 4.5 \sim 5.5 \text{ V}$, Ta = $-30 \sim 85 ^{\circ}\text{C}$, $f_{\text{IN}} = 400 \sim 1000 \text{ MHz}$)

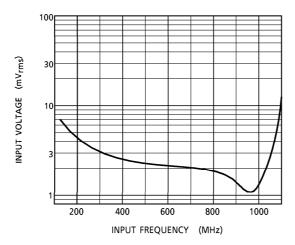
CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage		Vcc	_	_	4.5	5.0	5.5	V
Supply Current		ICC	_	$V_{CC} = 5.0 V$	_	40	70	mA
Operating Frequency Range		fIN	_	_	400	_	1000	MHz
Input Voltage Range		V _{IN}	_	_	50	_	250	mV _{rms}
Output Amplitude		Vout	_	_	1.0	1.2	_	V _{p-p}
Input Voltage	"L" Level	V _{IL}	_	PSC	0		V _{CC} × 0.3	\ \
Input Current	"H" Level	VIH	_	PSC	V _{CC} × 0.3		V _{CC}	٧
	"L" Level	IJL	_	PSC $V_{CC} = 5.0 \text{ V}, V_{IL} = 1.0 \text{ V}$	– 700	_	– 200	μΑ
"H" Level		lιΗ	_	PSC $V_{CC} = 5.0 \text{ V}, V_{IH} = 4.0 \text{ V}$	- 200	_	- 50	μΑ

TEST CIRCUIT (Input voltage sensitivity)



INPUT VOLTAGE SENSITIVITY

 $(V_{CC} = 5.0 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$



OUTLINE DRAWING DIP8-P-300-2.54 Unit: mm 6.4 ± 0.2 7.62 10.1MAX 9.6±0.2 0.85±0.1 0.30MIN 3.5±0.2 0.5±0.1 0.25 M 0.99TYP 2.54 1.2±0.1

Weight: 0.5 g (Typ.)