

Overview

The STK392-010 is a convergence correction circuit IC for video projectors. It incorporates three output amplifiers in a single package, making possible the construction of CRT horizontal and vertical convergence correction output circuits for each of the RGB colors using just two hybrid ICs.

Applications

· General video projectors

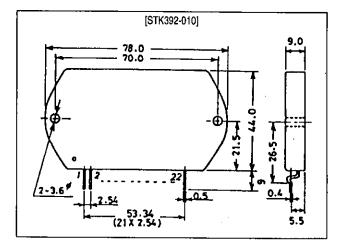
Features

- 3 output amplifier circuits in a single package (22-pin)
- High absolute maximum supply voltage (V_{CC} max = $\pm 38V$)
- Low thermal resistance (θ j-c = 2.6°C/W)
- High temperature stability ($T_C \max = 125^{\circ}C$)
- · Separate predriver and output stage supplies
- Output stage supply switching for high-performance designs
- Pins are arranged in separate groups of inputs, supply, and outputs to reduce the adverse effects of pattern layout on characteristics and to make design easier.
- Constant-current circuit in the predriver for stable supply switching operation
- Large lineup of family devices (STK392-000 series) to cover the range from general applications to high-class applications using a single PCB

Package Dimensions

unit: mm

4086A



Specifications

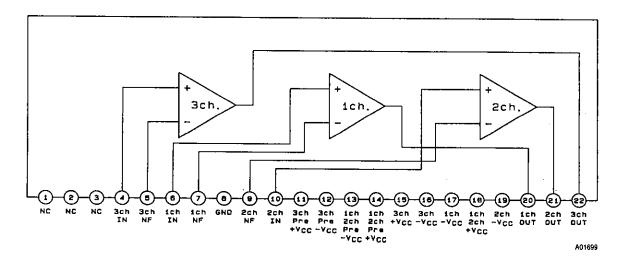
Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit V A	
Maximum supply voltage	V _{CC} max		±38		
Maximum collector current	l _c	Tr8, 10, 18, 20, 28, 30	5.0		
Thermal resistance	Өј-с	Tr8, 10, 18, 20, 28, 30 (per transistor)	2.6	°C/W	
Junction temperature	Tj		150	°C	
Operating substrate temperature	Tc		125	•C	
Storage temperature	Tstg		-30 to +125	°C	

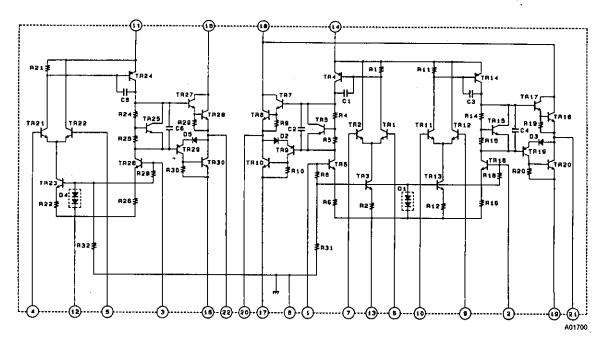
Operating Characteristics at Ta = 25°C, Rg = 50Ω

Parameter	Symbol	Conditions	min	typ	max	Unit
Output noise voltage	V _{NO}	V _{CC} = ±30V	_	-	0.2	mVrms
Quiescent current	Icco	V _{CC} = ±30V	30	90	150	mA
Neutral voltage	V _N	V _{CC} = ±30V	-50	0	+50	mV
Output delay time	to	V _{CC} = ±30V, f = 15.75kHz, triangular wave input, V _{OUT} = 1.5Vp-p	-	-	1.0	μs
Frequency response	fн	V _{CC} = ±30V, -3dB, (0dB at 1kHz), sine wave input, V _{in} = 50mVp-p	-	1.8	_	MHz

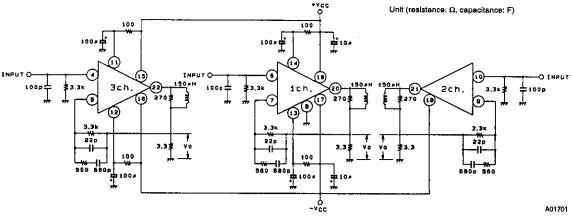
Block Diagram



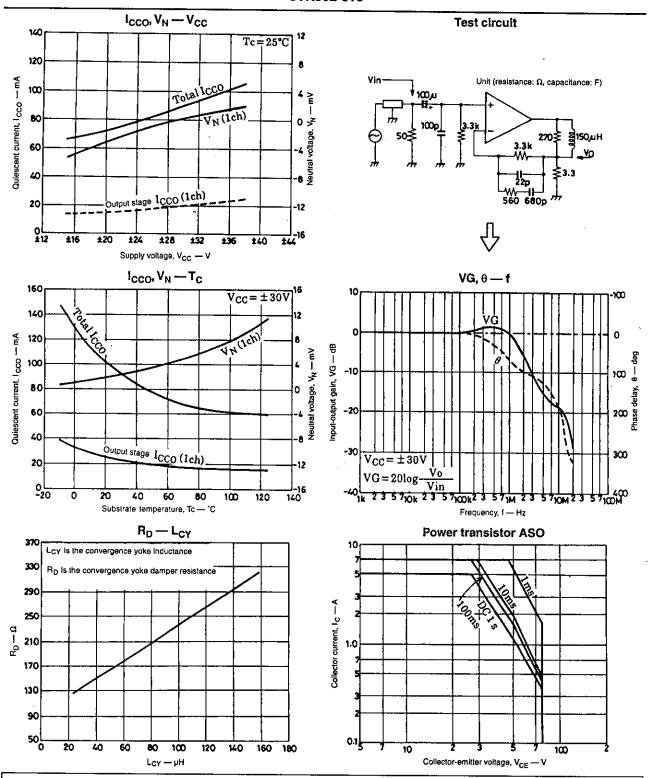
Equivalent Circuit



Test Circuit



 $\begin{array}{lll} \text{Vo:} & & \text{V}_{NO} \text{ is measured by connecting a VTVM.} \\ & & \text{V}_{N} \text{ is measured by connecting a DC voltmeter.} \\ & & \text{t}_{D} \text{ is measured by connecting an oscilloscope.} \end{array}$



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