



LA75676V-S

VIF/SIF IF Signal-Processing Circuit for TV and VCR Products

Overview

The LA75676V-S is an NTSC intercarrier VIF/SIF IC that adopts a semi-adjustment-free structure. In particular, it uses VCO adjustment to make AFT adjustment unnecessary and thus simplifies the overall adjustment process. A PLL-based technique is adopted for FM detection. The 5 V supply voltage provides compatibility with other multimedia systems. In addition it achieves high audio quality by incorporating a built-in buzz canceller that suppresses Nyquist buzz.

Functions

[VIF]

- VIF amplifier • PLL detector • RF AGC • EQ amplifier
- AFT • IF AGC • Buzz canceller

[SIF]

- Limiter amplifier • PLL FM detector

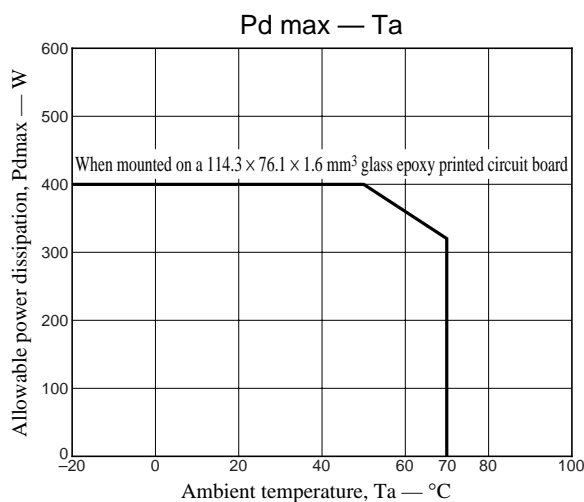
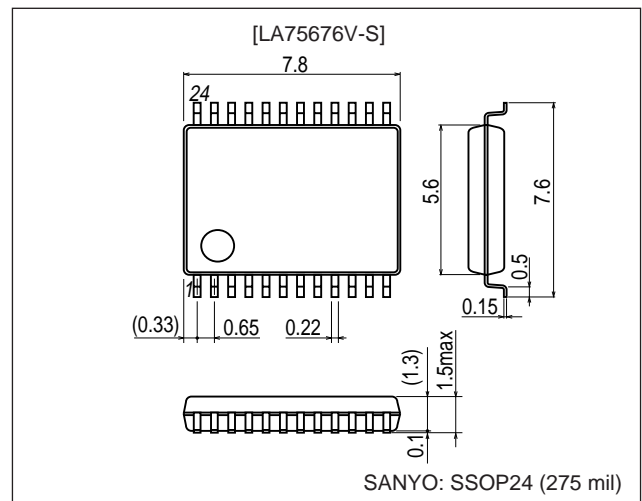
Features

- No AFT or SIF coils are used, thus eliminating adjustments.
- Excellent audio performance due to the built-in buzz canceller.
- $V_{CC} = 5\text{ V}$ and a low power dissipation of 250 mW.

Package Dimensions

unit: mm

3175B-SSOP24



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Specifications

Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		6	V
Circuit voltage	V13, V17		V _{CC}	V
Circuit current	I6		-3	mA
	I10		-10	mA
	I14		-2	mA
Allowable power dissipation	Pd max	Ta ≤ 70°C, *: Mounted on a PCB.	400	mW
Operating temperature	Topr		-20 to +70	°C
Storage temperature	Tstg		-55 to +150	°C

*: Stipulated PCB: 114.3 x 76.1 x 1.6 mm³, glass epoxy printed circuit board

Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V _{CC}		5	V
Operating voltage range	V _{CCOP}		4.5 to 5.5	V

Electrical Characteristics at Ta = 25°C, V_{CC} = 5.0 V, fp = 45.75 MHz

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
[VIF Block]						
Circuit current	I5		33	41	49	mA
Maximum RF AGC voltage	V14H		V _{CC} - 0.5	V _{CC}		V
Minimum RF AGC voltage	V14L			0	0.5	V
Input sensitivity	V _{IN}	S1 = OFF	32	38	44	dBμV
AGC range	G _R		58	63		dB
Maximum allowable input	V _{INmax}		95	100		dBμV
No-signal video output voltage	V6		3.5	3.8	4.1	V
Sync signal tip voltage	V6 tip		0.9	1.2	1.5	V
Video output level	V _O		1.7	2.0	2.3	Vp-p
Black noise threshold voltage	V _{BTH}		0.5	0.8	1.1	V
Black noise clamp voltage	V _{BCL}		1.6	1.9	2.2	V
Video signal-to-noise ratio	S/N		48	52		dB
C-S beating	IC-S		38	43		dB
Frequency characteristics	f _C	6 MHz	-3.0	-1.5		dB
Differential gain	DG			3.0	6.5	%
Differential phase	DP			3	5	deg
No-signal AFT voltage	V13		2.0	2.5	3.0	V
Maximum AFT voltage	V13H		4.0	4.4	5.0	V
Minimum AFT voltage	V13L		0	0.18	1.00	V
AFT detection sensitivity	Sf		28	40	52	mV/kHz
VIF input resistance	R _{IN}	45.75 MHz		1.5		kΩ
VIF input capacitance	C _{IN}	45.75 MHz		3		pF
APC pull-in range (U)	f _{PU}		1.3	2.0		MHz
APC pull-in range (L)	f _{PL}			-2.0	-1.4	MHz
AFT tolerance frequency 1	dfa 1		-150	0	+150	kHz
VCO 1 maximum variability range (U)	dfu		1.5	2.0		MHz
VCO 1 maximum variability range (L)	dfL			-2.0	-1.5	MHz
VCO control sensitivity	B		1.3	2.7	5.4	kHz/mV

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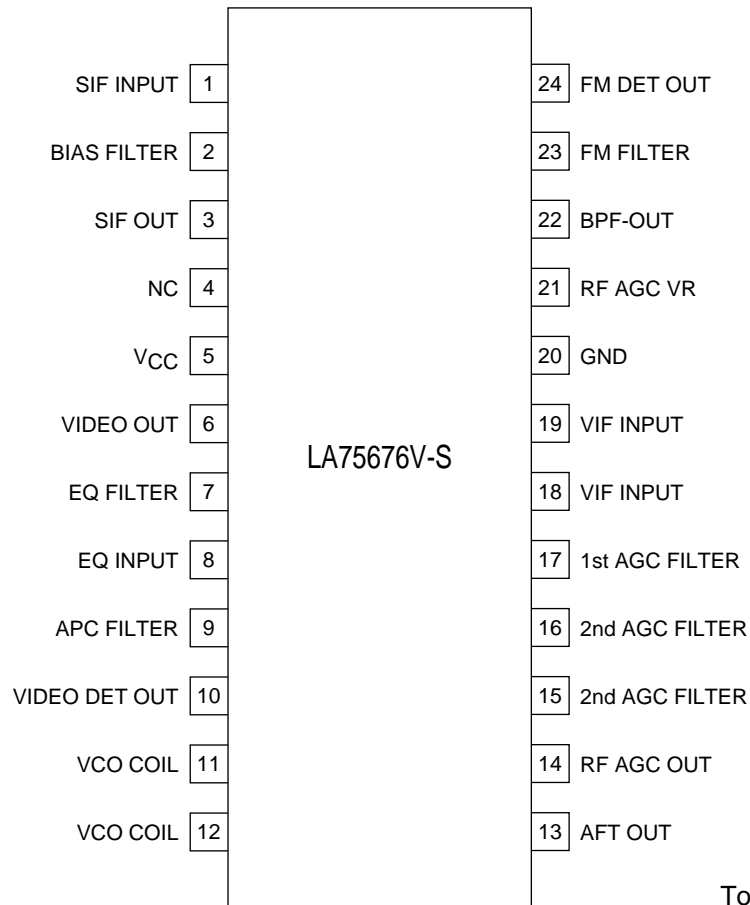
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
[SIF Block]						
Limiting sensitivity	V _{li} (lim)		39	45	51	dB μ V
FM detection output voltage	V _O (FM)	4.5 MHz \pm 25 kHz *1	400	520	660	mVrms
AMR rejection ratio	AMR		50	60		dB
Total harmonic distortion	THD			0.3	0.8	%
SIF signal-to-noise ratio	S/N (FM)		59	64		dB
4.5 MHz output level	V _{sout}	SIF IN 80 dB μ V	87	94	101	dB μ V

Note: If a wider FM detector output dynamic range is desired, insert a resistor and a capacitor in series between pin 23 and ground to adjust the level.

Pin Assignment

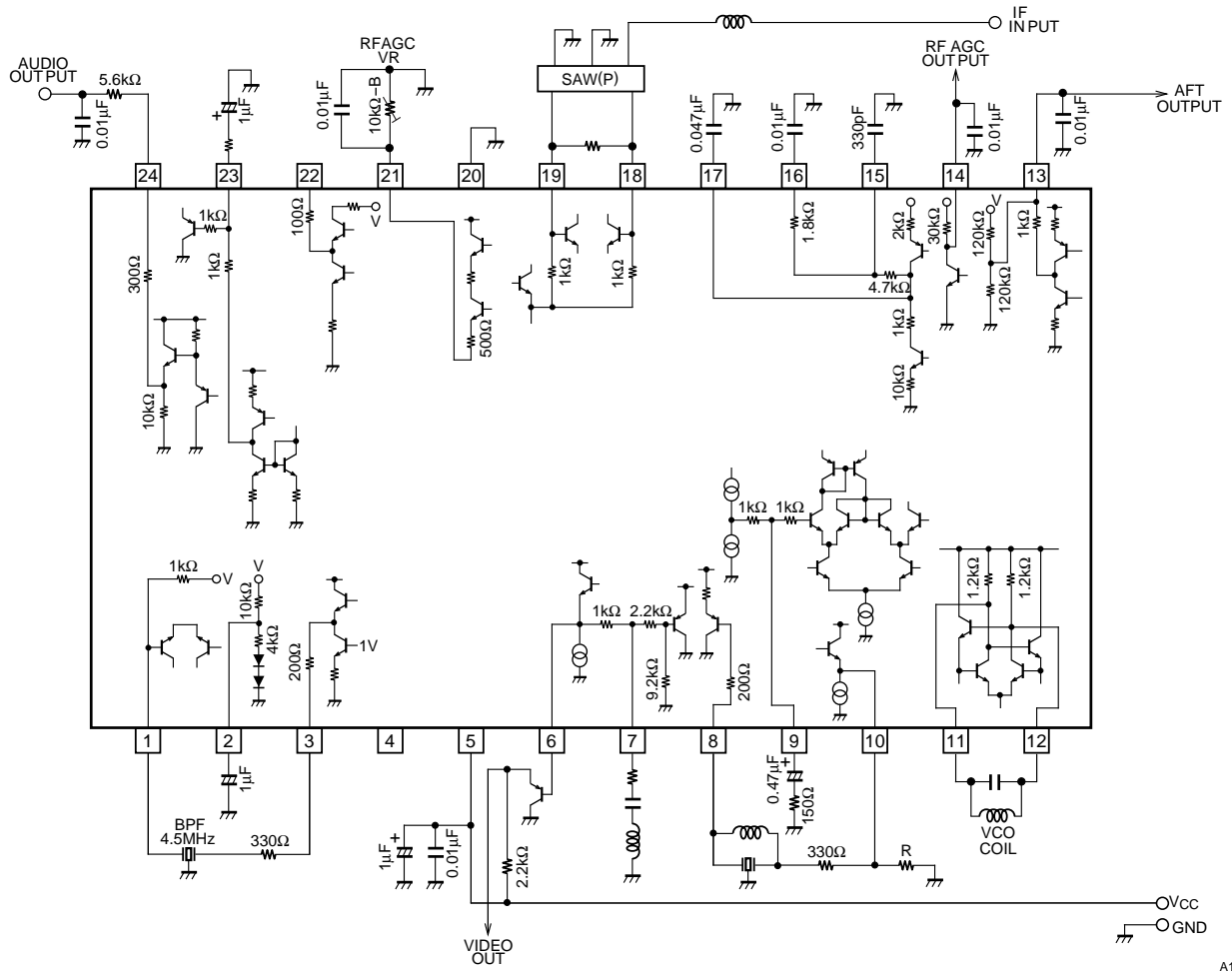


Top view

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Internal Equivalent Circuit and External Circuits

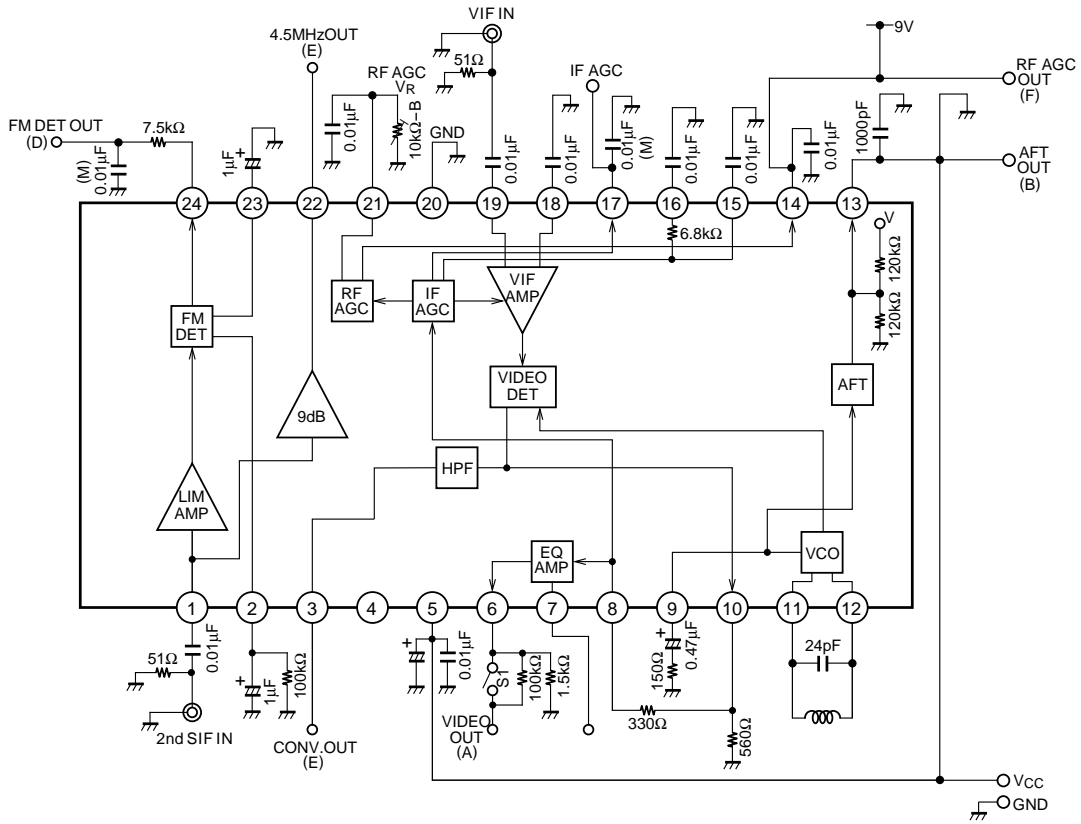


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Note: Resistor R must have a value of 560 Ω or larger.

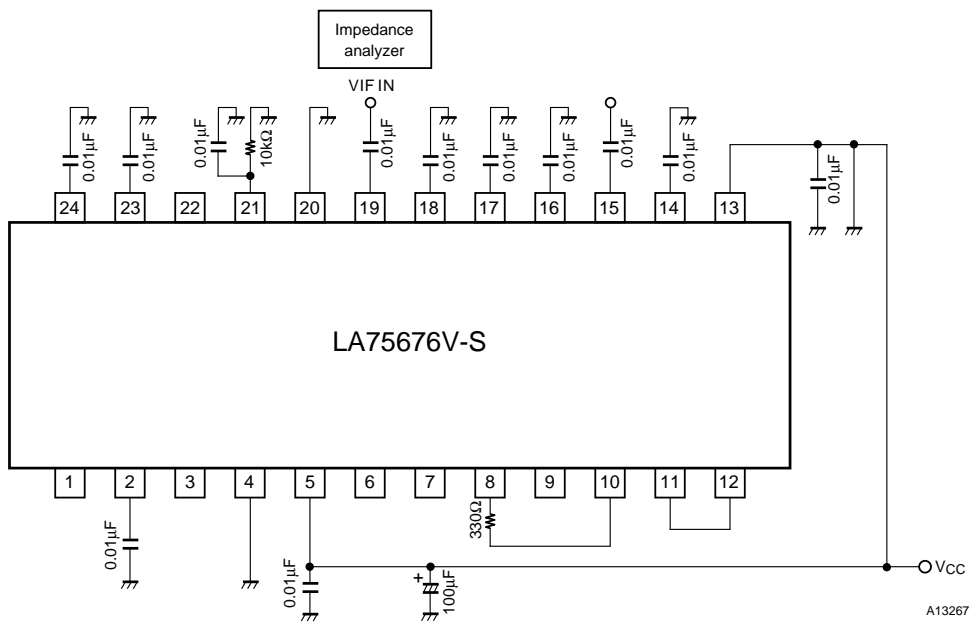
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AC Characteristics Test Circuit



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Test Circuit

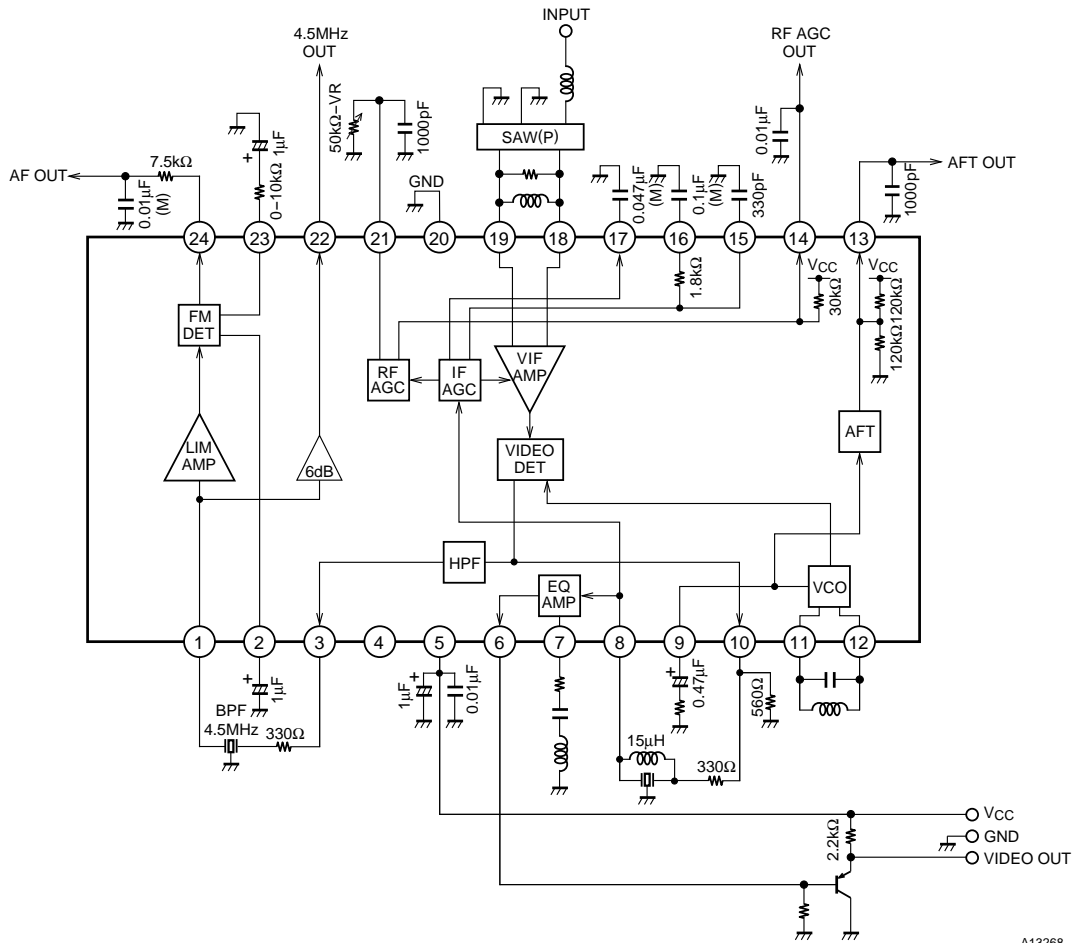


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Sample Application Circuit

PAL SPLIT



A13268

Pin Functions

Pin No.	Pin	Function	Equivalent circuit
1	SIF input	<p>SIF input</p> <p>The input impedance is about 1 kΩ.</p> <p>Since interference signals* entering this input can result in buzzing and beat signals, the pattern layout for the signal input to this pin must be designed carefully.</p> <p>*: Signals that can interfere with audio include video and chrominance signals. Thus the VIF carrier signal can cause interference.</p>	<p>A13269</p>
2	FM power supply filter	<p>The FM detector signal-to-noise ratio can be improved by inserting a filter in the FM detector bias line.</p> <p>C1 must be 0.47 μF or higher, and we recommend 1 μF.</p> <p>If the FM detector is not used, a 2 kΩ resistor must be inserted between pin 2 and ground. This stops the FM detector VCO circuit.</p>	<p>A13270</p>
3	SIF out	<p>Outputs the intercarrier detector output that has been passed through a high-pass filter.</p> <p>(4.5 MHz output)</p>	<p>A13271</p>
4	NC	This pin should be left open.	
5	V _{CC}	Use lines that are as short as possible for V _{CC} /ground decoupling.	

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Pin No.	Pin	Function	Equivalent circuit
6 7 8	EQ amp	<p>Equalizer circuit</p> <p>This circuit corrects the frequency characteristics of the video signal.</p> <p>Pin 8 is the input to the EQ amplifier. The EQ amplifier takes a 1.5 Vp-p video signal as its input and amplifies that to a 2.0 Vp-p level.</p> <ul style="list-style-type: none"> Notes on the equalizer amplifier design <p>The equalizer amplifier is designed as a voltage follower amplifier with a gain of about 2.3 dB. If frequency characteristics correction is required, insert the capacitor, inductor, and resistor between pin 7 and ground in series.</p> <ul style="list-style-type: none"> Using the equalizer amplifier <p>If the input signal is v_i and the output signal v_o, then</p> $\frac{R1}{Z} + 1 \quad (V_i + V_{in}) = V_o \times G$ <p>G: Gain of the voltage follower amplifier V_{in}: Imaginary voltage G: About 2.3 dB</p> <p>Assuming $V_{in} \approx 0$, then AV will be:</p> $AV = \frac{V_o G}{V_i} = \frac{R1}{Z} + 1$ <p>R1 is an IC internal 1 kΩ resistor. Simply select a value of Z according to the desired characteristics. However, note that the equalizer amplifier gain will be a maximum at the Z resonance, so care is required to prevent distortion from occurring.</p>	<p style="text-align: right;">A13272</p> <p style="text-align: right;">A13273</p>
9	APC filter	<p>PLL detector APC filter connection</p> <p>The APC time constant is switched internally by the IC. When locked, the VCO is controlled by the route A, and the gain is reduced. When unlocked or during weak field reception, the VCO is controlled by the route B, and the gain is increased.</p> <p>We recommend the following values for this APC filter:</p> <p>R = 150 to 390 Ω C = 0.47 μF.</p>	<p style="text-align: right;">A13274</p>

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Pin No.	Pin	Function	Equivalent circuit
10	Composit video output	Outputs a video signal that includes the SIF carrier. A resistor must be inserted between pin 10 and ground to acquire adequate drive capability. $R \geq 560 \Omega$	<p>A13275</p>
11 12	VCO tank	VCO tank circuit for video detection See the separately provided coil specifications for details on the tank circuit. This VCO is a vector synthesis VCO circuit.	<p>A13276</p>
13	AFT output	AFT output This circuit includes a function that controls the AFT voltage so that it naturally goes to the center voltage during weak field reception. A 120 kΩ bleeder resistor is built in. Note that the sensitivity can be lowered by attaching an external resistor.	<p>A13277</p>
14	RF AGC output	RF AGC output This output controls the tuner RF AGC. The internal circuit includes both a 30 kΩ pull-up resistor and a 100 Ω protective resistor. Determine the value of the external bleeder resistor to match the specifications of the tuner.	<p>A13278</p>

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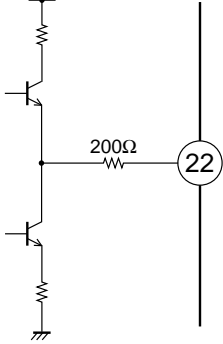
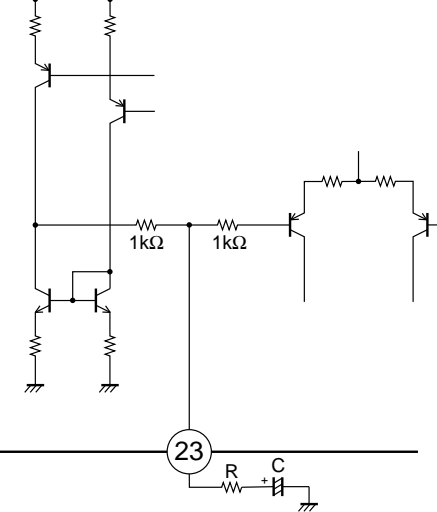
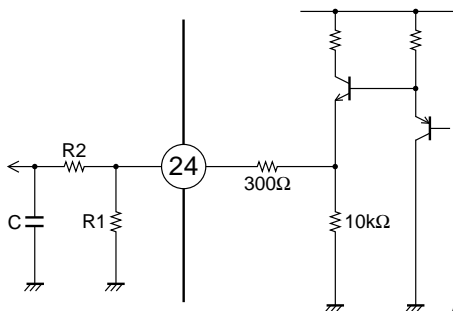
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Pin No.	Pin	Function	Equivalent circuit
15 16 17	AGC filter	<p>IF AGC filter connection</p> <p>The AGC voltage is created by smoothing the signal that results from peak detection by the AGC detector at pins 17 (first AGC), and 15 and 16 (second AGC). The video signal input to this IF AGC detector is a signal that was passed through the audio trap circuit.</p>	<p style="text-align: right;">A13279</p>
18 19	VIF input	<p>VIF amplifier input</p> <p>The input circuit is a balanced input, and its input impedance is due to the following component values.</p> <p>$R = 1.5\text{ k}\Omega$</p> <p>$C = 3\text{ pF}$</p>	<p style="text-align: right;">A13280</p>
20	GND		
21	RF AGC VR	<p>RF AGC adjustment</p> <p>This pin sets the tuner's RF AGC operating point. Both the FM output and the video output can be muted by setting this pin to the ground level.</p>	<p style="text-align: right;">A13281</p>

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Pin No.	Pin	Function	Equivalent circuit
22	BPF-out	<p>Bandpass filter output</p> <p>The output to the external bandpass filter is passed through an internal 6 dB amplifier before being output.</p>	 <p style="text-align: right;">A13282</p>
23	FM filter	<p>Filter that holds the FM detector output DC voltage fixed.</p> <p>Normally, a 1 μF electrolytic capacitor is used. If the low band (around 50 Hz) frequency characteristics are of concern, this value should be increased. The FM detection output level can be reduced and the FM dynamic range improved by inserting the resistor R in series with the capacitor between pin 23 and ground.</p>	 <p style="text-align: right;">A13283</p>
24	FM detector output	<p>Audio FM detector output</p> <p>This is an emitter-follower circuit with a 300 Ω resistor inserted in series.</p> <ul style="list-style-type: none"> • Stereo applications In some application that provide input to a stereo decoder, the input impedance may be reduced, resulting in distortion in the L-R signal and degraded stereo characteristics. If this problem occurs, add a resistor between pin 24 and ground. $R1 \geq 5.1 \text{ k}\Omega$ • Mono applications Construct an external deemphasis circuit. $t = CR2$ 	 <p style="text-align: right;">A13284</p>

Notes on Sanyo SAW Filters

There are two types of SAW filters, which differ in the piezoelectric substrate material used, as follows:

- Lithium tantalate (LiTaO3) SAW filter

TSF11□□ ... Japan

TSF12□□ ... US

Although lithium tantalate SAW filters have the low temperature coefficient of $-18 \text{ ppm}/^\circ\text{C}$, they suffer from a large insertion loss. However, it is possible, at the cost of increasing the number of external components required, to minimize this insertion loss by using a matching circuit consisting of coils and other components at the SAW filter output. At the same time as minimizing insertion loss, this technique also allows the frequency characteristics, level, and other aspects to be varied, and thus provides increased circuit design flexibility. Also, since the SAW filter reflected wave level is minimal, the circuit can be designed with a low in-band ripple level.

- Lithium niobate (LiNbO3) SAW filter

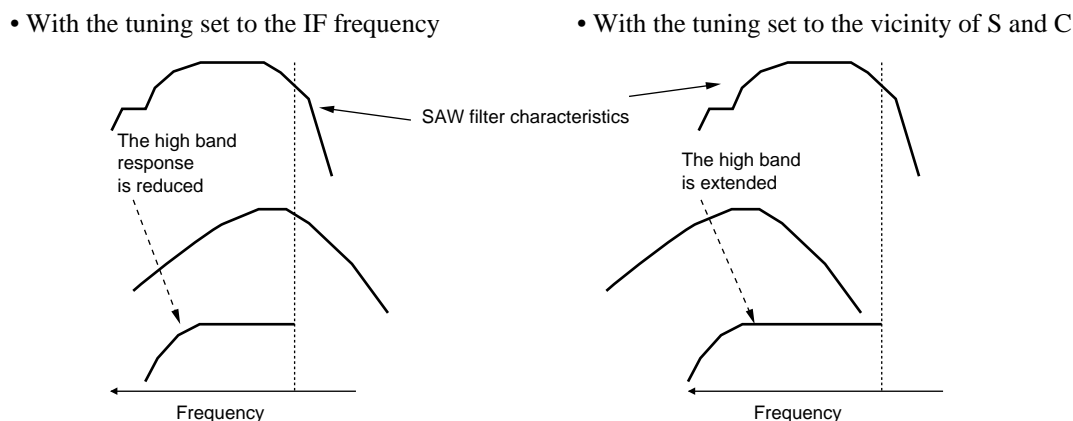
TSF52□□ ... US

TSF53□□ ... PAL

Although lithium niobate SAW filters have the high temperature coefficient of $-72 \text{ ppm}/^\circ\text{C}$, they feature an insertion loss about 10 dB lower than that of lithium tantalate SAW filters. Accordingly, there is no need for a matching circuit at the SAW filter output. Although the in-band ripple is somewhat larger than with lithium tantalate SAW filters, since they have a low impedance and a small field slew, they are relatively immune to influences from peripheral circuit components and the geometry of the printed circuit board pattern. This allows stable out-of-band trap characteristics to be acquired. Due to the above considerations, lithium tantalate SAW filters are used in applications for the US and Japan that have a high IF frequency, and lithium niobate SAW filters are used in PAL and US applications that have a low IF frequency.

Notes on SAW Filter Matching

In SAW filter input circuit matching, rather than matching the IF frequency, flatter video band characteristics can be acquired by designing the tuning point to be in the vicinity of the audio carrier rather than near the chrominance carrier. The situation shown in figure on the right makes it easier to acquire flat band characteristics than that in figure on the left.



A13285

Coil Specifications

	JAPAN $f = 58.75 \text{ MHz}$	US $f = 45.75 \text{ MHz}$	PAL $f = 38.9 \text{ MHz}$
VCO coil	<p>t=5t 0.12ø C=24pF</p> <p>A13286</p> <p>Prototype no. V291XCS-3220Z The Toko Electric Corporation</p>	<p>t=6t 0.12ø C=24pF</p> <p>A13287</p> <p>Prototype no. 291XCS-3188Z The Toko Electric Corporation</p>	<p>t=7t 0.12ø C=24pF</p> <p>A13288</p> <p>Prototype no. 292GCS-7538Z The Toko Electric Corporation</p>
SAW filter (SPLIT)	Picture TSF1137U Sound	Picture TSF1241 Sound	Picture TSF5315 Sound
SAW filter (INTER)		TSF5220, TSF5221	TSF5321, TSF5344

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Notes on VCO Transformer Circuits**• Built-in capacitor VCO transformer circuits**

When power is first applied, the heat generated by the IC is transmitted through the printed circuit board to the VCO transformer. However, the VCO coil frame functions as a heat sink and dissipates the heat from the IC. As a result, it is relatively difficult to transmit heat to the VCO transformer's built-in capacitor, and drift at power on is minimal. Therefore, it suffices to design the circuit so that the coil and capacitor thermal characteristics cancel. Ideally, it is better to use a coil with a core material that has low temperature coefficient characteristics.

• External capacitor VCO transformer circuits

When an external capacitor is used, the heat generated by the IC is transmitted through the printed circuit board directly to the VCO tank circuit external capacitor. While this capacitor is heated relatively early after power is applied, the coil is not influenced as much by this heat, and as a result, the power-on drift is larger. Accordingly, a coil whose core material has low temperature coefficient characteristics must be used. It is also desirable to use a capacitor with similarly low temperature coefficient characteristics.

Note: Applications that use an external capacitor here must use a chip capacitor. If an ordinary capacitor is used, problems such as the oscillator frequency changing with the capacitor orientation may occur.

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