

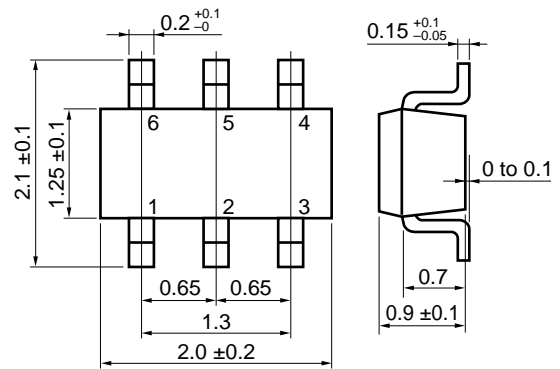
N-CHANNEL MOS FET ARRAY  
 FOR SWITCHING

The  $\mu$ PA672T is a super-mini-mold device provided with two MOS FET elements. It achieves high-density mounting and saves mounting costs.

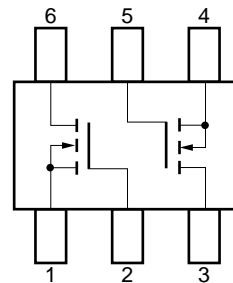
FEATURES

- Two MOS FET circuits in package the same size as SC-70
- Automatic mounting supported

PACKAGE DIMENSIONS (in millimeters)



PIN CONNECTION



1. Source 1 (S1)
  2. Gate 1 (G1)
  3. Drain 2 (D2)
  4. Source 2 (S2)
  5. Gate 2 (G2)
  6. Drain 1 (D1)
- Marking: MA

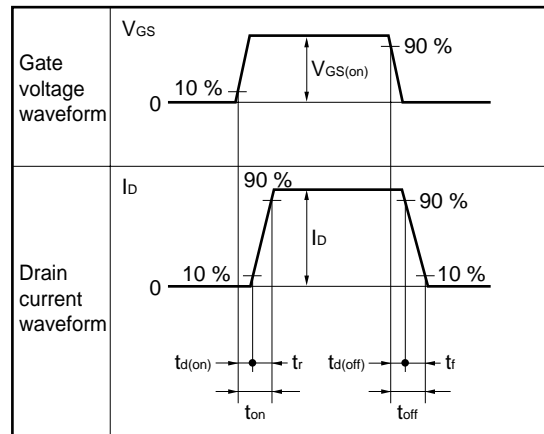
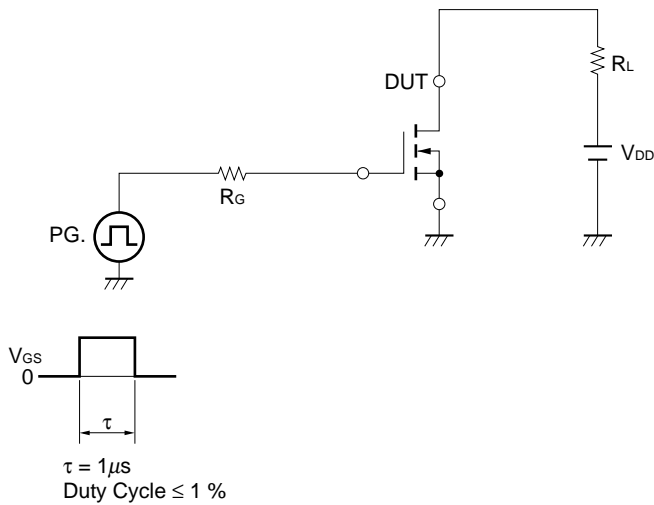
ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ }^\circ\text{C}$ )

PARAMETER	SYMBOL	TEST CONDITIONS	RATINGS	UNIT
Drain to Source Voltage	$V_{DSS}$		50	V
Gate to Source Voltage	$V_{GSS}$		$\pm 7.0$	V
Drain Current (DC)	$I_{D(DC)}$		100	mA
Drain Current (pulse)	$I_{D(pulse)}$	$PW \leq 10\text{ ms, Duty Cycle} \leq 50\%$	200	mA
Total Power Dissipation	$P_T$		200 (Total)	mW
Channel Temperature	$T_{ch}$		150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		-55 to +150	$^\circ\text{C}$

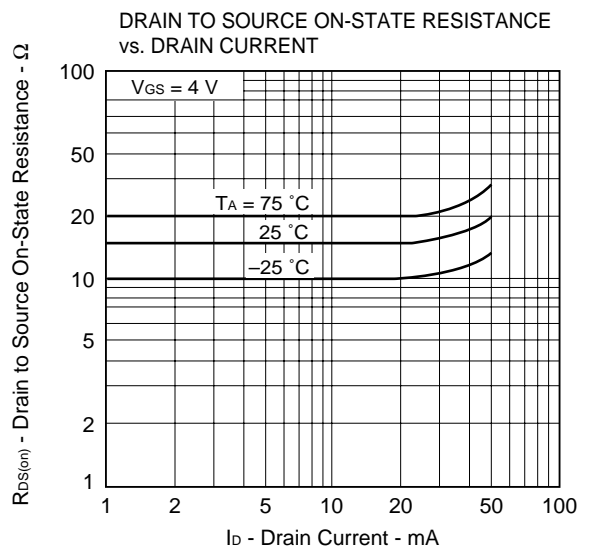
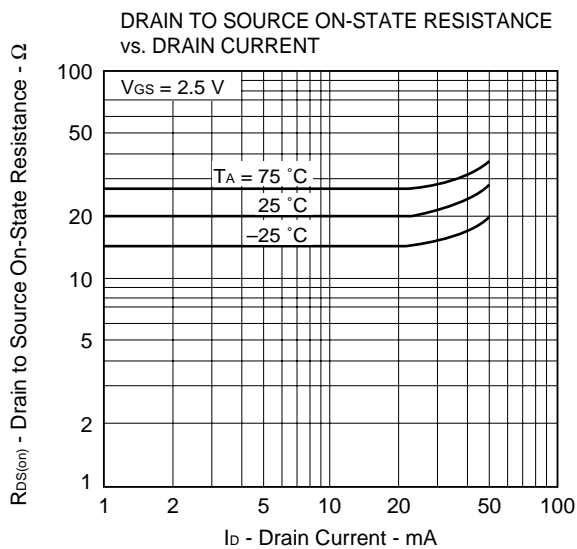
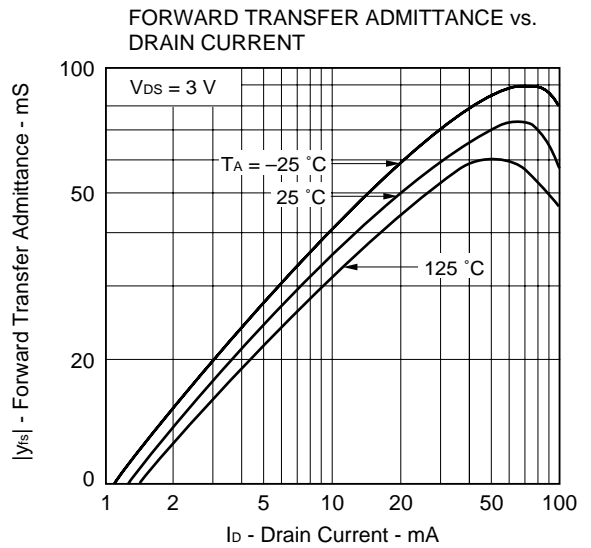
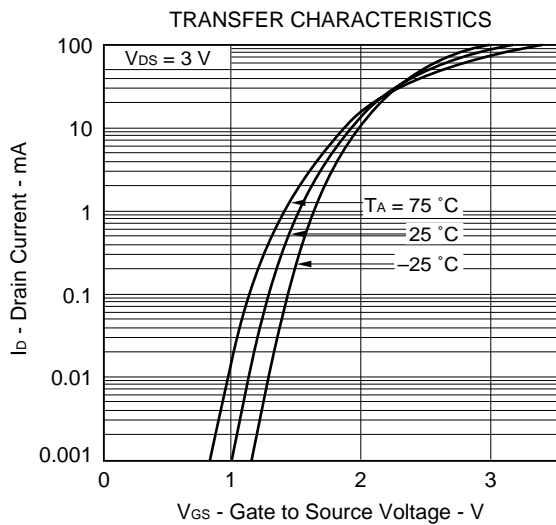
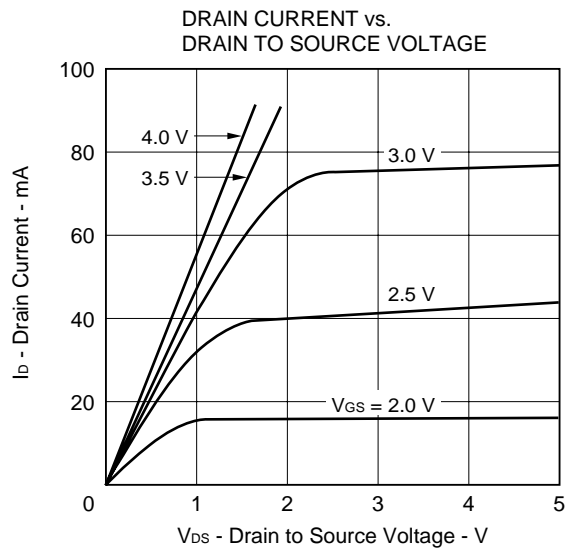
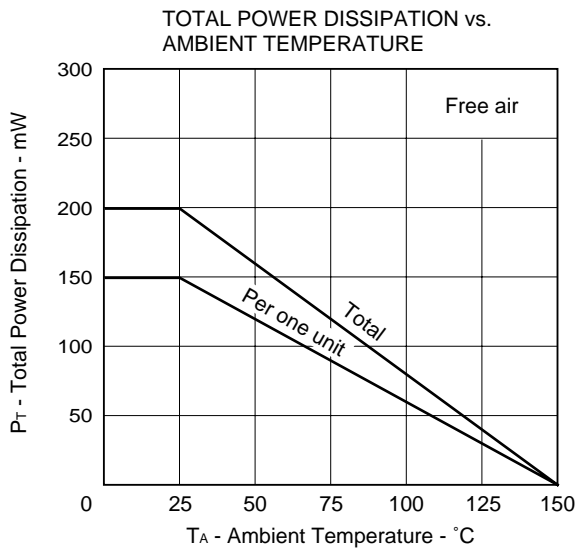
**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C)**

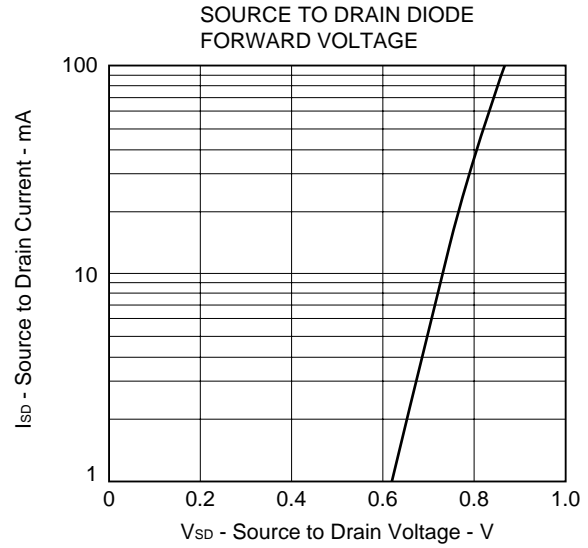
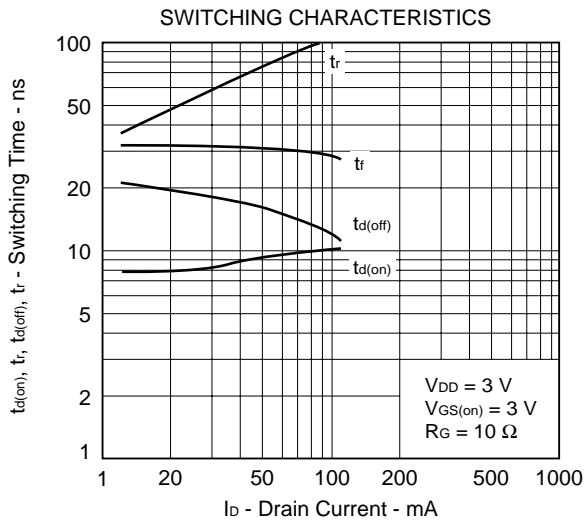
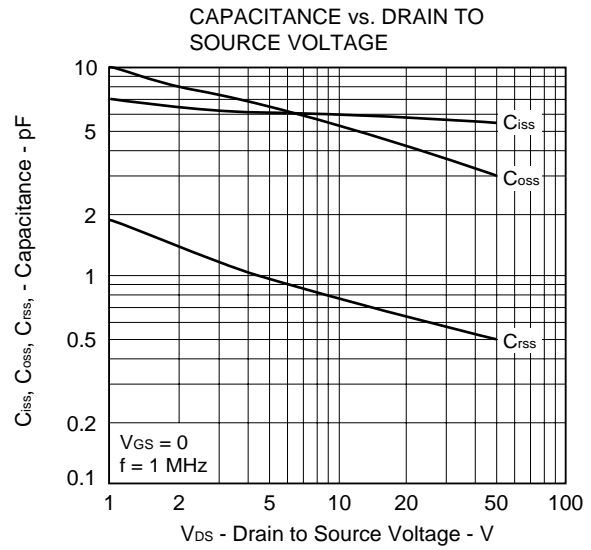
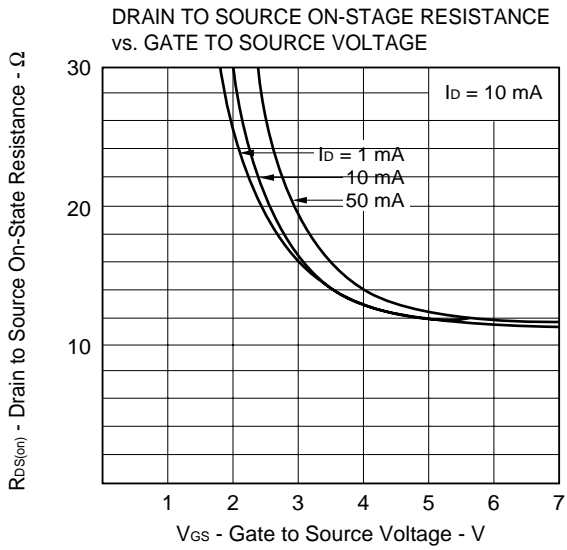
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0			10	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±7.0 V, V <sub>DS</sub> = 0			±5.0	μA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 3.0 V, I <sub>D</sub> = 1.0 μA	0.7	1.0	1.5	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> = 3.0 V, I <sub>D</sub> = 10 mA	20			mS
Drain to Source On-State Resistance	R <sub>DS(on)1</sub>	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 10 mA		20	40	Ω
Drain to Source On-State Resistance	R <sub>DS(on)2</sub>	V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 10 mA		15	20	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 3.0 V, V <sub>GS</sub> = 0, f = 1.0 MHz		6		pF
Output Capacitance	C <sub>oss</sub>			8		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			1.2		pF
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 3 V, I <sub>D</sub> = 20 mA, V <sub>GS(on)</sub> = 3 V, R <sub>G</sub> = 10 Ω, R <sub>L</sub> = 120 Ω		9		ns
Rise Time	t <sub>r</sub>			50		ns
Turn-Off Delay Time	t <sub>d(off)</sub>			20		ns
Fall Time	t <sub>f</sub>			40		ns

**SWITCHING TIME MEASUREMENT CIRCUIT AND CONDITIONS**



TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ )





## REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system	TEI-1202
Quality grade on NEC semiconductor devices	IEI-1209
Semiconductor device mounting technology manual	C10535E
Guide to quality assurance for semiconductor devices	MEI-1202
Semiconductor selection guide	X10679E

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Anti-radioactive design is not implemented in this product.