

Quad Differential Amp

Monolithic IC MM1366

Outline

This IC is a 0dB gain amp with 4 channels of built-in differential input and differential output. It has powerful noise removal at common mode level.

Features

- (1) S/N can be improved for common mode noise on internal and external signal transmission due to differential input/output.
- (2) CMRR (common mode rejection ratio): 60 dB typ.
- (3) PSRR (power supply rejection ratio): 80 dB typ.
- (4) Consumption current 1.5mA typ.

Package

TSOP-20A

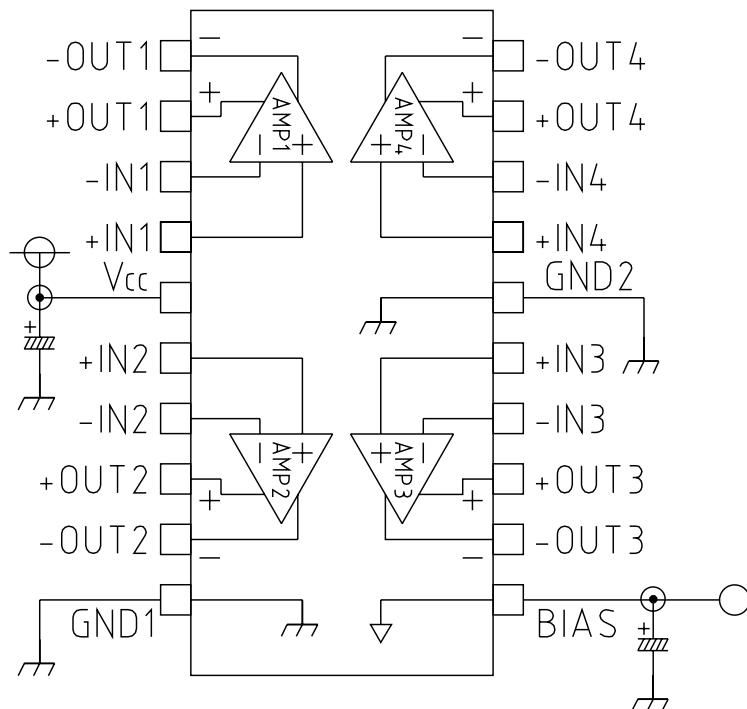
Applications

- (1) Notebook PC audio

Pin Description

Pin No.	Pin name	Functions	Pin No.	Pin name	Functions
1	-OUT1	Inverting output of AMP1	11	BIAS	BIAS voltage terminal
2	+OUT1	Non Inverting output AMP1			BIAS voltage=Vcc/2
3	-IN1	Inverting input to AMP1	12	-OUT3	Inverting output of AMP3
4	+IN1	Non Inverting input to AMP1	13	+OUT3	Non Inverting output of AMP3
5	Vcc	Supply voltage input pin	14	-IN3	Inverting input to AMP3
6	+IN2	Non Inverting input to AMP2	15	+IN3	Non Inverting output of AMP3
7	-IN2	Inverting input to AMP2	16	GND2	GND for AMP3, AMP4
8	+OUT2	Non Inverting output of AMP2			connected to GND1.
9	-OUT2	Inverting output of AMP2	17	+IN4	Non Inverting output of AMP4
10	GND1	GND for AMP1, AMP2 connected to GND2.	18	-IN4	Inverting input to AMP4
			19	+OUT4	Non Inverting output of AMP4
			20	-OUT4	Inverting output of AMP4

Block Diagram



Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Ratings	Units
Storage temperature	T _{STG}	-40~+125	°C
Operating temperature	T _{OPR}	-20~+75	°C
Supply voltage	V _{ccmax.}	10	V
Allowable loss	P _d	300	mW

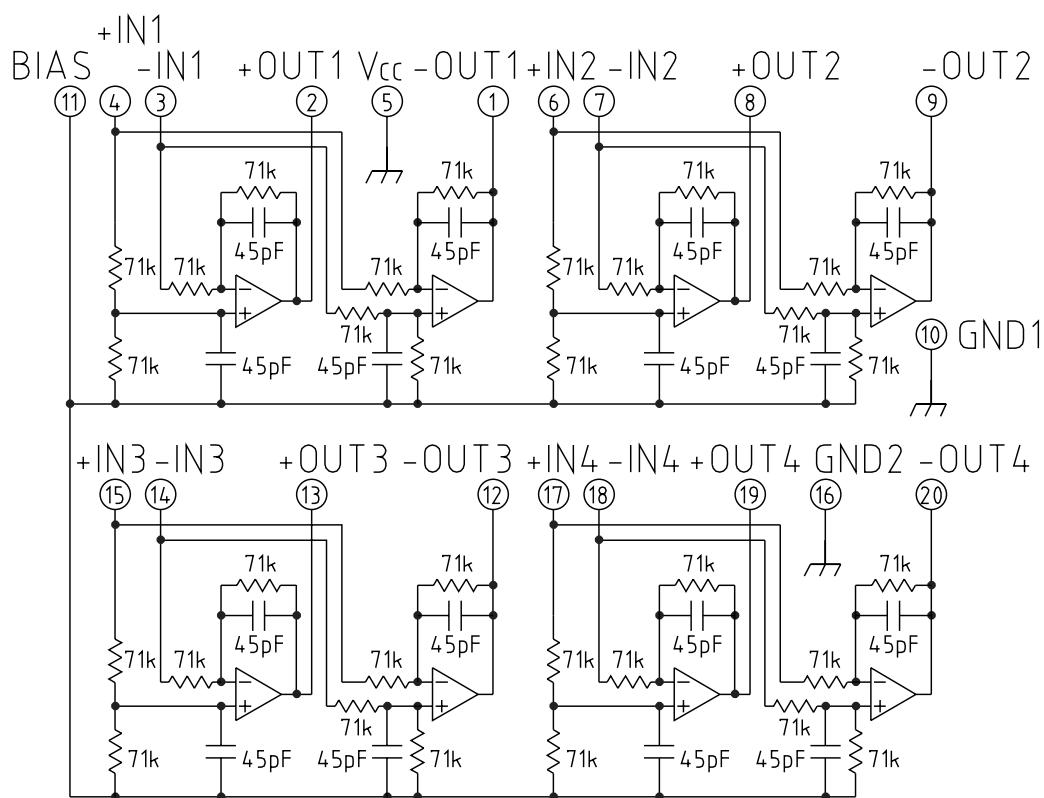
Recommended Operating Conditions

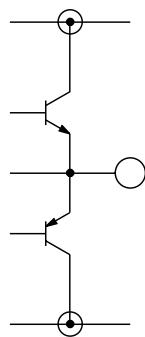
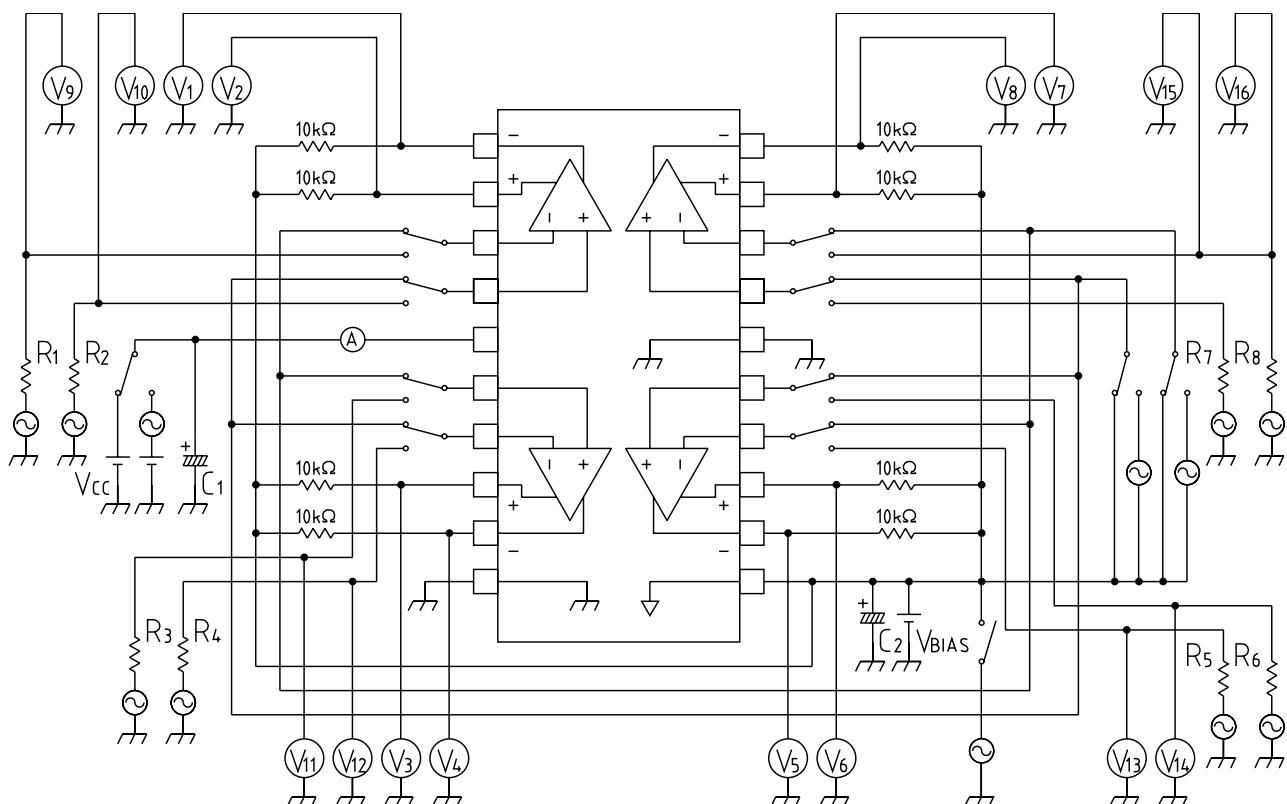
Item	Symbol	Ratings	Units
Operating temperature	T _{OPR}	-20~+75	°C
Operating voltage 1	V _{OP}	4.0~6.0	V
Operating voltage 2	V _{OPBIAS}	V _{OP} /2	V

Electrical Characteristics (Except where otherwise indicated, Ta=25°C, V_{CC}=5V, f=1kHz, V_{BIA}S=2.5V)

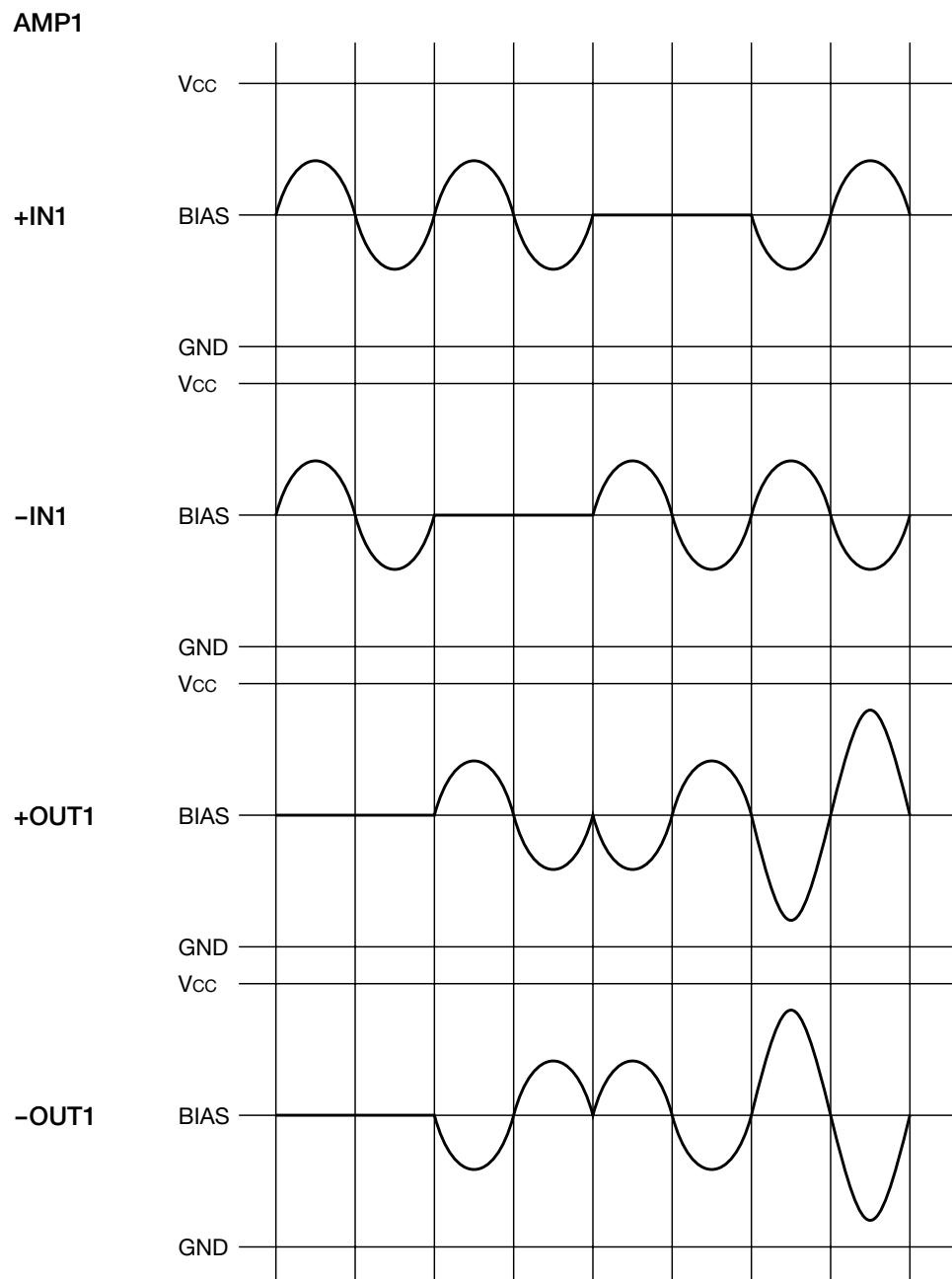
Item	Signal	Measurement conditions	Min.	Typ.	Max.	Unit
Full circuit current consumption	I _{CC}	When non signal input, non load Input voltage=0, R _L =∞	0.9	1.5	2.4	mA
Input impedance	Z _{IN}		37.5	47.0	57.5	kΩ
Input voltage (DC)	V _i		1.0		V _{CC} -1.0	V
Voltage gain 1	A _{V1}	R _L =10kΩ, f _{IN} =20Hz~10kHz	-1.0	0	+1.0	dB
Voltage gain 2 (50kHz)	A _{V2}	R _L =10kΩ, f _{IN} =50kHz	-6.0	-3.0	+1.0	dB
Voltage gain 3 (200kHz)	A _{V3}	R _L =10kΩ, f _{IN} =200kHz	-16.0	-12.0	-8.0	dB
Differential BIAS voltage input / output	ΔV _B		-12		12	mV
Output voltage	V _{OR}		1.0		V _{CC} -1.0	V
Maximum output voltage	V _{max.}	R _L =10kΩ, When THD=10% V _{BIA} S=2.5V	1.00	1.25		Vrms
Total Harmonic Distortion	THD	R _L =10kΩ, Input 1.0V _{P-P}		0.03	0.10	%
Output impedance	Z _O			5.0	15.0	Ω
Common mode ripple rejection ratio	CMRR	R _L =10kΩ, Input 1.0V _{P-P}	50	60		dB
Power supply ripple rejection ratio 1	PSRR1	f=100Hz, R _L =10kΩ, V _{CC} =1.0V _{P-P}	70	80		dB
Power supple ripple rejection ratio 2	PSRR2	f=20kHz, R _L =10kΩ, V _{CC} =1.0V _{P-P}	46	52		dB
Output noise voltage	V _{ON}	Range 10Hz~110kHz BW=10Hz~110kHz		30	90	μVrms

Internal Circuit



Output circuit**Measuring Circuit**

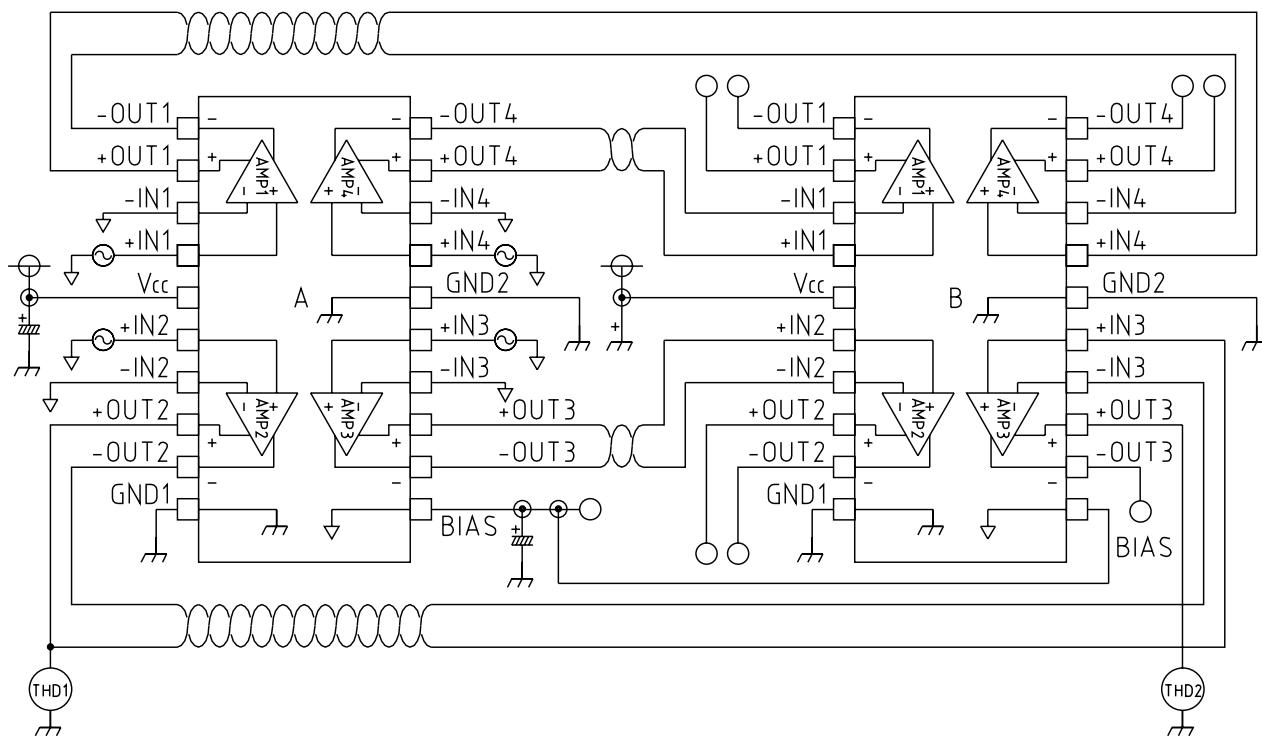
Time Waveforms



*1 : AMP2, AMP3, AMP4 are same as AMP1.

*2 : BIAS voltage=Vcc/2

Application Circuit



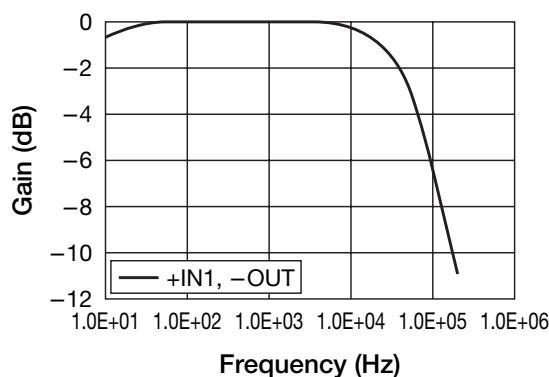
Note 1 : This is 0dB differential amp that have plus and minus pin at input and output.
(The condition of measuring gain is single input and output.)

Note 2 : The gain is 6dB at typical application circuit.

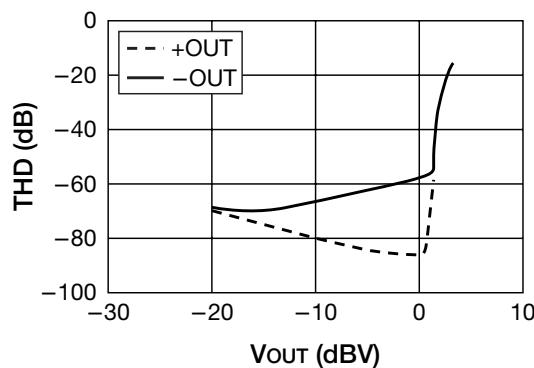
Note 3 : When connect A amplifier 2 and B Amplifier 3, improve for distortion rate from
THD1=-70dB to THD2=-75dB. (when +OUT3 output-10dB voltage)

Characteristics

Gain-Frequency



V_{OUT}-THD



Note: The distortion rate for +IN, -OUT can be improved by -15dB as compared to +IN, -OUT.
($V_{OUT} = -10\text{dBV}$)