

Data Sheet January 2000 File Number 2933.3

# 15A, 1000V Ultrafast Dual Diode

The RURG15100CC is an ultrafast dual diode with soft recovery characteristics ( $t_{rr}$  < 100ns). It has low forward voltage drop and is of silicon nitride passivated ionimplanted epitaxial planar construction.

This device is intended for use as a freewheel/clamping diode and rectifier in a variety of switching power supplies and other power switching applications. Its low stored charge and ultrafast recovery with soft recovery characteristic minimizes ringing and electrical noise in many power switching circuits, thus reducing power loss in the switching transistors.

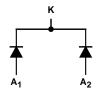
Formerly developmental type TA09906.

# **Ordering Information**

PART NUMBER	PACKAGE	BRAND
RURG15100CC	TO-247	URG15100C

NOTE: When ordering, use the entire part number.

# Symbol



#### **Features**

•	Ultrafast with Soft Recovery <100ns
•	Operating Temperature
•	Reverse Voltage

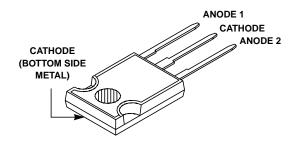
- Avalanche Energy Rated
- Planar Construction

# **Applications**

- · Switching Power Supplies
- Power Switching Circuits
- · General Purpose

#### **Packaging**

**JEDEC STYLE TO-247** 



<b>Absolute Maximum Ratings</b> (Per Leg) $T_C = 25^{\circ}C$ , Unless Otherwise Specified		
	RURG15100CC	UNITS
Peak Repetitive Reverse VoltageV <sub>RRM</sub>	1000	V
Working Peak Reverse VoltageV <sub>RWM</sub>	1000	V
DC Blocking Voltage	1000	V
Average Rectified Forward Current $I_{F(AV)}$ ( $T_C = 142^{\circ}C$ )	15	Α
Repetitive Peak Surge Current	30A	Α
Nonrepetitive Peak Surge Current	200A	Α
Maximum Power Dissipation	100	W
Avalanche Energy (See Figures 7 and 8)	20	mJ
Operating and Storage Temperature	-65 to 175	οС

**Electrical Specifications** (Per Leg)  $T_C = 25^{\circ}C$ , Unless Otherwise Specified.

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
V <sub>F</sub>	I <sub>F</sub> = 15A	-	-	1.8	V
	$I_F = 15A, T_C = 150^{\circ}C$	-	-	1.5	V
I <sub>R</sub>	V <sub>R</sub> = 1000V	-	-	100	μΑ
	$V_R = 1000V, T_C = 150^{\circ}C$	-	-	500	μΑ
t <sub>rr</sub>	I <sub>F</sub> = 1A, dI <sub>F</sub> /dt = 100A/μs	-	-	100	ns
	$I_F = 15A$ , $dI_F/dt = 100A/\mu s$	-	-	125	ns
t <sub>a</sub>	$I_F = 15A$ , $dI_F/dt = 100A/\mu s$	-	75	-	ns
t <sub>b</sub>	$I_F = 15A$ , $dI_F/dt = 100A/\mu s$	-	40	-	ns
$R_{ heta JC}$		-	-	1.5	°C/W

#### **DEFINITIONS**

 $V_F$  = Instantaneous forward voltage (pw = 300 $\mu$ s, D = 2%).

 $I_R$  = Instantaneous reverse current.

 $t_{rr}$  = Reverse recovery time at  $dI_F/dt$  = 100A/ $\mu$ s (See Figure 6), summation of  $t_a$  +  $t_b$ .

 $t_a$  = Time to reach peak reverse current at  $dI_F/dt$  = 100A/ $\mu$ s (See Figure 6).

 $t_b$  = Time from peak  $I_{RM}$  to projected zero crossing of  $I_{RM}$  based on a straight line from peak  $I_{RM}$  through 25% of  $I_{RM}$  (See Figure 6).

 $R_{\theta JC}$  = Thermal resistance junction to case.

pw = Pulse width.

D = Duty cycle.

# **Typical Performance Curves**

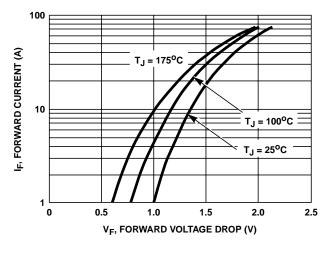


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

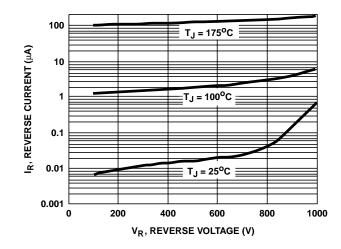


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

#### Typical Performance Curves (Continued)

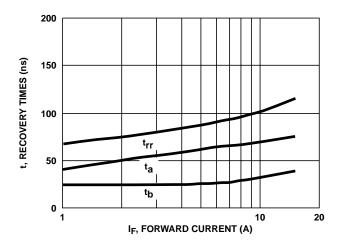


FIGURE 3. t<sub>rp</sub>, t<sub>a</sub> AND t<sub>b</sub> CURVES vs FORWARD CURRENT

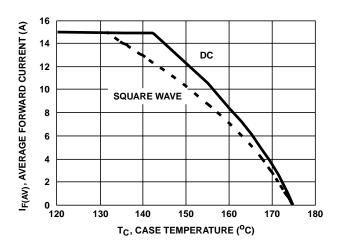


FIGURE 4. CURRENT DERATING CURVE

#### Test Circuits and Waveforms

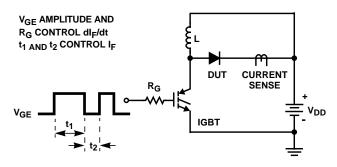


FIGURE 5. t<sub>rr</sub> TEST CIRCUIT

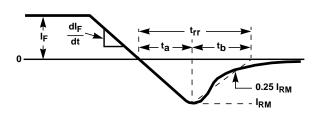


FIGURE 6. t<sub>rr</sub> WAVEFORMS AND DEFINITIONS

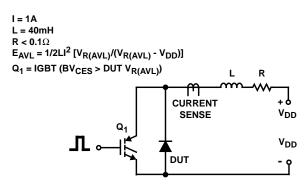


FIGURE 7. AVALANCHE ENERGY TEST CIRCUIT

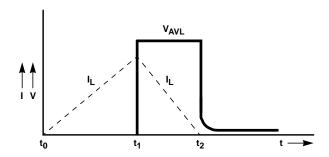


FIGURE 8. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

All Intersil semiconductor products are manufactured, assembled and tested under ISO9000 quality systems certification.

Intersil semiconductor products are sold by description only. Intersil Corporation reserves the right to make changes in circuit design and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by Intersil is believed to be accurate and reliable. However, no responsibility is assumed by Intersil or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Intersil or its subsidiaries.

For information regarding Intersil Corporation and its products, see web site www.intersil.com