TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8440H,TA8440F

DC MOTOR FULL BRIDGE DRIVER F TYPE: UNDER DEVELOPMENT

The TA8440H is a full-bridge driver for selecting the forward and reverse running of a motor with brushes and is able to control 4 modes of forward, reverse, stop and braking.

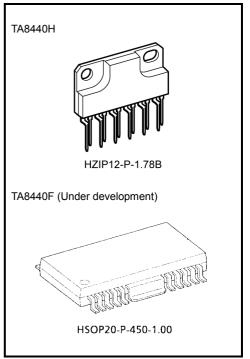
The motor driving unit and the control unit have a separate power supply line, independently and the TA8440H is also usable as a stepping motor driver.

FEATURES

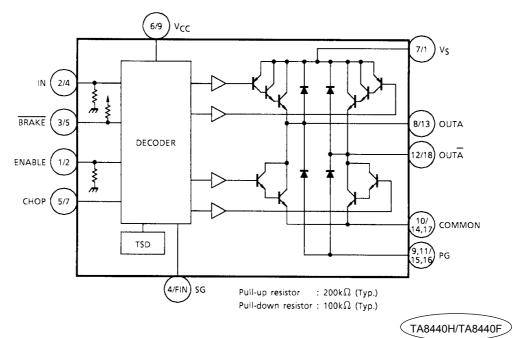
- $\bullet~$ Output current is as large as 1.5A (AVE) and 3.0A (PEAK).
- 4 modes of forward, reverse, stop, and braking are available and a counter-electromotive force absorbing diode has been built-in.
- Thermal shutdown circuit incorporated.
- Input is compatible with CMOS.

BLOCK DIAGRAM

- Built-in input pull-up resistor. BRAKE = $200 \text{ k}\Omega$ (Typ.)
- Built-in input pull-down resistor. IN, ENABLE = $100 \text{ k}\Omega$ (Typ.)



Weight HZIP12-P-1.78B: 4.04 g (Typ.) HSOP20-P-450-1.00: 0.79 g (Typ.)



TA8440F: 3, 6, 8, 10, 11, 12, 19, 20 pin is No Connection.

PIN FUNCTION

Pin No.		SYMBOL	FUNCTIONAL DESCRIPTION				
Н	F	STMBOL	I ONCHONAL DESCRIPTION				
1	2	ENABLE	ENABLE terminal				
2	4	IN	Forward rotation / reverse rotation switch terminal				
3	5	BRAKE	BRAKE terminal				
4	FIN	SG	Signal GND				
5	7	CHOP	PWM signal input terminal				
6	9	V _{CC}	Power voltage supply terminal for control				
7	1	VS	Power voltage supply terminal for motor driver				
8	13	OUTA	Output terminal				
9	15	PG	Power GND				
10	14, 17	COMMON	COMMON terminal				
11	16	PG	Power GND				
12	18	OUTĀ	Output terminal				

TA8440F: 3, 6, 8, 10, 11, 12, 19, 20 pin is No Connection.

FUNCTION

	INF	TUY	OUT	PUT	MODE	
IN	BRAKE	ENABLE	CHOP	OUTA	OUT Ā	MOTOR
н	Н	Н	L	Н	L	CW / CCW
L	Н	Н	L	L	Н	CCW / CW
(*)	(*)	L	(*)	∞	8	Stop
(*)	L	Н	(*)	L	L	Brake
н	Н	Н	Н	∞	L	Chop
L	Н	Н	Н	L	8	Chop

*: Don't care ∞: High impedance

MAXIMUM RATING (Ta = 25°C)

CHARACTERIS	SYMBOL	RATING	UNIT		
Supply Voltage	V _{CC}	7	V		
Supply Voltage	VS	50			
Input Voltage	V _{IN}	-0.3~V _{CC}	V		
Output Current	AVE	I _{O (AVE.)}	1.5	А	
Output Current	PEAK	I _{O (PEAK)}	3.0 (Note 1)	А	
Power Dissipation		PD	2.52 (Note 2)	w	
Power Dissipation		гD	25.0 (Note 3)	vv	
Operating Temperature	T _{opr}	-30~75	°C		
Storage Temperature	T _{stg}	「 _{stg} −55~150			

Note 1: t = 100 ms

Note 2: No heat sink

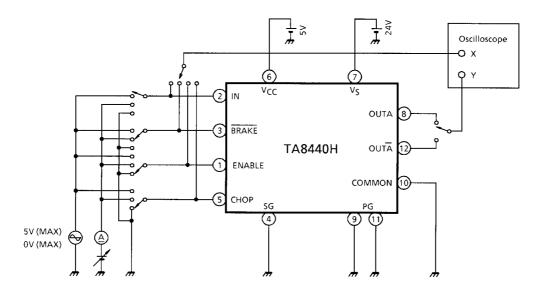
Note 3: Tc = $75^{\circ}C$

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5 V$, $V_S = 24 V$, Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN	TYP.	MAX	UNIT
Input Voltage	High	V _{IN (H)}	- 1	IN, CHOP, ENABLE, BRAKE		3.5	_	V _{CC}	v
	Low	V _{IN (L)}				GND	—	1.5	
Input Current	High	I _{IN-1 (H)}	- 1	CHOP	V _{IN} = 5 V	_	5	52	μA
		I _{IN-2 (H)}		IN, ENABLE		_	40	60	
		I _{IN-3 (H)}		BRAKE		_	0	5.5	
		I _{IN-1 (L)}		CHOP		_	0	5.5	
Input Current	Low	I _{IN-2 (L)}		IN, ENABLE	V _{IN} = 0 V Source type	_	0	5.5	
		I _{IN-3 (L)}		BRAKE		_	25	52	
Current Consumption (I)		I _{CC1}	2	Stop		_	6	10.5	
		I _{CC2}		Forward / reverse		_	10	14.5	mA
		I _{CC3}		Brake		_	14	18.5	
		I _{S1}	2	Stop		_	2	4.2	
Current Consumpt	ion (II)	I _{S2}		Forward / reverse		_	3.5	5.0	mA
		I _{S3}		Brake		_	2.5	3.7	
	Upper Side	V _{sat-U1}		I _{OUT} = 1.5A		1.5	2.0	2.7	V
Output saturation	Under Side	V _{sat-L1}	2			0.7	1.25	1.9	
voltage	Upper Side	V _{sat-U2}	- 3	I _{OUT} = 3.0A		2.7	3.0	3.9	
	Under Side	V _{sat-L2}				1.7	2.0	2.9	
Diode Forward	Upper Side	V _{F-U1}		I _{OUT} = 1.5A		_	3.5	_	v
Orientation Voltage	Under Side	V _{F-L1}				_	1.3	_	
Output Leakage	Upper Side	Іон		V _S = 30V		_	_	200	μA
Current	Under Side	I _{OL}	- 4			_	_	100	
Shut Down Tempe	Shut Down Temperature		_	_		_	170	_	°C
·		t _{pLH}		IN-OUT		_	2.7	_	μs
	t _{pHL}	_	_			1.2	_		
Transfer Time		t _{pLH}		CHOP-OUT		_	0.7	_	
		t _{pHL}	1			_	2.5	_	
		t _{pLH}		ENABLE-OUT		—	2.9	—	
		t _{pHL}	1			_	1.1	—	
		t _{pLH}	1	BRAKE -OUT		—	45	—	
	t _{pHL}	1	_			45	_		

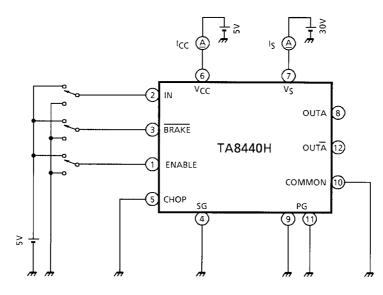
TEST CIRCUIT 1.

 $V_{IN\;(H),}\;V_{IN\;(L)},\;I_{IN\;(H)},\;I_{IN\;(L)}$



TEST CIRCUIT 2.

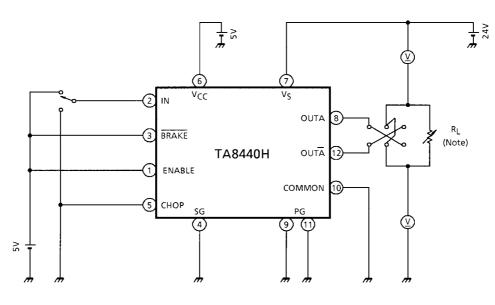
ICC1, ICC2, ICC3, IS1, IS2, IS3



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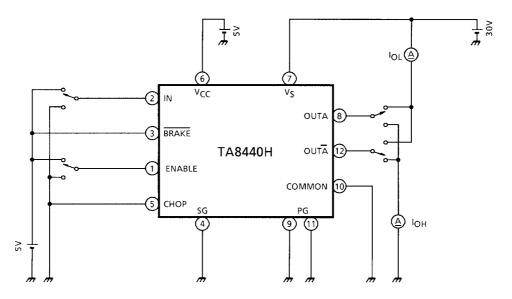
TEST CIRCUIT 3.

V_{sat-L}, V_{sat-U}

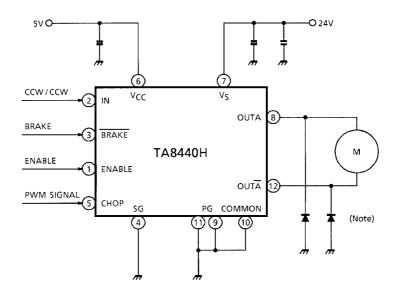


Note: Calibrate I_{OUT} to 1.5 / 3.0 A by R_L.





TOSHIBA APPLICATION CIRCUIT



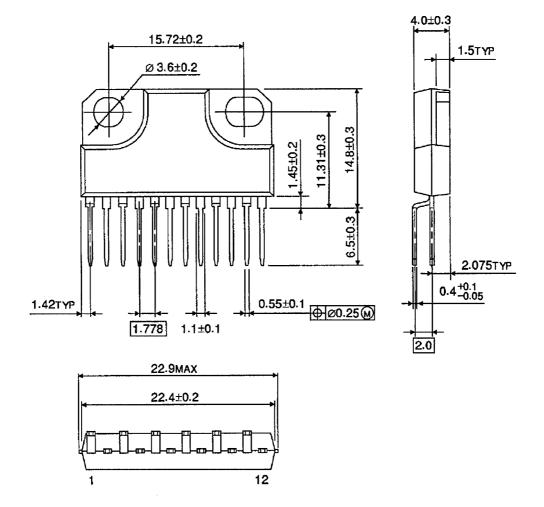
- Note 1: Schottky diode (2GWJ42) to be connected additionally between each output (pin 16 / 19 / 20 / 23) and GND for preventing Punch–Through Current.
- Note 2: Utmost care is necessary in the design of the output line, V_S and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

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PACKAGE DIMENSIONS

HZIP12-P-1.78B

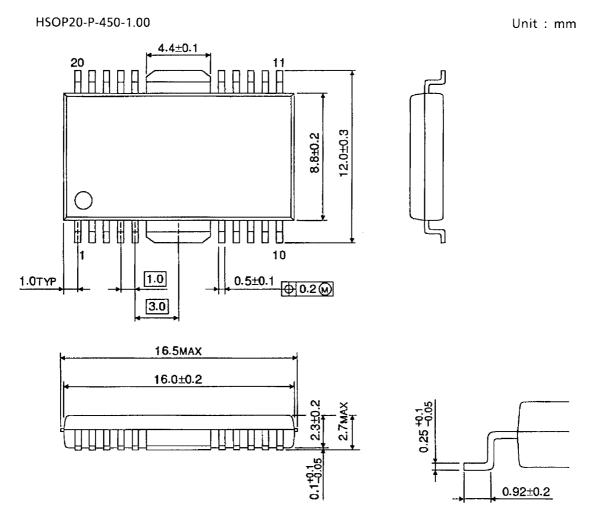
Unit: mm



Weight: 4.04 g (Typ.)

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PACKAGE DIMENSIONS



Weight: 0.79 g (Typ.)

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000707EBA

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