

No.3469

2SK1466

## N-Channel MOS Silicon FET

## Very High-Speed Switching Applications

## Features

- Low ON-state resistance.
  - Very high-speed switching.
  - Converters.

**Absolute Maximum Ratings at  $T_a = 25^\circ\text{C}$**

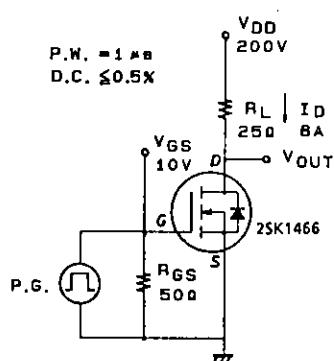
Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$		unit
Drain to Source Voltage	$V_{DSS}$	900 V
Gate to Source Voltage	$V_{GSS}$	$\pm 30$ V
Drain Current(DC)	$I_D$	16 A
Drain Current(Pulse)	$I_{DP}$	PW $\leq 10\mu\text{s}$ , duty cycle $\leq 1\%$ 32 A
Allowable Power Dissipation	$P_D$	T <sub>c</sub> = $25^\circ\text{C}$ 250 W
		3.5 W
Channel Temperature	T <sub>ch</sub>	150 °C
Storage Temperature	T <sub>stg</sub>	-55 to +150 °C

### **Electrical Characteristics at Ta = 25°C**

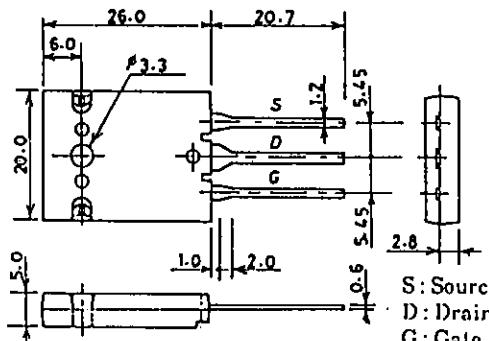
			min	typ	max	unit
D-S Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> =1mA,V <sub>GS</sub> =0	900			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =900V,V <sub>GS</sub> =0		1.0		mA
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V,V <sub>DS</sub> =0			±100	nA
Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =10V,I <sub>D</sub> =1mA	2.0		3.0	V
Forward Transfer Admittance	Y <sub>fs</sub>	V <sub>DS</sub> =20V,I <sub>D</sub> =8A	5.0	10		S
Static Drain to Source on State Resistance	R <sub>D(on)</sub>	I <sub>D</sub> =8A,V <sub>GS</sub> =10V		0.6	0.8	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =20V,f=1MHz	3200			pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =20V,f=1MHz	1000			pF
Reverse Transfer Capacitance	C <sub>rss</sub>	V <sub>DS</sub> =20V,f=1MHz	700			pF
Turn-ON Delay Time	t <sub>d(on)</sub>			35		ns
Rise Time	t <sub>r</sub>			120		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>	I <sub>D</sub> =8A,V <sub>GS</sub> =10V V <sub>DD</sub> =200V,R <sub>GS</sub> =50Ω		850		ns
Fall Time	t <sub>f</sub>			200		ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =16A,V <sub>GS</sub> =0		1.8		V

(Note) Be careful in handling the 2SK1466 because it has no protection diode between gate and source.

### Switching Time Test Circuit



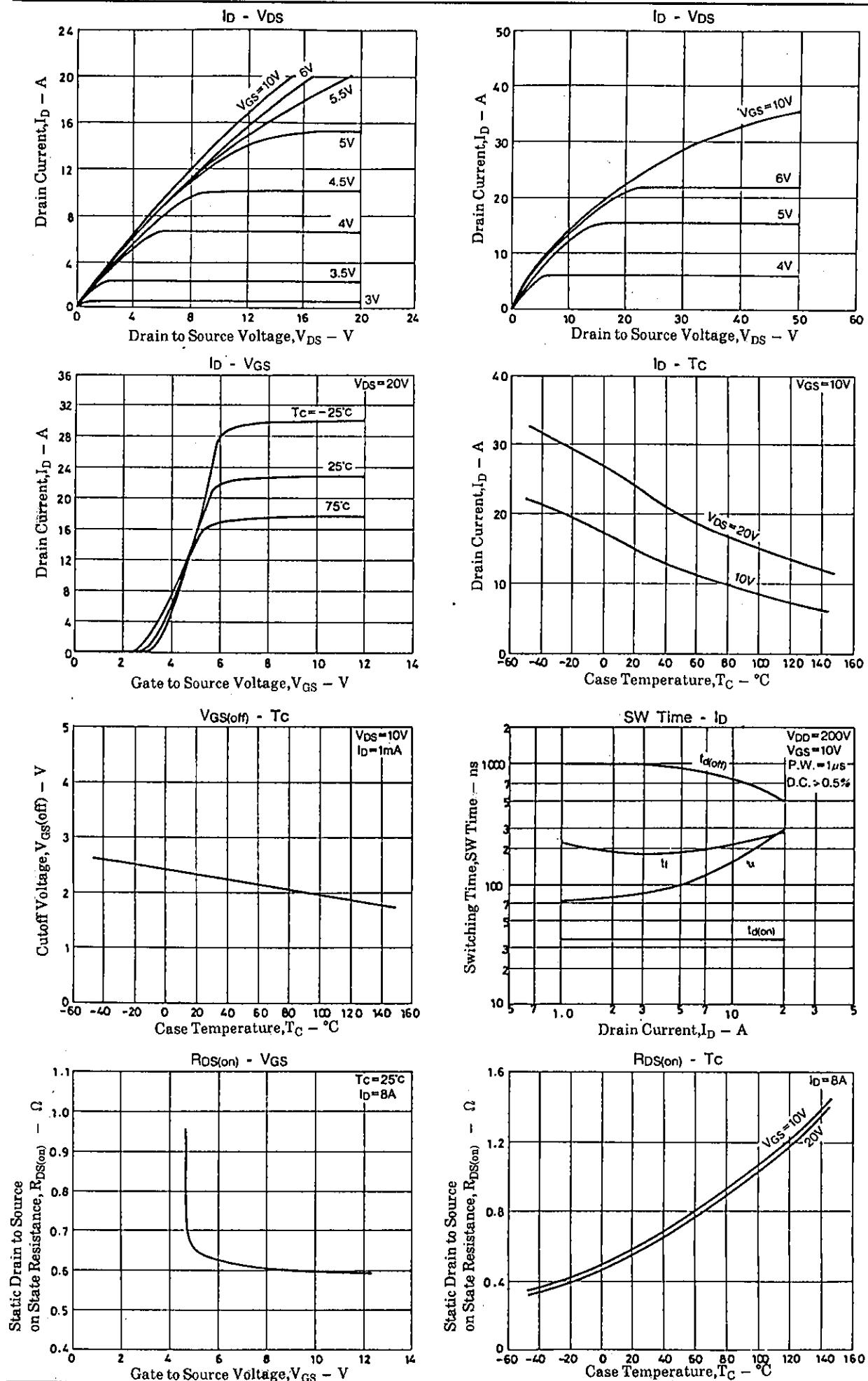
## **Package Dimensions 2077**

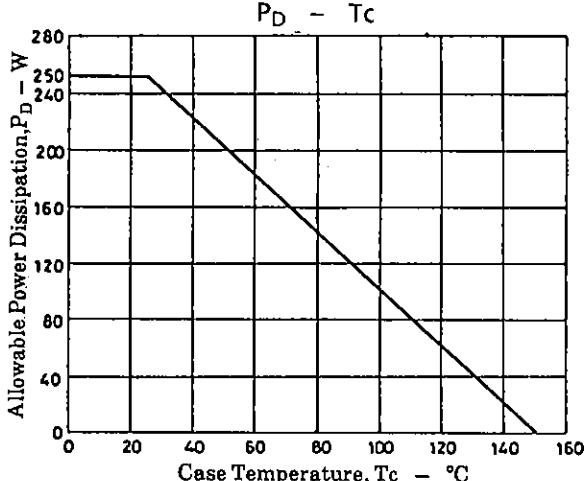
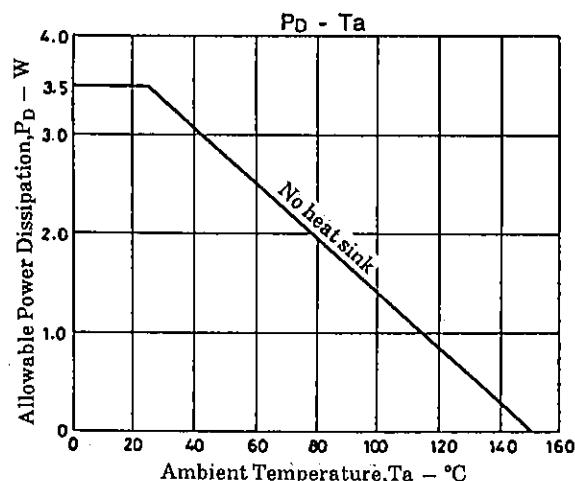
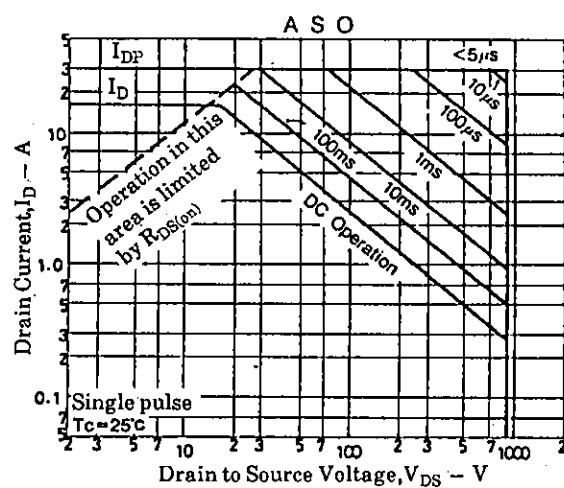
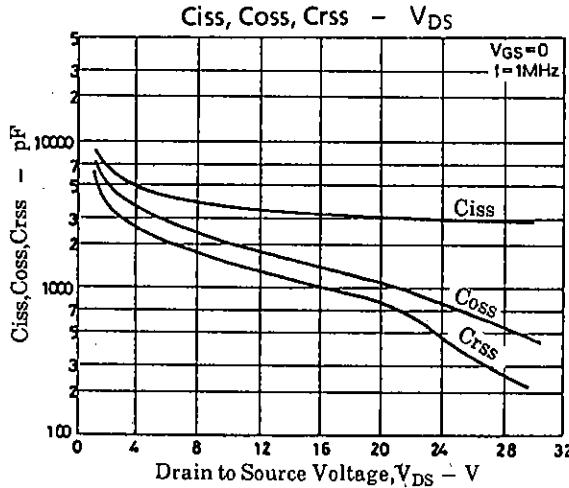
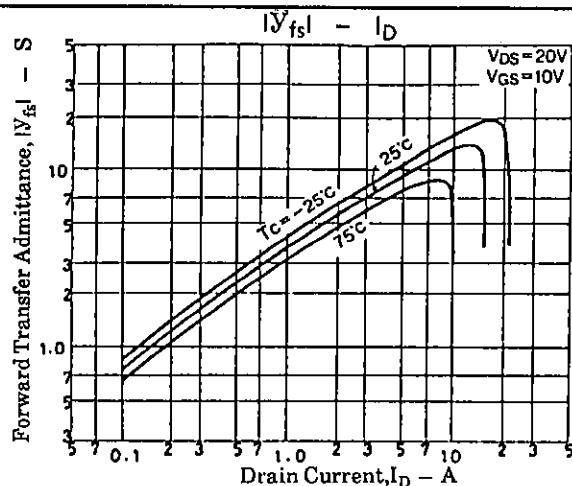
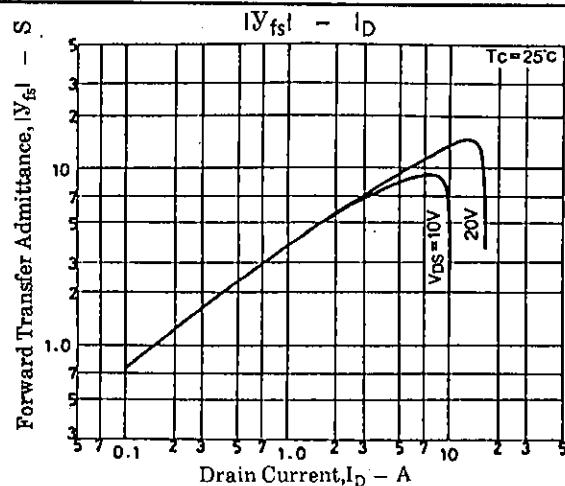


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