TOSHIBA Transistor Silicon NPN Epitaxial Type

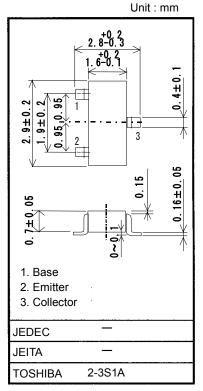
# 2SC6033

High-Speed Swtching Applications DC-DC Converter Applications Storobe Flash Applications

- High DC current gain:  $h_{FE} = 250 \text{ to } 400 \text{ (IC} = 0.3 \text{ A)}$
- Low collector-emitter saturation: VCE (sat) = 0.18 V (max)
- High-speed switching: tf = 38 ns (typ.)

### **Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		$V_{CBO}$	100	V	
Collector-emitter voltage		V <sub>CEX</sub>	80	V	
		V <sub>CEO</sub>	50	V	
Emitter-base voltage		V <sub>EBO</sub>	6	V	
Collector current	DC	Ic	2.5	Α	
	Pulse	I <sub>CP</sub>	5		
Base current		ΙΒ	0.3	Α	
Collector power dissipation	t = 10s	Pc (Note 1)	1.00	W	
	DC	1 C (Note 1)	0.625		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 0.01g (Typ.)

Note 1: Mounted on an FR4 board (glass epoxy, 1.6mm thick, Cu area: 64.5 mm²)

## **Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I <sub>CBO</sub>	$V_{CB} = 100 \text{ V}, I_E = 0$	_	_	0.1	μΑ
Emitter cut-off current		I <sub>EBO</sub>	$V_{EB} = 6 V, I_{C} = 0$	_	_	0.1	μΑ
Collector-emitter breakdown voltage		V (BR) CEO	$I_C = 10 \text{ mA}, I_B = 0$	50	_	_	V
DC current gain		h <sub>FE</sub> (1)	$V_{CE} = 2 \text{ V}, I_{C} = 0.3 \text{ A}$	250	_	400	
		h <sub>FE</sub> (2)	$V_{CE} = 2 \text{ V}, I_{C} = 1.0 \text{ A}$	120	_	_	
Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 33 mA	_	_	0.18	V
Base-emitter saturation voltage		V <sub>BE (sat)</sub>	I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 33 mA	_	_	1.10	V
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1MHz	_	18	_	pF
Switching time	Rise time	t <sub>r</sub>	See Figure 1. $V_{CC} \simeq 20 \text{ V, R}_L = 20 \Omega$ $I_{B1} = -I_{B2} = 33 \text{ mA}$	_	25	_	ns
	Storage time	t <sub>stg</sub>		_	470	_	
	Fall time	t <sub>f</sub>		_	38	_	

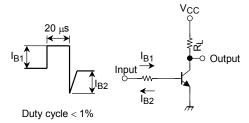
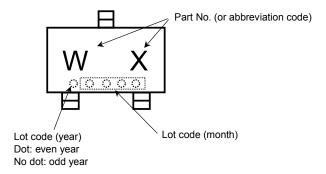
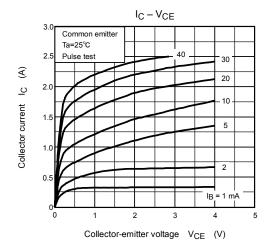


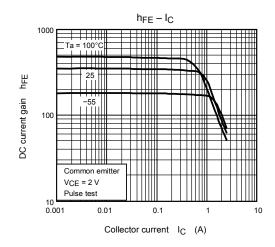
Figure 1 Switching Time Test Circuit & Timing Chart

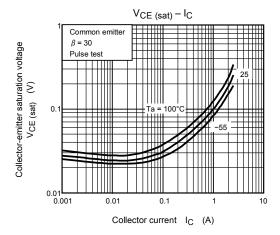
## Marking

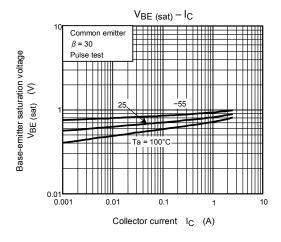


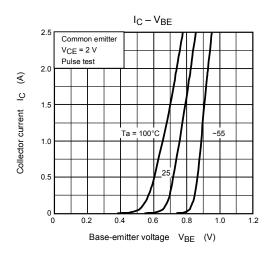
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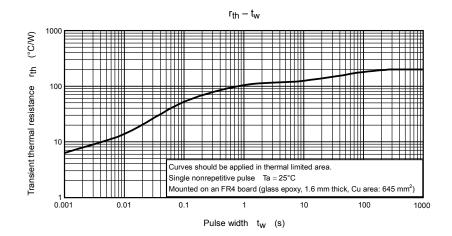


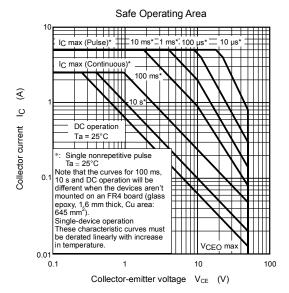






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