Silicon N-Channel MOS FET

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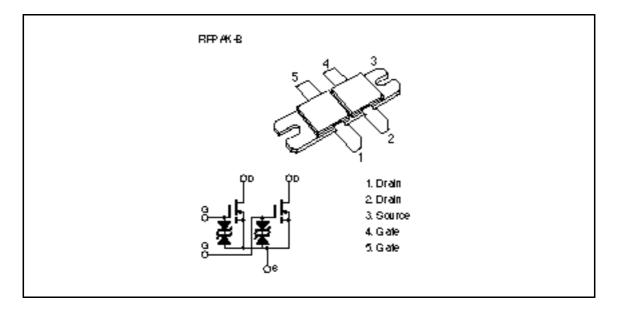
Application

VHF amplifier

Features

- High gain, high efficiency $PG = 15 \ dB, \quad D = 65\% \ typ \ (f = 200 \ MHz)$
- Compact package Suitable for push - pull circuit

Outline





Absolute Maximum Ratings (Ta = 25° C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	120	V	
Gate to source voltage	V _{GSS}	±20	V	
Drain current	I _D	12	А	
Channel dissipation	Pch*1	180	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

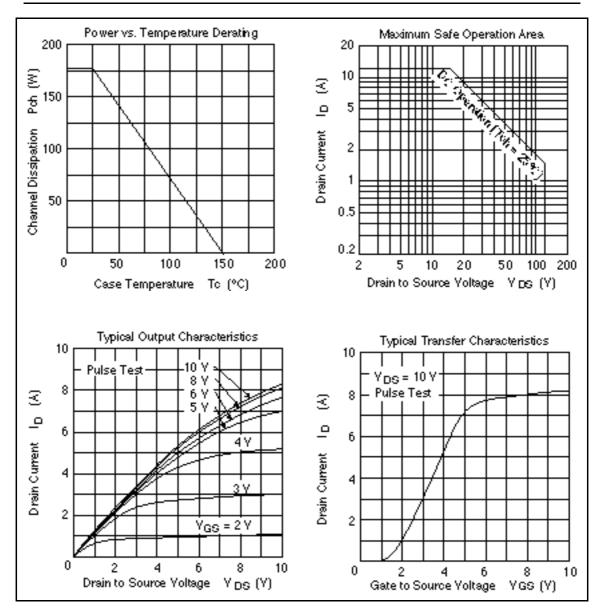
Note: 1. Value at $T_c = 25^{\circ}C$

Electrical Characteristics (Ta = 25°C)

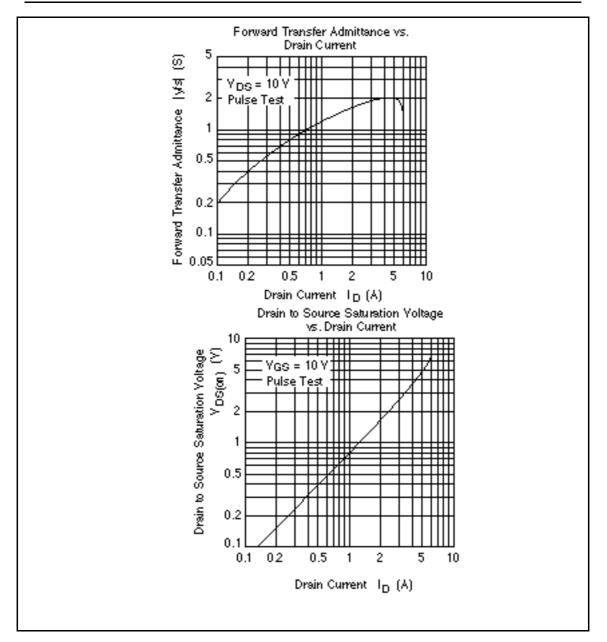
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage*1	$V_{(BR)DSS}$	120	_	_	V	$I_{\rm D} = 1$ mA, $V_{\rm GS} = 0$
Gate to source breakdown voltage*1	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current*1	I _{DSS}	_	_	0.5	mA	$V_{\rm DS} = 100 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage*1	$V_{\text{GS(off)}}$	0.5	_	2.0	V	$I_{\rm D}$ = 1 mA, $V_{\rm DS}$ = 10 V
Drain to source cutoff voltage*1	$V_{\text{DS(on)}}$	_	2.7	3.5	V	$I_{\rm D} = 3 \text{ A}, V_{\rm GS} = 10 \text{ V}^{*2}$
Forward transfer admittance*1	y _{fs}	1.5	1.8	_	S	$I_{\rm D}$ = 2.5 A, $V_{\rm DS}$ = 10 V* ²
Input capacitance*1	Ciss	—	185	_	pF	$V_{GS} = 5 V, V_{DS} = 0$ f = 1 MHz
Output capacitance*1	Coss	—	60	_	pF	$V_{DS} = 50 V, V_{GS} = 0$ f = 1 MHz
Output Power	Po	150	180	_	W	$V_{\text{DS}} = 60 \text{ V}, \text{ I}_{\text{DQ}} = 0.2 \text{ A}$
Drain Efficiency	D	_	65	_	%	f = 200 MHz, Pin = 5 W

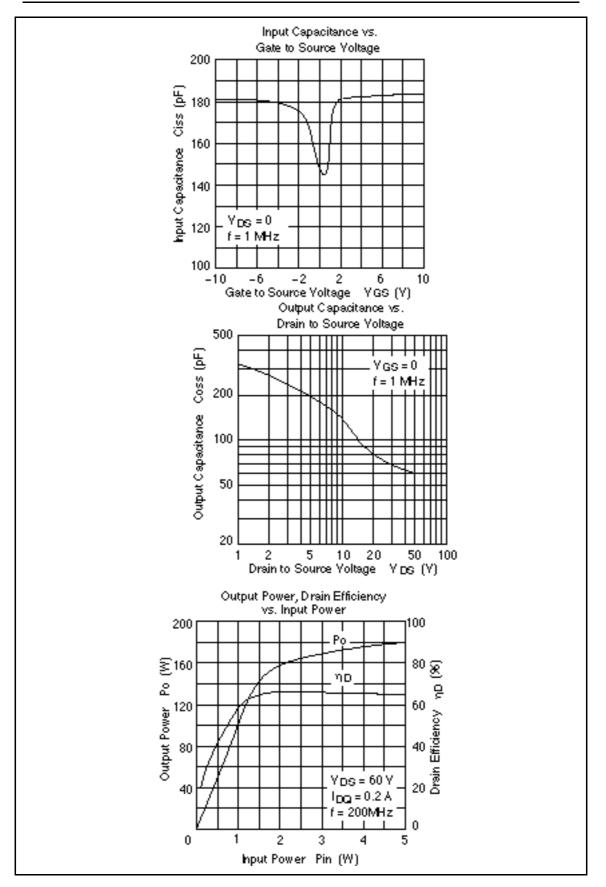
Notes: 1. Shows / unit FET

2. Pulse Test



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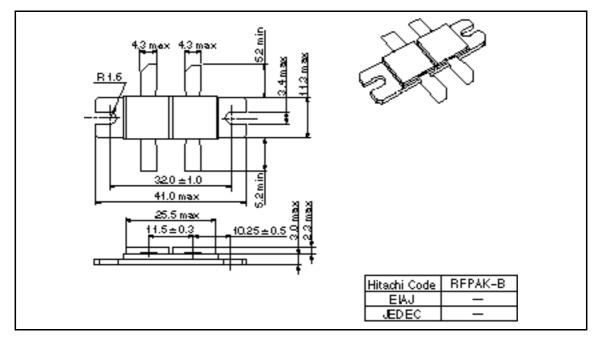


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Package Dimensions

Unit: mm



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