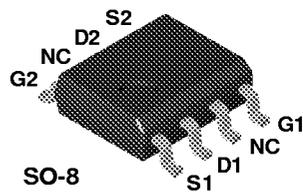


**NPDS5911
NPDS5912**



N-Channel General Purpose Dual Amplifier

Sourced from Process 93.

Absolute Maximum Ratings*

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{DG}	Drain-Gate Voltage	25	V
V _{GS}	Gate-Source Voltage	25	V
I _{GF}	Forward Gate Current	10	mA
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

General Purpose Dual Amplifier
(continued)

NPDS5911 / NPDS5912

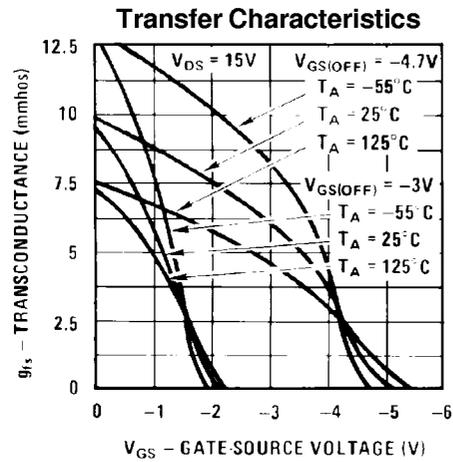
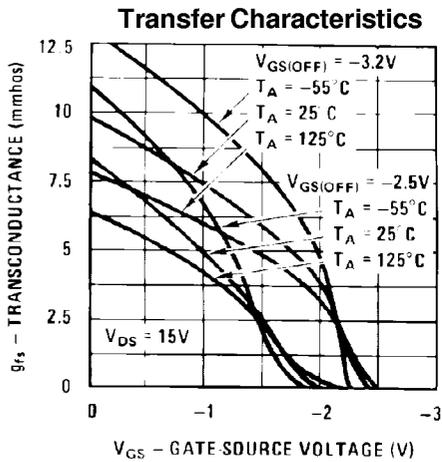
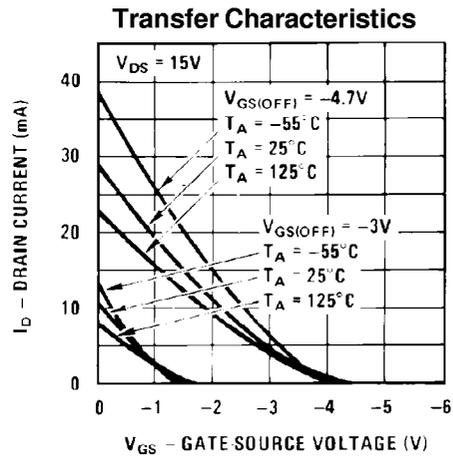
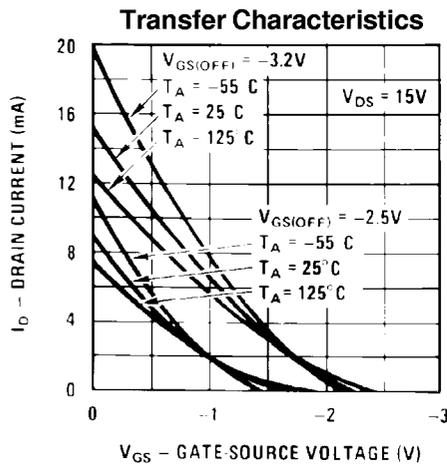
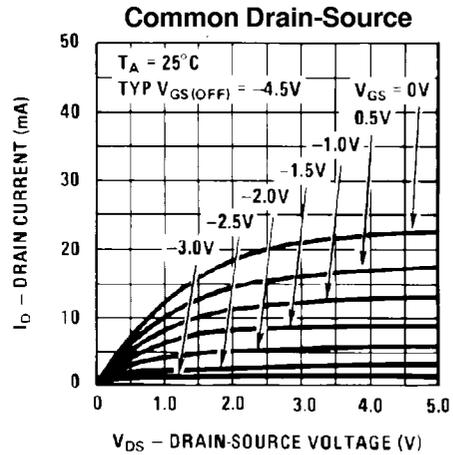
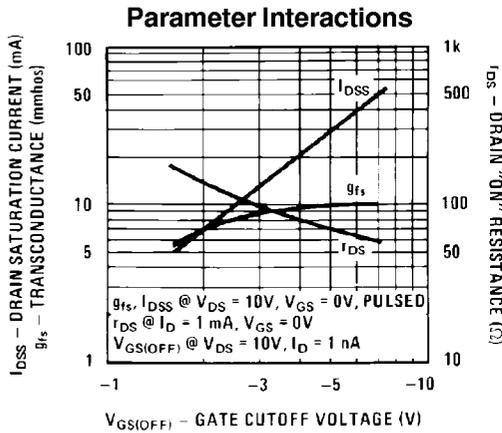
Electrical Characteristics

TA = 25 °C unless otherwise noted

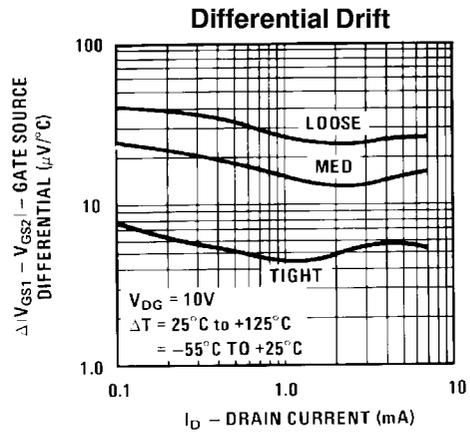
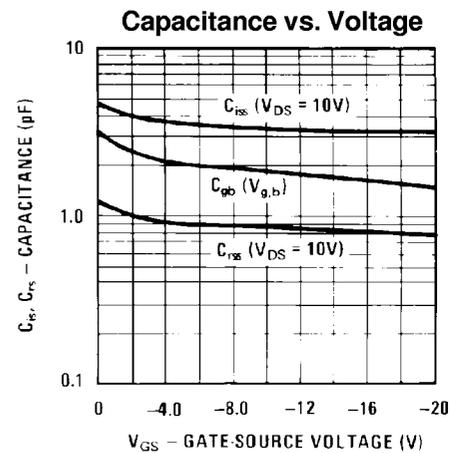
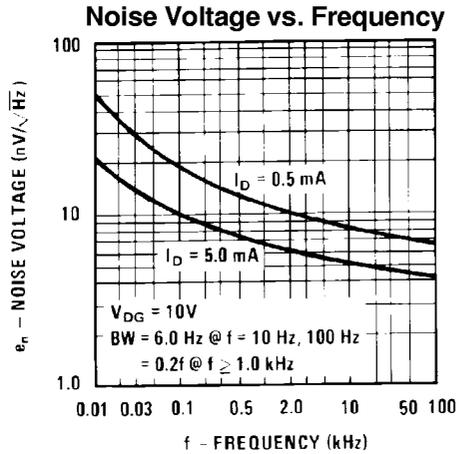
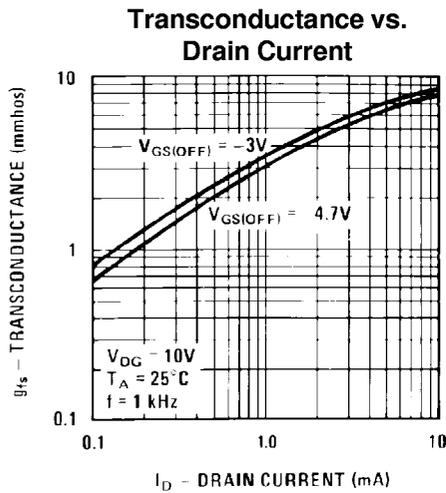
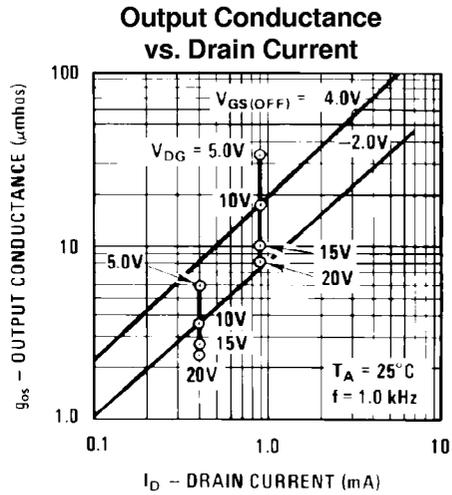
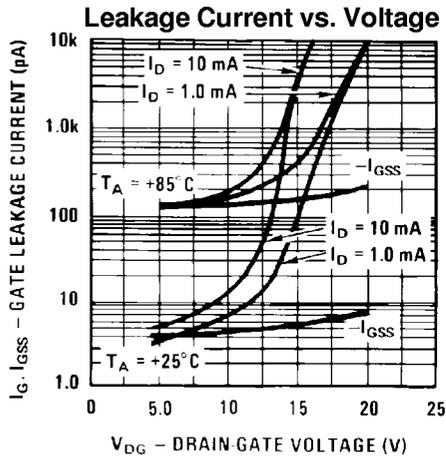
Symbol	Parameter	Test Conditions	Min	Max	Units
OFF CHARACTERISTICS					
$V_{(BR)GSS}$	Gate-Source Breakdown Voltage	$I_G = 1.0 \mu A, V_{DS} = 0$	- 25		V
I_{GSS}	Gate Reverse Current	$V_{GS} = 15 V, V_{DS} = 0$ $V_{GS} = 15 V, V_{DS} = 0, T_A = 150 \text{ }^\circ\text{C}$		100 250	pA nA
$V_{GS(off)}$	Gate-Source Cutoff Voltage	$V_{DS} = 10 V, I_D = 1.0 \text{ nA}$	- 1.0	- 5.0	V
V_{GS}	Gate-Source Voltage	$V_{DG} = 10 V, I_D = 5.0 \text{ mA}$	- 0.3	- 4.0	V
$V_{G1 - G2}$	Voltage Gate 1 - Gate 2	$V_{DS} = 0, I_G = + / - 1.0 \mu A$	+ / - 25		V
ON CHARACTERISTICS					
I_{DSS}	Zero-Gate Voltage Drain Current*	$V_{DS} = 10 V, V_{GS} = 0$	7.0	40	mA
SMALL SIGNAL CHARACTERISTICS					
g_s	Common Source Forward Transconductance	$V_{DS} = 10 V, I_D = 5.0 \text{ mA}, f = 1.0 \text{ kHz}$ $V_{DS} = 10 V, I_D = 5.0 \text{ mA}, f = 100 \text{ MHz}$	5000 5000	10,000 10,000	μmhos μmhos
g_{oss}	Common Source Output Conductance	$V_{DS} = 10 V, I_D = 5.0 \text{ mA}, f = 1.0 \text{ kHz}$ $V_{DS} = 10 V, I_D = 5.0 \text{ mA}, f = 100 \text{ MHz}$		100 150	μmhos μmhos
C_{iss}	Input Capacitance	$V_{DG} = 10 V, I_D = 5.0 \text{ mA}, f = 1.0 \text{ MHz}$		5.0	pF
C_{riss}	Reverse Transfer Capacitance	$V_{DS} = 10 V, I_D = 5.0 \text{ mA}, f = 1.0 \text{ kHz}$		1.2	pF
e_n	Equivalent Short-Circuit Input Noise Voltage	$V_{DG} = 10 V, I_D = 5.0 \text{ mA}, f = 10 \text{ kHz}$		20	$nV/\sqrt{\text{Hz}}$
NF	Noise Figure	$V_{DG} = 10 V, I_D = 5.0 \text{ mA}, f = 10 \text{ kHz}$ $R_G = 100 \text{ k}\Omega$		1.0	dB
$I_{DSS1} - I_{DSS2}$	I_{DSS} Match	$V_{DS} = 10 V, V_{GS} = 0$		5.0	%
$g_{s1} - g_{s2}$	g_s Match	$V_{DS} = 10 V, I_D = 5.0 \text{ mA}, f = 1.0 \text{ kHz}$		5.0	%
$g_{oss1} - g_{oss2}$	g_{oss} Match	$V_{DS} = 10 V, I_D = 5.0 \text{ mA}, f = 1.0 \text{ kHz}$		20	μmhos
$I_{G1} - I_{G2}$	I_G Match	$V_{DS} = 10 V, I_D = 5.0 \text{ mA}, T_A = 125 \text{ }^\circ\text{C}$		20	nA
$V_{GS1} - V_{GS2}$	Differential Match	$V_{DG} = 10 V, I_D = 5.0 \text{ mA},$ NPDS5911 NPDS5912		10 15	mV mV
$\Delta V_{GS1} - V_{GS2}$	Differential Drift	$V_{DG} = 10 V, V_{GS} = 0, I_D = 5.0 \text{ mA},$ $T_A = 25 \text{ to } 125 \text{ }^\circ\text{C}$ NPDS5911 NPDS5912 $V_{DG} = 10 V, I_D = 5.0 \text{ mA},$ $T_A = -55 \text{ to } 25 \text{ }^\circ\text{C}$ NPDS5911 NPDS5912		20 40 20 40	$\mu\text{V}/^\circ\text{C}$ $\mu\text{V}/^\circ\text{C}$ $\mu\text{V}/^\circ\text{C}$ $\mu\text{V}/^\circ\text{C}$

*Pulse Test: Pulse Width \leq 300 ms, Duty Cycle \leq 2%

Typical Characteristics (continued)



Typical Characteristics (continued)



Typical Characteristics (continued)

