

# PC957L0NSZ

※ VDE (VDE0884) approved type is also available as an option

## ■ Features

1. High resistance to noise (CMR:MIN. 15kV/μs)
2. High speed response  
( $t_{PHL}$ :MAX. 0.8μs,  $t_{PLH}$ :MAX. 0.8μs)
3. Standard DIP type
4. Isolation voltage ( $V_{iso(rms)}$ =5.0kV)
5. Recognized by UL, file No. E64380 (model No. **PC957L**)

## ■ Applications

1. Programmable controller
2. Inverter

## ■ Absolute Maximum Ratings ( $T_a=25^{\circ}\text{C}$ )

	Parameter	Symbol	Rating	Unit
Input	<sup>*1</sup> Forward current	$I_F$	25	mA
	Reverse voltage	$V_R$	5	V
Output	<sup>*2</sup> Power dissipation	P	45	mW
	Output current	$I_O$	8	mA
	Supply voltage	$V_{CC}$	-0.5 to +30	V
	Output voltage	$V_O$	-0.5 to +20	V
	<sup>*3</sup> Power dissipation	$P_O$	100	mW
<sup>*4</sup>	Isolation voltage	$V_{iso(rms)}$	5.0	kV
	Operating temperature	$T_{opr}$	-55 to +100	°C
	Storage temperature	$T_{stg}$	-55 to +125	°C
<sup>*5</sup>	Soldering temperature	$T_{sol}$	270	°C

<sup>\*1</sup> When ambient temperature goes above 70°C, the power dissipation goes down at 0.8mA/°C

<sup>\*2</sup> When ambient temperature goes above 70°C, the power dissipation goes down at 0.8mW/°C

<sup>\*3</sup> When ambient temperature goes above 70°C, the power dissipation goes down at 1.9mW/°C

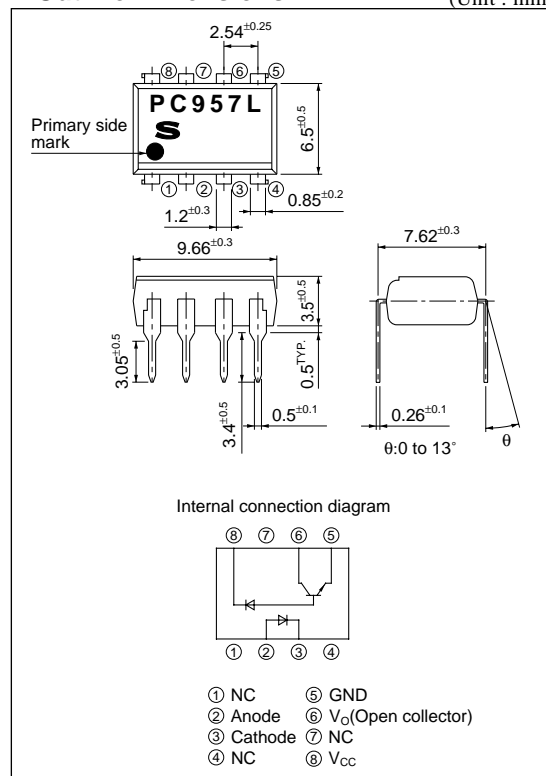
<sup>\*4</sup> 40 to 60%RH, AC for 1minute

<sup>\*5</sup> For 10s

## High Speed and High CMR \*OPIC Photocoupler

## ■ Outline Dimensions

(Unit : mm)



\* "OPIC" (Optical IC) is a trademark of the SHARP Corporation.  
An OPIC consists of a light-detecting element and signal-processing circuit integrated onto a single chip.

## ■ Electro-optical Characteristics \*6

(Unless otherwise specified Ta=0 to +70°C)

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input	Forward voltage	$V_F$	$T_a=25^\circ\text{C}$ , $I_F=16\text{mA}$	—	1.7	1.95	V
	Reverse current	$I_R$	$T_a=25^\circ\text{C}$ , $V_R=5\text{V}$	—	—	10	$\mu\text{A}$
	Terminal capacitance	$C_t$	$T_a=25^\circ\text{C}$ , $V_F=0$ , $f=1\text{MHz}$	—	60	250	pF
Output	High level output current (1)	$I_{OH(1)}$	$T_a=25^\circ\text{C}$ , $I_F=0$ , $V_{CC}=V_O=5.5\text{V}$	—	3	500	nA
	High level output current (2)	$I_{OH(2)}$	$T_a=25^\circ\text{C}$ , $I_F=0$ , $V_{CC}=V_O=15\text{V}$	—	0.01	1	$\mu\text{A}$
	High level output current (3)	$I_{OH(3)}$	$I_F=0$ , $V_{CC}=V_O=15\text{V}$	—	—	50	$\mu\text{A}$
	Low level output voltage	$V_{OL}$	$I_F=16\text{mA}$ , $V_{CC}=4.5\text{V}$ , $I_O=2.4\text{mA}$	—	0.1	0.4	V
	Low level supply current	$I_{CCL}$	$I_F=16\text{mA}$ , $V_{CC}=15\text{V}$ , $V_O=\text{open}$	—	120	—	$\mu\text{A}$
	High level supply current (1)	$I_{CCH(1)}$	$T_a=25^\circ\text{C}$ , $I_F=0$ , $V_{CC}=15\text{V}$ , $V_O=\text{open}$	—	0.02	1	$\mu\text{A}$
	High level supply current (2)	$I_{CCH(2)}$	$I_F=0$ , $V_{CC}=15\text{V}$ , $V_O=\text{open}$	—	—	2	$\mu\text{A}$
	Current transfer ratio (1)	CTR (1)	$T_a=25^\circ\text{C}$ , $I_F=16\text{mA}$ , $V_{CC}=4.5\text{V}$ , $V_O=0.4\text{V}$	19	—	50	%
Transfer characteristics	Current transfer ratio (2)	CTR (2)	$I_F=16\text{mA}$ , $V_{CC}=4.5\text{V}$ , $V_O=0.4\text{V}$	15	—	—	%
	Isolation resistance	$R_{ISO}$	$T_a=25^\circ\text{C}$ , $\text{DC}=500\text{V}$ , 40 to 60% RH	$5 \times 10^{10}$	$1 \times 10^{11}$	—	$\Omega$
	Floating capacitance	$C_f$	$T_a=25^\circ\text{C}$ , $V=0$ , $f=1\text{MHz}$	—	0.6	1	pF
	*7 "High→Low" propagation delay time	$t_{PHL}$	$T_a=25^\circ\text{C}$ , $V_{CC}=5\text{V}$	—	0.2	0.8	$\mu\text{s}$
	*7 "Low→High" propagation delay time	$t_{PLH}$	$I_F=16\text{mA}$ , $R_L=1.9\Omega$	—	0.6	0.8	$\mu\text{s}$
	*8 Instantaneous common mode rejection voltage "Output : High level"	$CM_H$	$T_a=25^\circ\text{C}$ , $I_F=0$ , $V_{CC}=5\text{V}$ $V_{CM(p-p)}=1.0\text{kV}$ , $R_L=1.9\text{k}\Omega$	15	30	—	$\text{kV}/\mu\text{s}$
	*8 Instantaneous common mode rejection voltage "Output : Low level"	$CM_L$	$T_a=25^\circ\text{C}$ , $I_F=16\text{mA}$ , $V_{CC}=5\text{V}$ $V_{CM(p-p)}=1.0\text{kV}$ , $R_L=1.9\text{k}\Omega$	−15	−30	—	$\text{kV}/\mu\text{s}$

\*6 When measuring output and transfer characteristics, connect a by-pass capacitor (0.01 $\mu\text{F}$  or more) between  $V_{CC}$  ⑧ and GND ⑤ near the device

\*7 Refer to Fig.1

\*8 Refer to Fig.2

Fig.1 Test Circuit for Propagation Delay Time

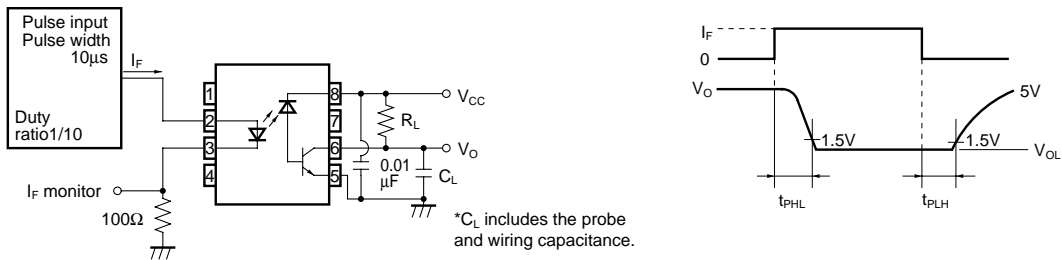
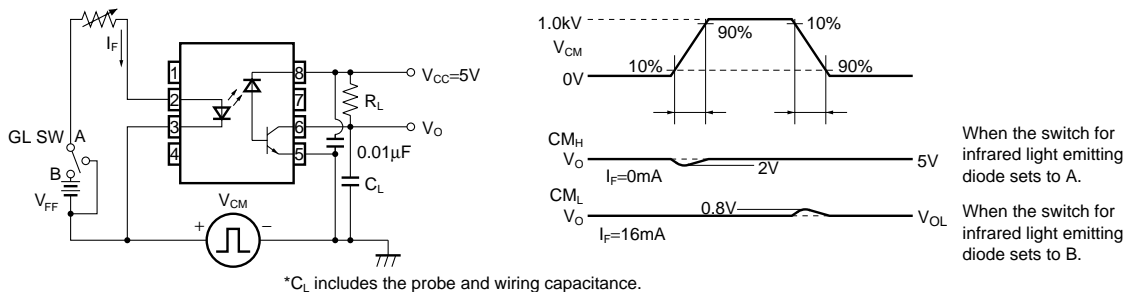
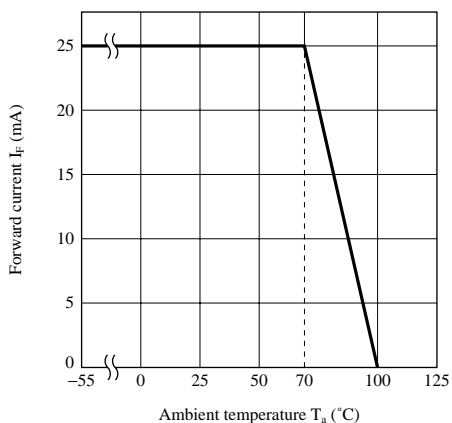


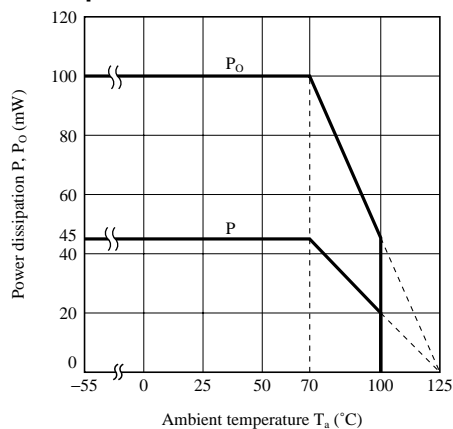
Fig.2 Test Circuit for Instantaneous Common Mode Rejection Voltage



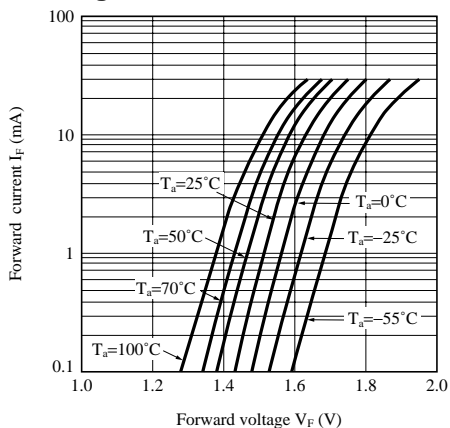
**Fig.3 Forward Current vs. Ambient Temperature**



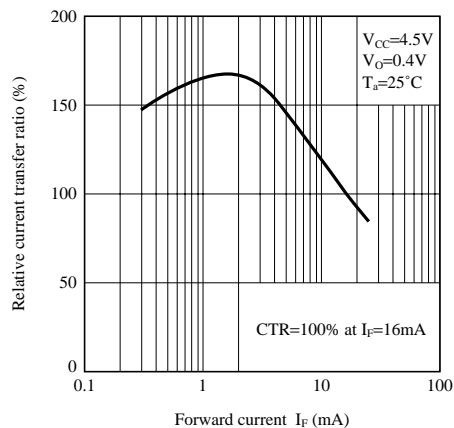
**Fig.4 Power Dissipation vs. Ambient Temperature**



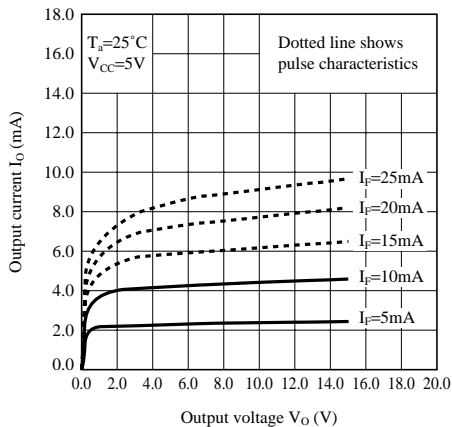
**Fig.5 Forward Current vs. Forward Voltage**



**Fig.6 Relative Current Transfer Ratio vs. Forward Current**



**Fig.7 Output Current vs. Output Voltage**



**Fig.8 Relative Current Transfer Ratio vs. Ambient Temperature**

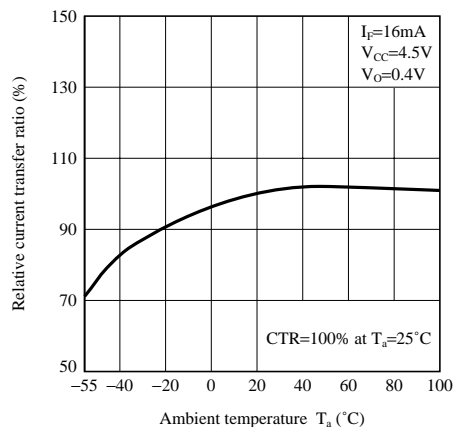


Fig.9 High Level Output Current vs. Ambient temperature

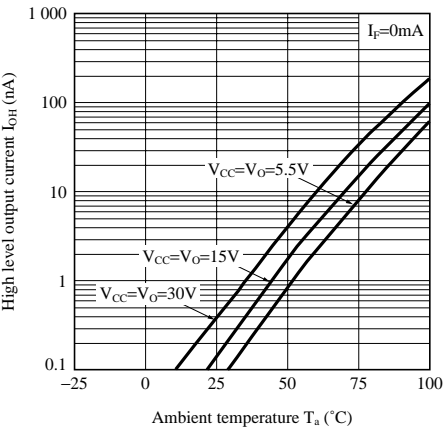
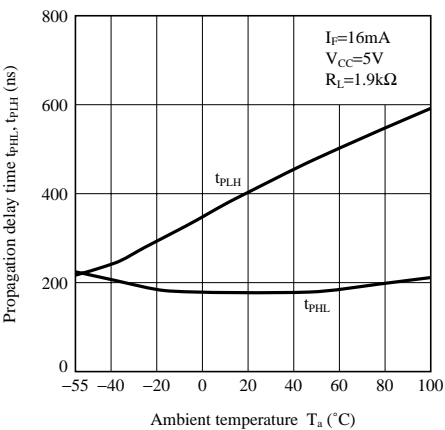


Fig.10 Propagation Delay Time vs. Ambient Temperature



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