

□ MN1871675

Type		MN1871675	
ROM (x8-bit)		16K	
RAM (x8-bit)		320	
Minimum Instruction Execution Time		2/3 dividing 0.5 μ s (at 4.5 to 5.5V, 12MHz)	
Interrupts		• RESET • External 0 • External 1 • Timer 0 • Timer 1 • Timer 2 • Remote Control • Line 21 • COSD	
Timer Counter		Timer Counter 0 : 8-bit x 1 Clock Source1/1, 1/4, 1/16, 1/64 of System Clock Interrupt SourceOverflow of Timer Counter 0 Timer Counter 1 : 8-bit x 1 Clock Source1/2, 1/16, 1/64, 1/256, 1/512 of System Clock Interrupt SourceOverflow of Timer Counter 1 Time Base Counter Clock Source1/4096 of System Clock Interrupt Source1/1, 1/2, 1/4, 1/8 of Timer Counter 2 Watchdog	
I/O Pins	I/O	21	• Common use : 3
	Input	1	• Common use : 1
	High Voltage Output	7	• Nch Open-drain (Breakdown Voltage 12V) : 7
A/D Inputs		5-bit x 7ch (without S/H)	
PWM		14-bit x 1ch (Repetition Cycle 16 μ s, at 12MHz), 8-bit x 8ch (Repetition Cycle 32 μ s, at 12MHz), 7-bit x 1ch (Repetition Cycle 16 μ s, at 12MHz)	
Special Ports		Remote Control Reception	
CRTC		Single OSD built-in (Caption OSD : 12 x 26, 144 letters)	
Notes		Remote Control Data Detection Circuit built-in	
Package		SDIP064-P-0750	

Electrical Characteristics

A/D Converter Characteristics

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
A/D Conversion Time	TAD	fosc=12MHz	9			μ s
Analog Input Voltage	VAD		VSS		VDD	V

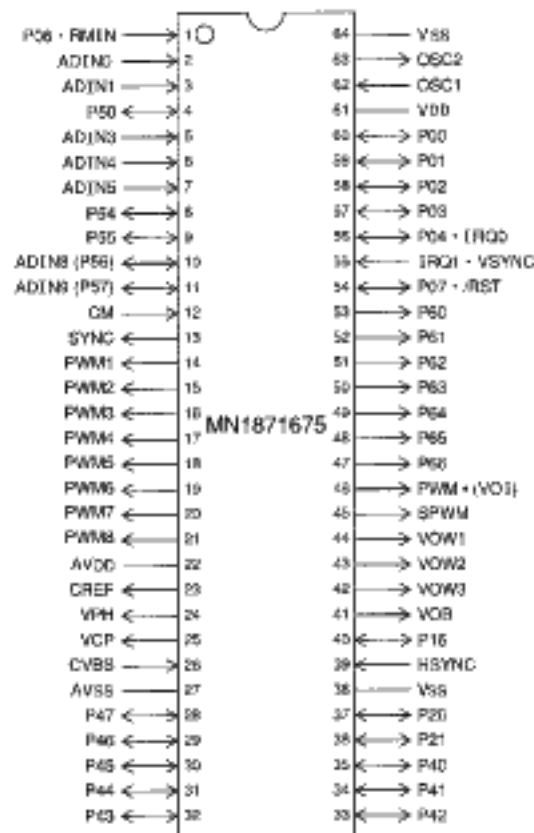
(Ta= -20 to +70°C, VDD=5.0V, VSS=0V)

Support Tool

■ **In-Circuit Emulator** PX-JCE1870 / 80 + PX-PRB1876476

■ **EPROM built-in Type** Use **MN18P76476** in SOIP064-P-0750 package.

Pin Assignment



SOIP064-P-0750