

2SK1296

Silicon N-Channel MOS FET

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device
 - Can be driven from 5 V source
- Suitable for motor drive, DC-DC converter, power switch and solenoid drive

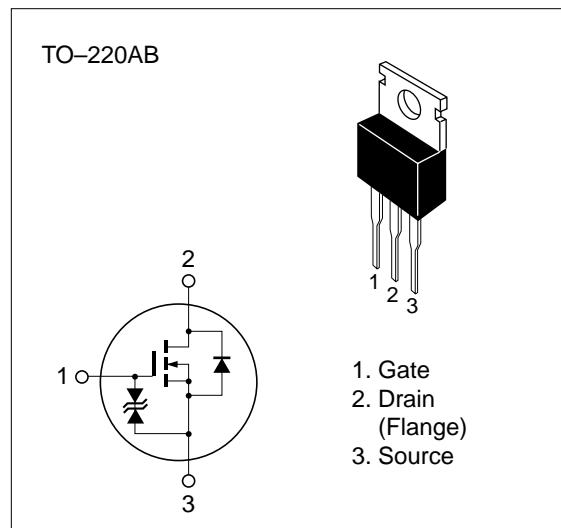


Table 1 Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	60	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	30	A
Drain peak current	I _{D(pulse)} *	120	A
Body to drain diode reverse drain current	I _{DR}	30	A
Channel dissipation	P _{ch} **	75	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	–55 to +150	°C

* PW ≤ 10 µs, duty cycle ≤ 1 %

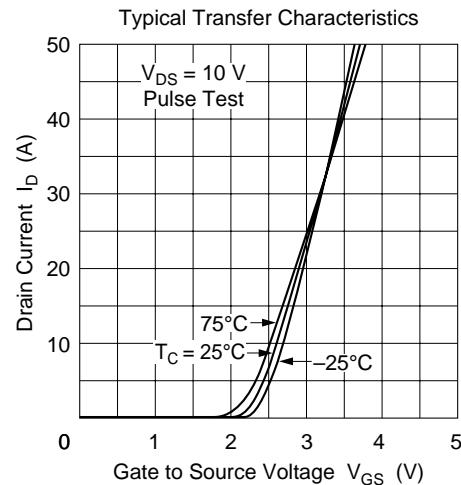
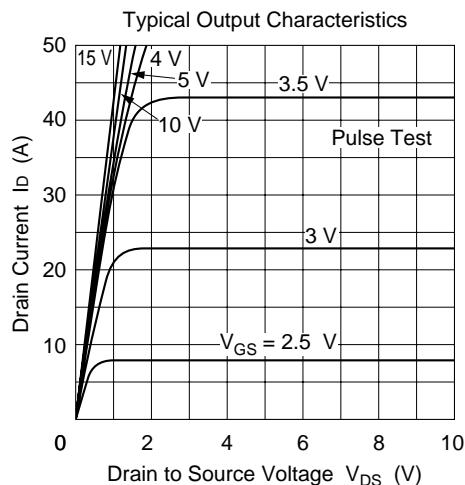
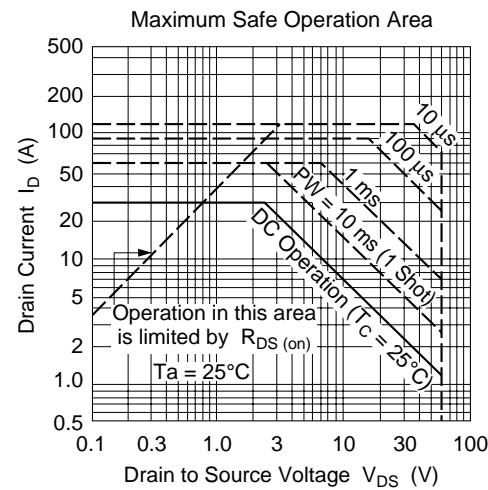
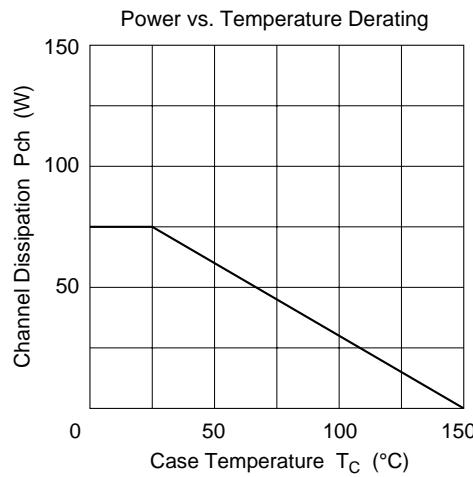
** Value at T_C = 25 °C

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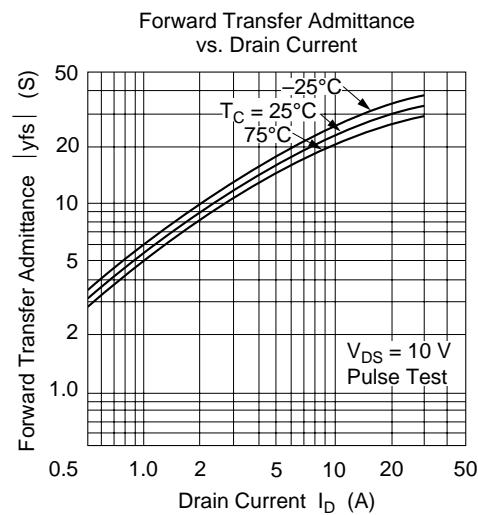
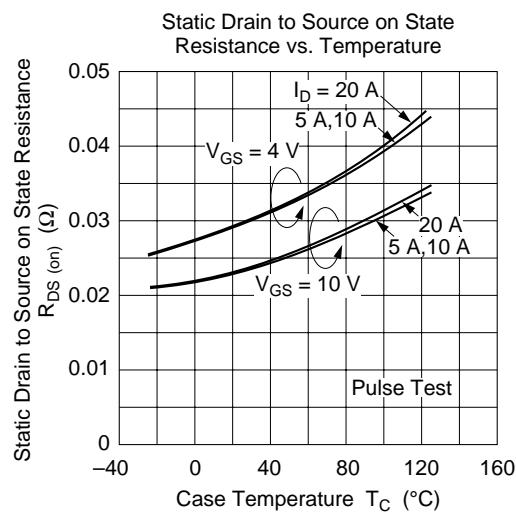
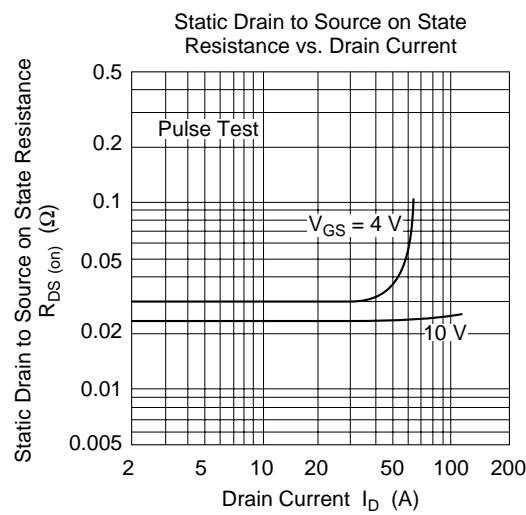
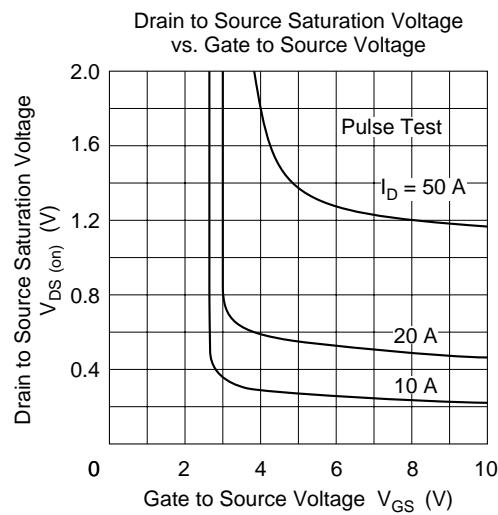
Table 2 Electrical Characteristics (Ta = 25°C)

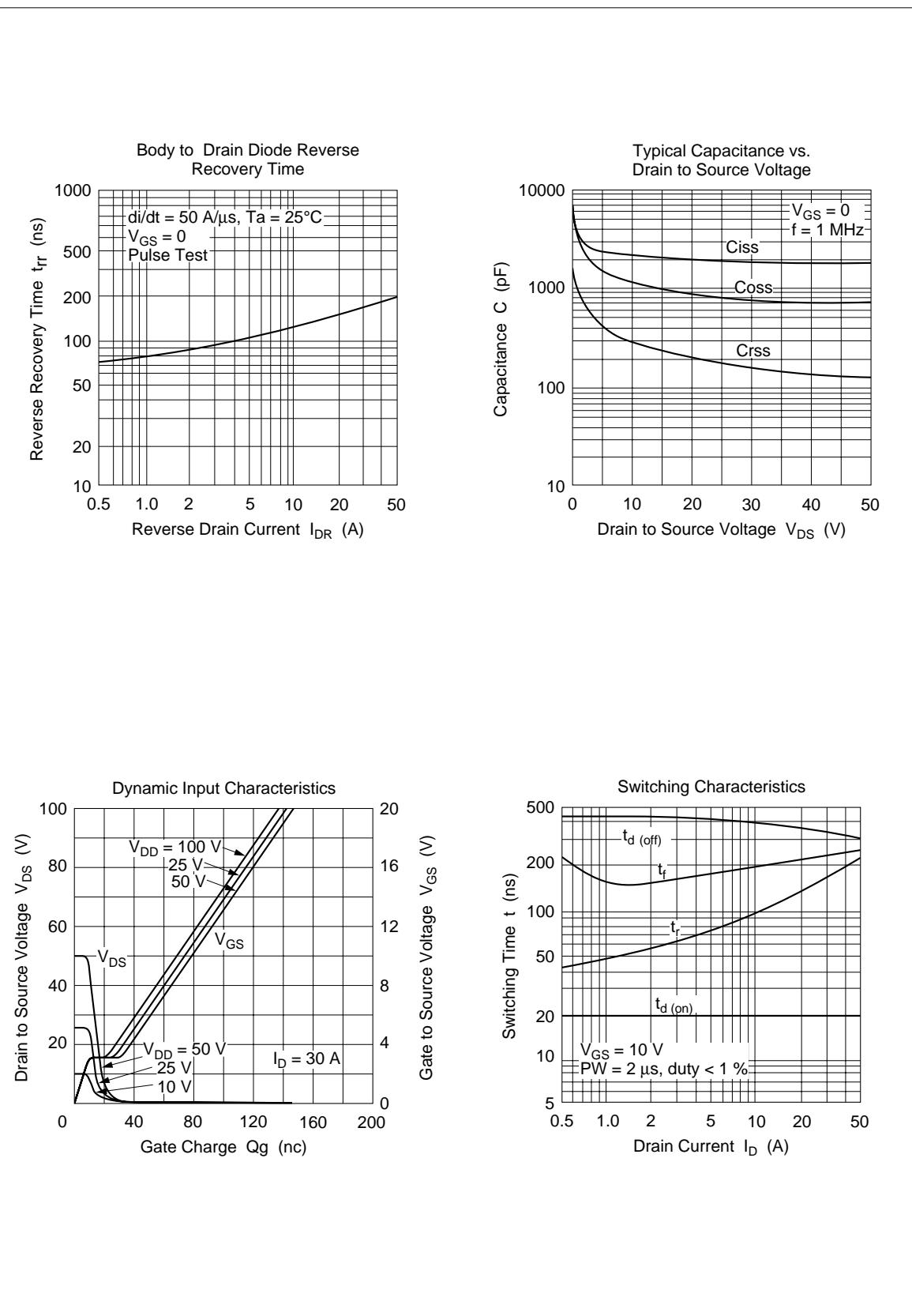
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	60	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	I _G = ±100 µA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	µA	V _{GS} = ±16 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	250	µA	V _{DS} = 50 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	1.0	—	2.0	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	R _{DS(on)}	—	0.024	0.028	Ω	I _D = 15 A, V _{GS} = 10 V *
		—	0.030	0.040		I _D = 15 A, V _{GS} = 4 V *
Forward transfer admittance	y _{fs}	17	27	—	S	I _D = 15 A, V _{DS} = 10 V *
Input capacitance	C _{iss}	—	2250	—	pF	V _{DS} = 10 V, V _{GS} = 0,
Output capacitance	C _{oss}	—	1230	—	pF	f = 1 MHz
Reverse transfer capacitance	C _{rss}	—	300	—	pF	
Turn-on delay time	t _{d(on)}	—	20	—	ns	I _D = 15 A, V _{GS} = 10 V,
Rise time	t _r	—	125	—	ns	R _L = 2 Ω
Turn-off delay time	t _{d(off)}	—	390	—	ns	
Fall time	t _f	—	225	—	ns	
Body to drain diode forward voltage	V _{DF}	—	1.3	—	V	I _F = 30 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	—	160	—	ns	I _F = 30 A, V _{GS} = 0, di _F /dt = 50 A/µs

* Pulse Test

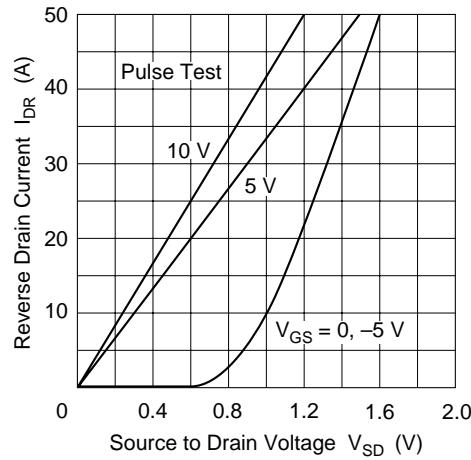


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Reverse Drain Current vs.
Source to Drain VoltageNormalized Transient Thermal Impedance $\gamma_s(t)$ vs. Pulse Width