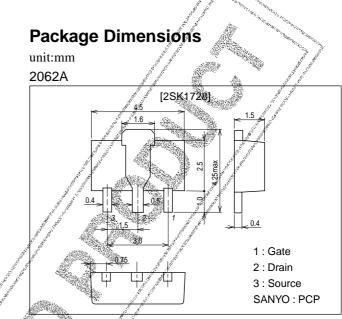


Features

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}	100	V
Gate-to-Source Voltage	V _{GSS}	±15	V
Drain Current (DC)		1	A
Drain Current (Pulse)	1 _{DP} PW≤10µs, duty cycle≤1%	4	A
Allowable Power Dissipation	P_ TC≠25°C	3.5	W
	PD Mounted on a ceramic board (250mm ² ×0.8mm)	1.3	W
Channel Temperature	Ich	150	°C
Storage Temperature	stg	–55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
Falameter			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS,	ľp≝1mA, V _{GS} =0	100			V
Zero-Gate Voltage Drain Current	IDSS /	[™] DS=100V, V _{GS} =0			100	μA
Gate-to-Source Leakage Current	IGSS	$V_{GS}=\pm 12V, V_{DS}=0$			±10	μA
Cutoff Voltage	VGS(øff)	V _{DS} =10V, I _D =1mA	1.0		2.0	V
Forward Transfer Admittance	∫ yfs	V _{DS} =10V, I _D =500mA	0.6	1.0		S
Static Drain-to-Source ON State Resistance	RDS(on)	I _D =500mA, V _{GS} =10V		2.7	3.5	Ω
	RDS(on)	I _D =500mA, V _{GS} =4V		3.2	4.2	Ω
11 200 11	y			Contin	ued on ne	ext page
11 1998 11						

Any and all SANYO products described or contained herein do not have specifications that can handle applications that equire extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO representative nearest you before using any SANYO products described or contained herein in such applications.

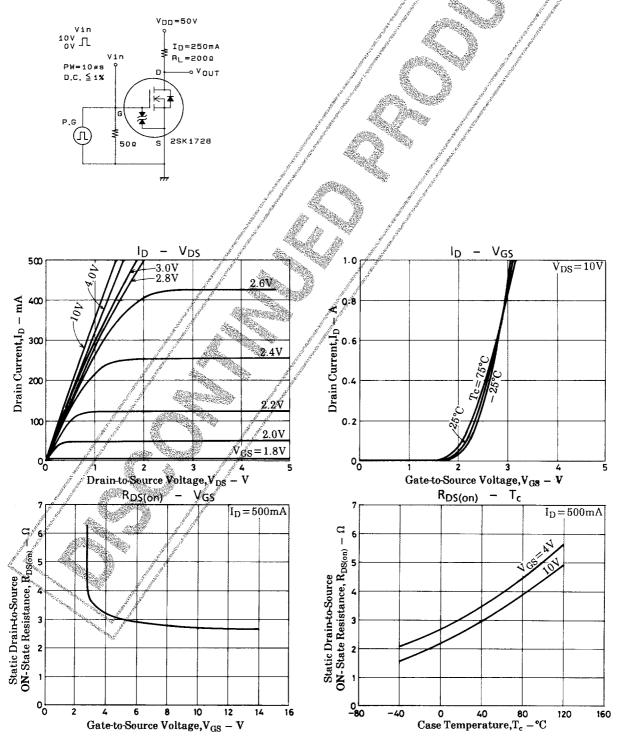
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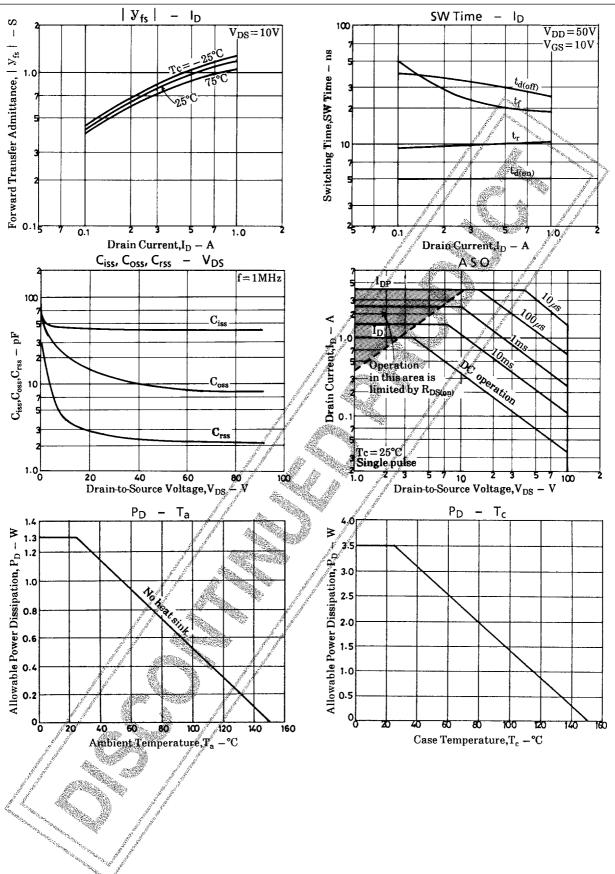
SANYO Electric Co., Ltd. Semiconductor Company TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

Continued from preceding page.

Parameter	Symbol	Conditions			Ratings		Unit
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz			45		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz			15		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz			3		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit		and the second	5		ns
Rise Time	t _r	See specified Test Circuit		and a second	10		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit	j.	a starter	-30	Canil Sec	ns
Fall Time	tf	See specified Test Circuit	and a second	Ś.	20	State Street	ns
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0	AND STREET		1.0	and the second second	V

Switching Time Test Circuit





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