

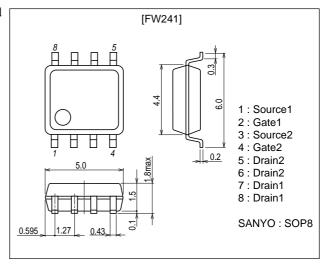


Ultrahigh-Speed Swiching Applications

Features

- This composite device allows high density mounting by unit : mm incorporating two MOSFET chips in one package that feature low on-resistance, ultrahigh switching speed, and drive voltage of 4.5V.
- The two chips have near characteristics, and especially suited for HDD.

Package Dimensions



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ΙD		3.5	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	14	Α
Allowable Power Dissipation	Do	Mounted on a ceramic board (2000mm ² X0.8mm)1unit	1.4	W
	PD	Tc=25°C	2.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

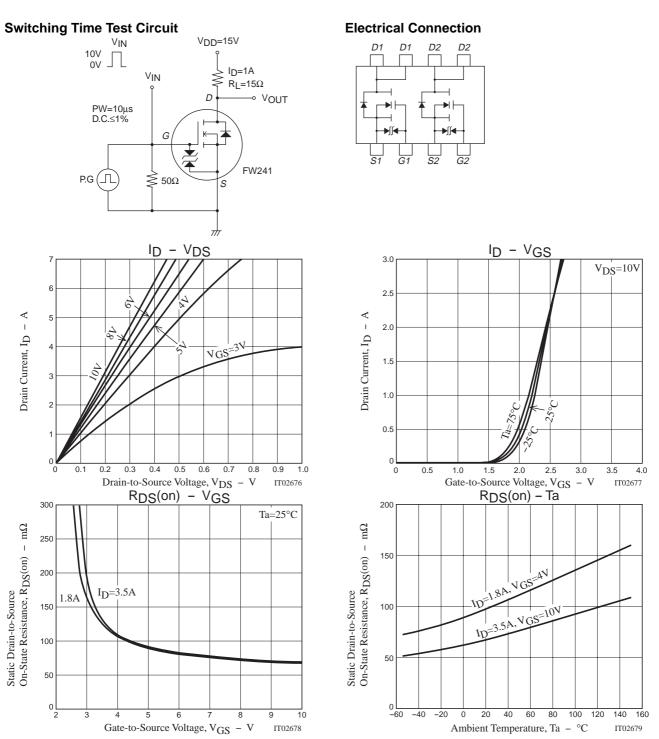
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0	30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ
Gate Threshold Voltage	VGS(th)	V _{DS} =V _{GS} , I _D =250μA	1.2		2.5	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =3.5A	3.7	5.3		S

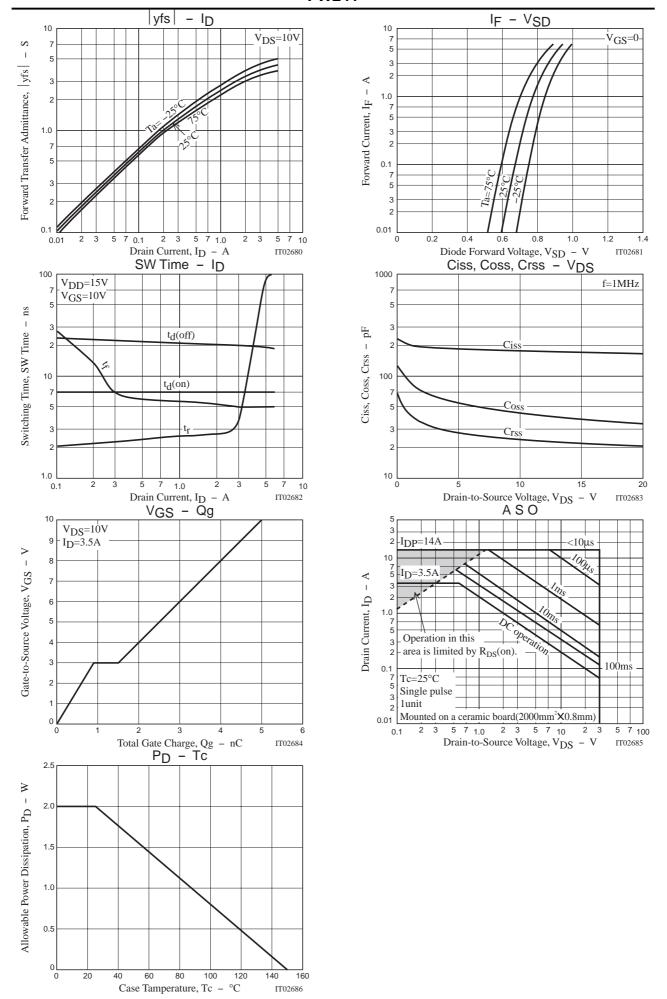
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Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			- Unit
			min	typ	max	Offic
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =3.5A, V _{GS} =10V		64	84	mΩ
	R _{DS} (on)2	I _D =1.8A, V _{GS} =4.5V		105	150	mΩ
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		180		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		42		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		25		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		7		ns
Rise Time	t _r	See specified Test Circuit		3		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		20		ns
Fall Time	tf	See specified Test Circuit		6		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =3.5A		5.0		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =3.5A		0.9		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =3.5A		0.6		nC
Diode Forward Voltage	V _{SD}	I _S =3.5A, V _{GS} =0		0.88	1.2	V





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