

Application Specific Discretes A.S.D.™

TERMINAL SET INTERFACE PROTECTION AND DIODE BRIDGE

MAIN APPLICATION

Telecom equipment requiring combined protection against transient overvoltages and rectification by diode bridge :

- Telephone set
- Base station for cordless set
- Fax machine
- Modem
- Caller Id equipment
- Set top box

DESCRIPTION

The TSIxxB1 provides the diode bridge and the crowbar protection function that can be found in most of telecom terminal equipment.

Integrated monolithically within a SO8 package, this ASD device allows space saving on the board and greater reliability.

FEATURES

- STAND-OFF VOLTAGE FROM 62V TO 265V
- PEAK PULSE CURRENT : 30 A (10/1000 μs)
- MAXIMUM DC CURRENT : I_F = 0.2 A
- HOLDING CURRENT :150 mA

IN ACCORDANCE WITH THE FOLLOWING STANDARDS :

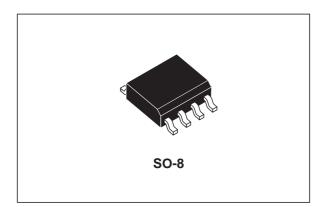
| CCITT K17 - K20 | 10/700 5/310 | μs μs | 1.5 kV 38A |
|-----------------|-----------------|----------|-------------------|
| VDE 0433 | 10/700 | μs | 2 kV |
| | 5/310 | μs | 40A(*) |
| CNET | 0.5/700 | μs | 1.5 kV |
| | 0.2/310 | μs | 38A |
| Bellcore | | | |
| TR-NWT-000974: | 10/1000 | | 1 kV |
| | 10/1000 | μs | 30A(*) |
| FCC Part 68 | 2/10 2/10 | μs μs | 2.5 kV 75A (*) |

MIL STD883C Method 3015-6

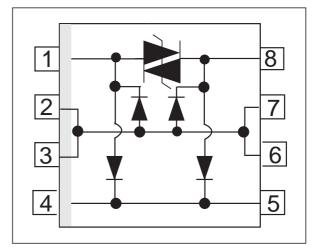
(*) with series resistor or PTC.

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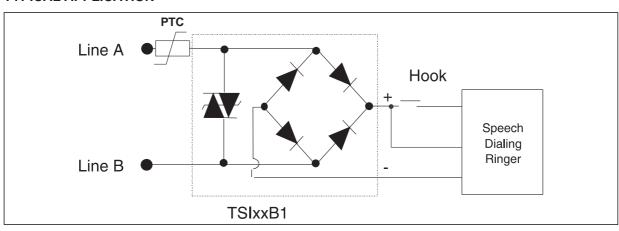
SCHEMATIC DIAGRAM



BENEFITS

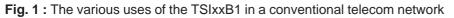
- Diode bridge for polarity guard and crowbar protection within one device.
- Single chip for greater reliability
- Reduces component count versus discrete solution
- Saves space on the board

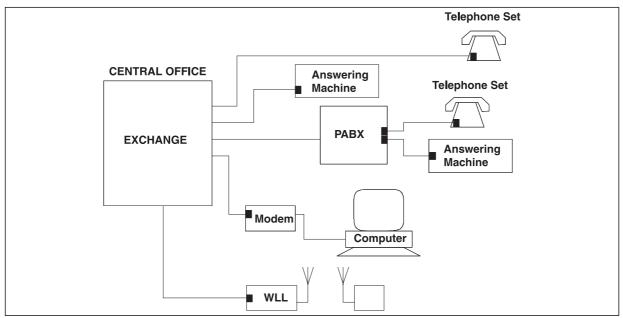
TSIxxB1 TYPICAL APPLICATION



Telecom terminals have a diode bridge for polarity guard, located at the line interface stage. They also have above this diode bridge one crowbar protection device that is mandatory to prevent atmospheric effects and AC mains disturbances from damaging the electronic circuitry that follows the diode bridge.

SGS-THOMSON proposes a one chip device that includes both protection and diode bridge. This is the concept of the TSIxxB1 devices.





ELECTRICAL PARAMETERS

The V_{RM} value corresponds to the maximum voltage of the application in normal operation. For instance, if the maximum line voltage is ranging between $(100V_{RMS} \text{ of ringing plus } 48V \text{ of battery voltage, then the protection chosen for this application shall have a V_{RM} close to 200V.$

The V_{BO} is the triggering voltage. This indicates the voltage limit for which the component short-circuits. Passing this V_{BO} makes the device turn on.

The I_{BO} is the current that makes the device turn on. Indeed, if we want a Trisil to be turned on not only the voltage across it shall pass the V_{BO} value but the current through it shall also pass the I_{BO} value.

In other words, if a voltage surge occurring on the line is higher than the V_{BO} value of a Trisil, whereas the line surge current is limited to a value that does not exceed the Trisil's I_{BO} value, then the Trisil will never turn into short circuit. At this time the surge will be <u>clamped</u> by the Trisil.

Anyhow the electronic circuitry located after the Trisil <u>will always be protected</u> whatever the Trisil state is (crowbar or clamping mode).

The I_H stands for the holding current. When the Trisil is turned on, as soon as the crossing current surge gets lower than this I_H value, the Trisil protection device turns back in its idle state.

<u>Remark</u> : for this reason the Trisil 's I_H value shall be chosen higher than what the maximum telecom line current can be.

Fig. 2 : Test circuit for the CCITT K17 recommendations

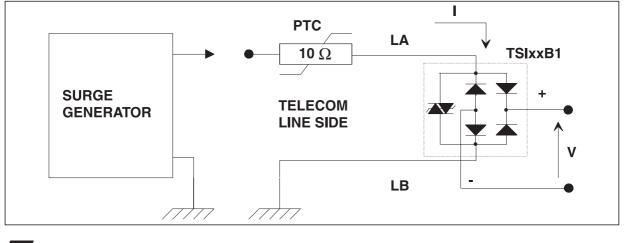
TSIxxB1 BEHAVIOUR WITH REGARD TO SURGE STANDARD :

The TSIxxB1 is able to replace both diode bridge and usual discrete protection on telecom terminals. Furthermore it complies with the CCITT K17 recommendations :

10/700 μs waveform surge test, (1.5kV

AC power induction test

AC power contact test



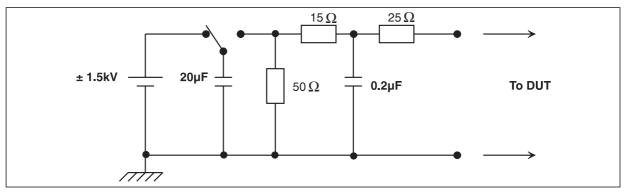
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TEST # 1 LIGHTNING SIMULATION

This test concerns the 10/700 μs waveform surge, \pm 1.5 kV.

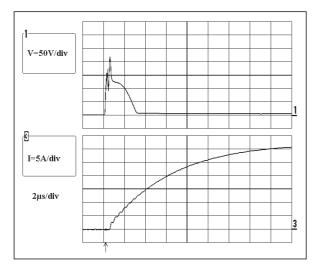
The surge generator used for the test has the following circuitry (fig.2).

Fig. 2 : 10/700 μ s waveform surge generator circuit



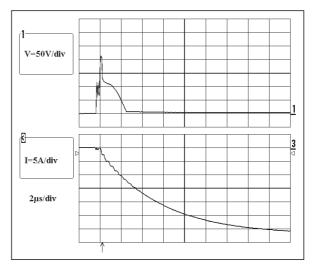
The behaviour of the TSI200B1 to this lightning surge is given below (fig. 3).

Fig. 3: Voltage across the TSI200B1 at the + and - terminations and current throught it for a 1.5 kV positive surge (fig.3a) and negative surge (fig. 3b)



These curves show the peak voltage the surge generates across the TSI200B1 + and - terminations. This lasts a short time (' 2 μ s) and after, as the internal protection gehaves like a short circuit. The voltage drop across the TSIxxB1 becomes a few volts. In the meanwhile all the surge current flows in the protection.

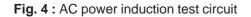
As far as the 10/700 μs waveform surge test is concerned,the TSIxxB1 withstand the ±1.5 kV test.

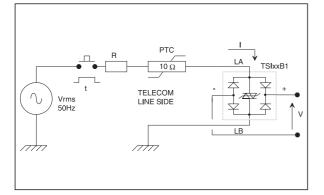


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TEST # 2 AC POWER INDUCTION TEST

This test simulates the induction phenomena that can happen between telecom lines and AC mains lines (fig. 4).





Part #1

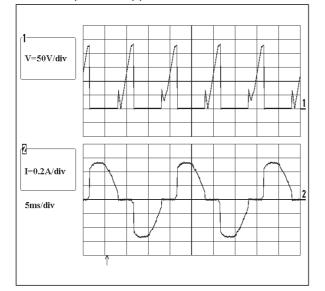
test conditions :

 $V_{RMS} = 240 V$ R = 600 Ω t = 0.2 s

Part #2 test conditions : $V_{RMS} = 600 V$ $R = 600 \Omega$

t = 0.2 s

Fig. 5 : Voltage at the + and - terminations of the TSI200B1, and current through it while test part 1 is applied.



The TSIxxB1 withstand the AC power induction test in both cases.

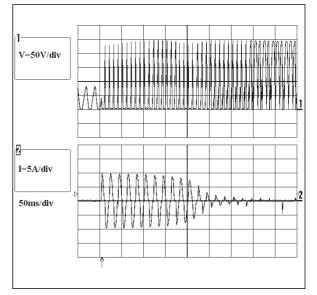
TEST #3 AC POWER CONTACT TEST

This test simulates the direct contact between the telecom lines and the AC mains lines.

The AC power contact test consists in applying 240V_{RMS} through a 10 Ω PTC during 15 minutes long on the device under test. The CCITT K17 recommendation specifies an internal generator impedance allowing 10 A_{RMS} when in short circuit.

The behavior of the TSI200B1 with respect to this surge is given in figure 6.

Fig. 6: Voltage at the TSI200B1 + & - terminations and the current through it.



The figure 6 shows that after 250ms there is no current anymore flowing through the TSI200B1 device. This is due to the action of the serial PTC that limits the current through the line. This PTC is mandatory for this test. It can also be replaced by a fuse or any other serial protection that "opens" the line loop under AC contact test.

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25°C)

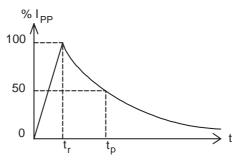
| Symbol | Parameter | Value | Unit | |
|------------------------|---|-------|---------------------|----|
| IPP | Non repetitive peak on-sate current (see note 1) 10/1000 μ s (open circuit voltage wave shape 10/100 μ s) 5/310 μ s (open circuit voltage wave shape 10/700 μ s) 2/10 μ s (open circuit voltage wave shape 2/10 μ s) | | 30 40 75 | A |
| ١ _F | Maximum DC current | | 0.2 | A |
| I _{TSM} | Non repetitive surge peak on-state currenttp = 20 mst = 1s | | 5 3.5 | A |
| T _{stg} Tj | Storage temperature range Maximum junction temperature | | - 55 to +150 150 | °C |
| TL | Maximum lead temperature for soldering during 10 s | | 260 | °C |

Note 1 : Pulse waveform :

 10/1000μs
 t_r=10μs
 t_p=1000μs

 5/310μs
 t_r=5μs
 t_p=310μs

 2/10μs
 t_r=2μs
 t_p=10μs

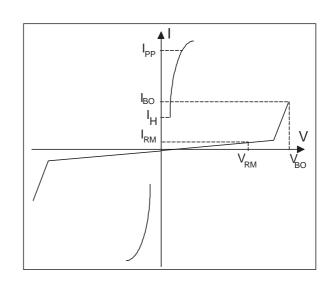


THERMAL RESISTANCE

| Symbol | Parameter | Value | Unit |
|----------------------|---------------------|-------|------|
| R _{th(j-a)} | Junction to ambient | 170 | °C/W |

ELECTRICAL CHARACTERISTICS (Tamb=25°C)

| Symbol | Parameter | |
|-----------------|------------------------------------|--|
| V _{RM} | Stand-off voltage | |
| V _{BO} | Breakover voltage | |
| V _{BR} | Breakdown voltage | |
| Iн | Holding current | |
| I _{BO} | Breakover current | |
| I _{RM} | Leakage current at V _{RM} | |
| I _{PP} | Peak pulse current | |
| С | Capacitance | |
| αΤ | Temperature coefficient | |



ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C) 1 - PROTECTION DEVICES PARAMETERS

| | I _{RM} @ | V RM | V _{BO} @ I _{BO} | I _H | IE | 80 | С |
|----------|-------------------|-------------|---|----------------|------|------|-------|
| Туре | | | note1 | note2 | no | te1 | note3 |
| Type | μΑ | V | V | mA | mA | mA | pF |
| | max. | | max. | min. | min. | max. | typ. |
| TSI62B1 | 1 5 | 50 62 | 90 | 150 | 50 | 400 | 200 |
| TSI180B1 | 1 5 | 50 180 | 250 | 150 | 50 | 400 | 200 |
| TSI200B1 | 1 5 | 50 200 | 290 | 150 | 50 | 400 | 200 |
| TSI220B1 | 1 5 | 50 220 | 330 | 150 | 50 | 400 | 200 |
| TSI265B1 | 1 5 | 50 265 | 380 | 150 | 50 | 400 | 200 |

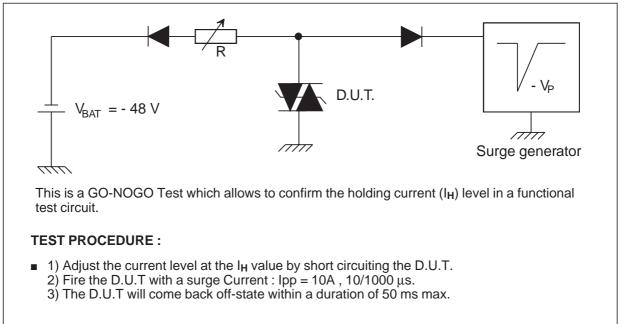
Note 1 : Measured at 50 Hz, one cycle

Note 2 : See test cricuit Note 3 : $V_R = 0V$, F = 1MHz, between pins 1 and 8.

2 - DIODE BRIDGE PARAMETERS

| Symbol | Test condition | Value | Unit |
|-----------------|------------------------|-------|------|
| V _F | $I_F = 20 \text{ mA}$ | 0.9 | V |
| (for one diode) | $I_F = 100 \text{ mA}$ | 1.1 | V |

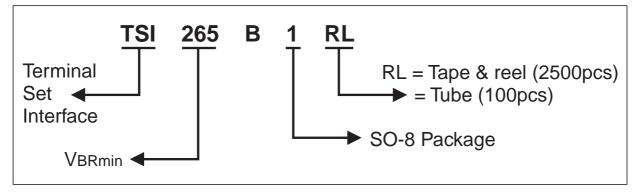
FUNCTIONAL HOLDING CURRENT (IH) TEST CIRCUIT : GO - NO GO TEST



MARKING

| Туре | Marking |
|----------|---------|
| TSI62B1 | TSI62 |
| TSI180B1 | TSI180 |
| TSI200B1 | TSI200 |
| TSI220B1 | TSI220 |
| TSI265B1 | TSI265 |

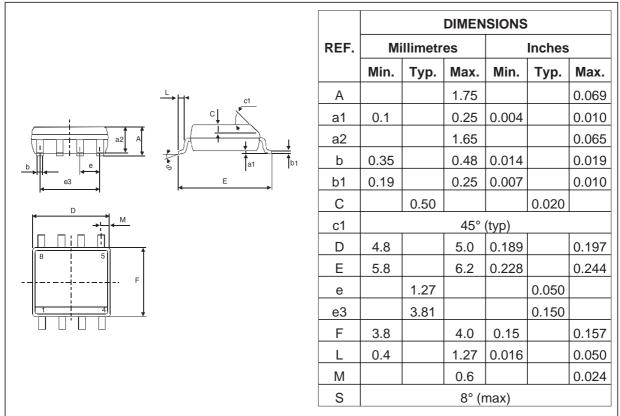
ORDER CODE



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PACKAGE MECHANICAL DATA

SO-8



Packaging : product supplied in tape and reel or antistatic tubes.

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