MN39143AT

Diagonal 6.0 mm (type-1/3) 410k-pixel CCD Area Image Sensor

Overview

The MN39143AT is a 6.0 mm (type-1/3) interline transfer CCD (IT-CCD) solid state image sensor device.

This device uses photodiodes in the optoelectric conversion section and CCDs for signal readout. The electronic shutter function has made an exposure time of 1/10000 seconds possible. Further, this device has the features of high sensitivity, low noise, broad dynamic range, and low smear.

This device has a total of 403 920 pixels (816 horizontal \times 495 vertical) and provides stable and clear images with a resolution of 550 horizontal TV-lines and 350 vertical TV-lines.

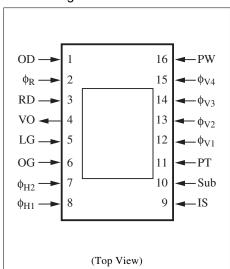
Part Number	Size	System	Color or B/W
MN39143AT	6.0 mm (type-1/3)	EIA	B/W

Features

- Effective pixel number 771 (horizontal) × 492 (vertical)
- High sensitivity
- Broad dynamic range
- Low smear
- Electronic shutter

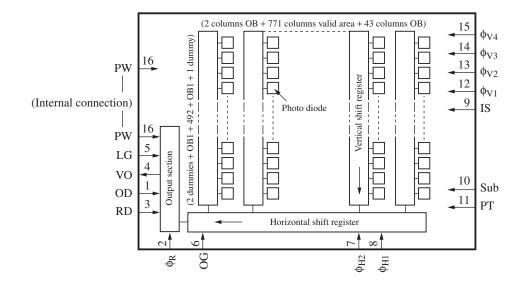
Applications

- Serveillance cameras
- FA, OA cameras



Pin Assignments

Block Diagram



Pin Descriptions

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	OD	Output drain	9	IS	Horizontal CCD input source
2	φ _R	Reset pulse	10	Sub	Substrate
3	RD	Reset drain	11	РТ	P-well for protection circuit
4	VO	Video output	12	ϕ_{V1}	Vertical shift register clock pulse 1
5	LG	Output load transistor gate	13	ϕ_{V2}	Vertical shift register clock pulse 2
6	OG	Output gate	14	φ _{V3}	Vertical shift register clock pulse 3
7	φ _{H2}	Horizontal register clock pulse 2	15	$\phi_{\rm V4}$	Vertical shift register clock pulse 4
8	φ _{H1}	Horizontal register clock pulse 1	16	PW	P-well

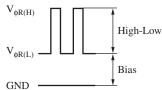
■ Device Parameter (H × V)

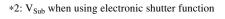
Parameter	Value	Unit
Total pixel number	816 × 495	pixel
Effective pixel number	771 × 492	pixel
Active pixel number	759 × 482	pixel
Image sensing block dimension	4.93×3.69	mm ²
Pixel dimension	6.40×7.50	μm^2

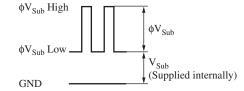
■ Absolute Maximum Ratings and Operating Conditions

Parameter		Absolute maximum rating		Operating condition			11	
		Lower limit	Upper limit	Min	Тур	Max	- Unit	
V _{RD}		- 0.2	18.0	14.5	15.0	15.5	V	
V _{OD}		- 0.2	18.0	14.5	15.0	15.5	V	
V _{IS}		- 0.2	18.0	14.5	15.0	15.5	V	
V _{LG}				(Internal bias)				
V _{OG}				(Internal bias)			V	
V _{PT} *3, 4		-9.0	0.2	-7.3	-7.0	-6.7	V	
V _{PW}		(Refere	nce voltage)		0		V	
$V_{\phi R}$ *1,	High-Low	- 0.2	5.0	3.0	3.3	3.6	V	
	Bias	- 0.2	5.0	(Supplied internally)		v		
$V_{\phi H1}$	High	- 0.2	5.0	3.0	3.3	3.6	V	
·	Low	- 0.2	5.0	- 0.1	0	0.1	V	
$V_{\phi H2}$	High	- 0.2	5.0	3.0	3.3	3.6	V	
	Low	- 0.2	5.0	- 0.1	0	0.1	V	
V _{Sub} *2		- 0.2	45.0	(S	(Supplied internally)		V	
$\phi V_{Sub}{}^{*2}$		- 0.2	45.0	21.0	22.0	23.0	V	
$V_{\phi V1} *^{3, 4}$	High	-9.0	18.0	14.5	15.0	15.5	V	
	Middle	-9.0	18.0	- 0.2	0	0.2	V	
	Low	-9.0	18.0	-7.3	-7.0	-6.7	V	
$V_{\phi V2} * 3, 4$	Middle	-9.0	15.0	- 0.2	0	0.2	V	
	Low	-9.0	15.0	-7.3	-7.0	-6.7	V	
V _{\$\phiV3} *3, 4	High	-9.0	18.0	14.5	15.0	15.5	V	
	Middle	-9.0	18.0	- 0.2	0	0.2	V	
	Low	-9.0	18.0	-7.3	-7.0	-6.7	V	
$V_{\phi V4} *^{3, 4}$	Middle	-9.0	15.0	- 0.2	0	0.2	V	
	Low	-9.0	15.0	-7.3	-7.0	-6.7	V	
Operating te	mperature	-10	70		25	_	°C	
Storage temperature		-30	80		_		°C	









*3: Absolute maximum rating $-0.2 < V_{\phi V} - V_{PT} < 24.5 (V)$

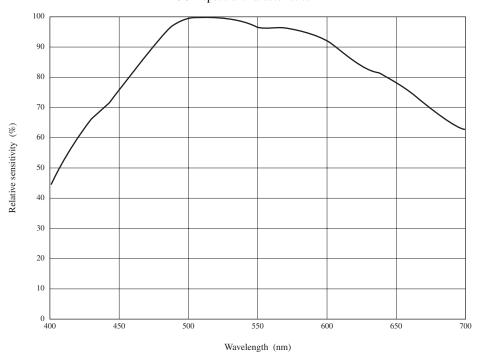
*4: Relation between V_{PT} and $V_{\phi VL}$

Set V_{PT} that is to meet the following conditions for VL voltage of the vertical shift clock waveform. $V_{PT} \le VL (V_{\phi V1L} \text{ to } V_{\phi V4L})$

Optical Characteristics

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
S/N ratio (dark)	S/Nd	Dark condition	57	60	—	dB
Sensitivity	So	Standard condition (J chart)		750	—	mV
Carrier saturation output	Sa	J chart		1 400	—	mV
Vertical smear	Sm	1/10 V chart, F2.8	_	-100	-95	dB

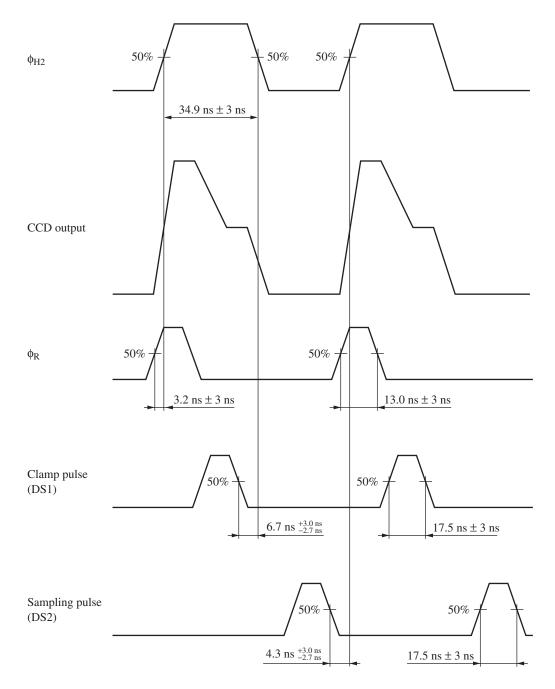
■ Graph of Characteristics



CCD spectral characteristics

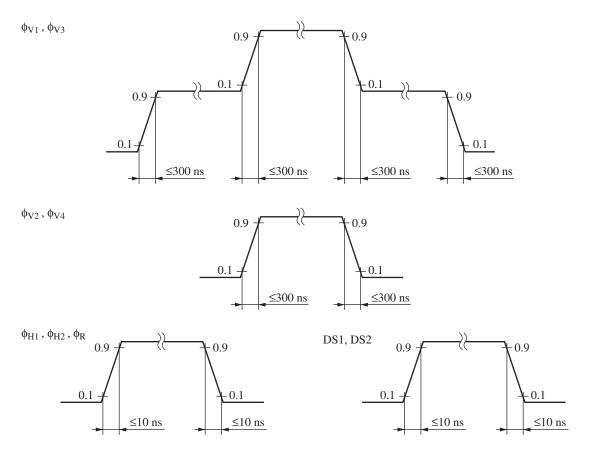
■ Timing Diagram

• High speed pulse timing

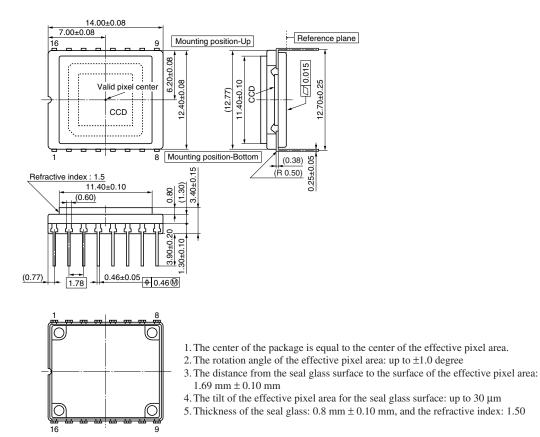


■ Timing Diagram (continued)

• Rise time and fall time of each pulse



- Package Dimensions (unit: mm)
- WDIP016-P-0500C



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