Unit: mm

# 2SD2575

### Silicon NPN epitaxial planer type

#### For low-frequency power amplification

#### Features

Low collector to emitter saturation voltage V<sub>CE(sat)</sub>.

#### Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	$V_{CBO}$	15	V
Collector to emitter voltage	$V_{CEO}$	10	V
Emitter to base voltage	$V_{\rm EBO}$	10	V
Peak collector current	${\rm I_{CP}}^{*1}$	9	A
Collector current	$I_{C}$	5	A
Collector power dissipation	$P_{C}$	750	mW
Junction temperature	$T_{j}$	150	°C
Storage temperature	$T_{\rm stg}$	<b>−55 ~ +150</b>	°C

<sup>\*1</sup> Measuring time: t = 380µsec

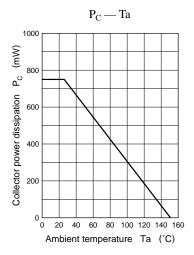
#### Electrical Characteristics (Ta=25°C)

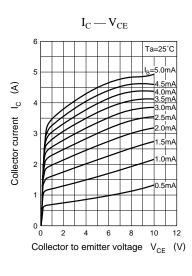
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	$I_{CBO}$	$V_{CB} = 10V, I_E = 0$			0.1	μΑ
	I <sub>CEO</sub>	$V_{CE} = 5V, I_{B} = 0$			1.0	μΑ
Emitter cutoff current	I <sub>EBO</sub>	$V_{EB} = 5V, I_{C} = 0$			0.1	μΑ
Collector to emitter voltage	V <sub>CEO</sub>	$I_{\rm C} = 1  \text{mA},  I_{\rm B} = 0$	10			V
Emitter to base voltage	V <sub>EBO</sub>	$I_{\rm E} = 10 \mu A, I_{\rm C} = 0$	10			V
Forward current transfer ratio	h <sub>FE1</sub>	$V_{CE} = 2V, I_{C} = 0.5A^{*}$	700			
	h <sub>FE2</sub>	$V_{CE} = 2V, I_{C} = 2A^{*}$	195			
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 3A, I_B = 0.1A^*$		0.28	0.5	V
Transition frequency	$f_T$	$V_{CB} = 6V, I_E = -50mA, f = 200MHz$		170		MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 20V, I_E = 0, f = 1MHz$		45	65	pF

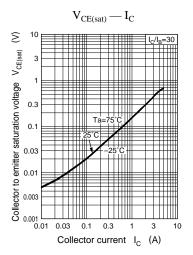
<sup>\*</sup> Pulse measurement

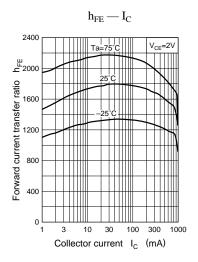
<sup>5.0±0.2</sup> 4.0±0.2 5.1±0.2 13.5±0.5 0.45 +0.2 1:Emitter 2:Collector 3:Base 2.54±0.15 JEDEC:TO-92 EIAJ:SC-43A

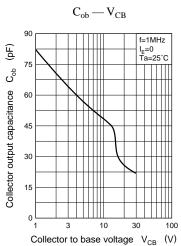
Transistor 2SD2575











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