2SK0657 (2SK657)

Silicon N-Channel MOS FET

For switching

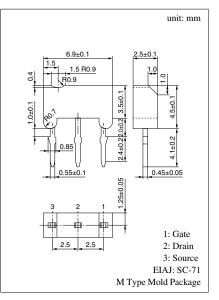
Features

- High-speed switching
- M type package, allowing easy automatic and manual insertion as well as stand-alone fixing to the printed circuit board.

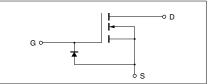
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Parameter	Symbol	Ratings	Unit
Drain to Source breakdown voltage	V _{DSS}	50	V
Gate to Source voltage	V _{GSO}	8	V
Drain current	ID	±100	mA
Max drain current	I _{DP}	±200	mA
Allowable power dissipation	P _D	400	mW
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

■ Absolute Maximum Ratings (Ta = 25°C)

■ Electrical Characteristics (Ta = 25°C)

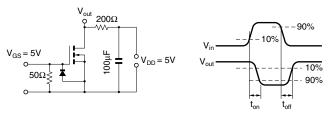


Internal Connection

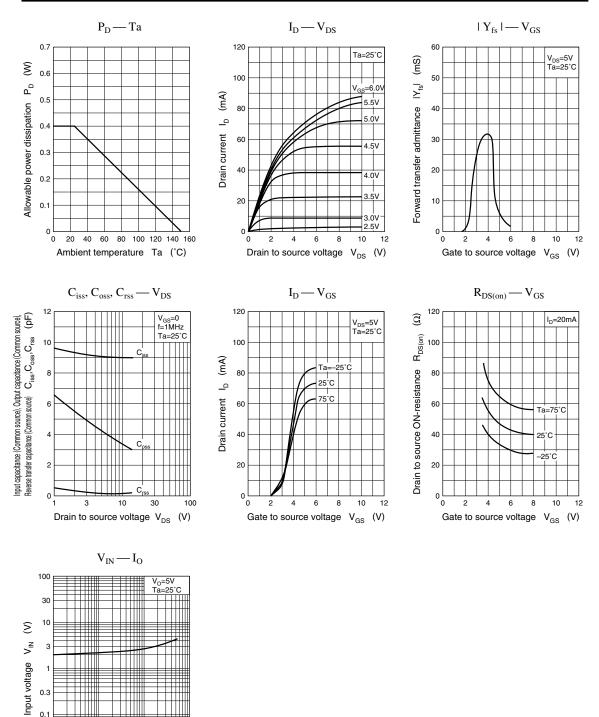


Parameter Symbol Conditions Unit min typ max Drain to Source cut-off current $V_{DS} = 10V, V_{GS} = 0$ 10 I_{DSS} μΑ $V_{GS} = 8V, V_{DS} = 0$ 50 Gate to Source leakage current I_{GSS} μΑ Drain to Source breakdown voltage V_{DSS} $I_D = 100 \mu A, V_{GS} = 0$ 50 V Gate threshold voltage $I_{D} = 100 \mu A, V_{DS} = V_{GS}$ 1.5 V V_{th} 3.5 $I_D = 20 \text{mA}, V_{GS} = 5 \text{V}$ Drain to Source ON-resistance 50 Ω R_{DS(on)} Forward transfer admittance $I_D = 20mA, V_{DS} = 5V, f = 1kHz$ 20 mS $|Y_{fs}|$ Input capacitance (Common Source) Ciss 15 pF Output capacitance (Common Source) $V_{DS} = 5V, V_{GS} = 0, f = 1MHz$ 6 pF Coss Reverse transfer capacitance (Common Source) C_{rss} 1.2 pF Turn-on time $V_{DD} = 5V, V_{GS} = 0$ to 5V, $R_L = 200\Omega$ 10 ns t_{on}" Turn-off time $t_{\rm off}^{*}$ $V_{DD} = 5V, V_{GS} = 5 \text{ to } 0V, R_{L} = 200\Omega$ 20 ns

^{*} t_{on}, t_{off} measurement circuit



Note) The part number in the parenthesis shows conventional part number.



0.03 0.01 0.1

0.3

3 10 30 100

(mA)

1 Output current Io

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