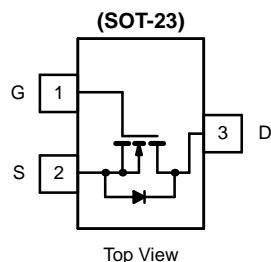


N-Channel 1.25-W, 2.5-V MOSFET

PRODUCT SUMMARY		
V _{DS} (V)	r _{D(on)} (Ω)	I _D (A)
20	0.085 @ V _{GS} = 4.5 V	2.8
	0.115 @ V _{GS} = 2.5 V	2.4



Top View

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±8	
Continuous Drain Current (T _J = 150°C) ^b	I _D	2.8	A
		2.2	
Pulsed Drain Current ^a	I _{DM}	10	
Continuous Source Current (Diode Conduction) ^b	I _S	1.6	
Power Dissipation ^b	P _D	1.25	W
		0.80	
Operating Junction and Storage Temperature Range	T _J , T _{Stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Maximum Junction-to-Ambient ^b	R _{thJA}	100	°C/W
Maximum Junction-to-Ambient ^c		166	

Notes

- a. Pulse width limited by maximum junction temperature.
- b. Surface Mounted on FR4 Board, t ≤ 5 sec.
- c. Surface Mounted on FR4 Board.

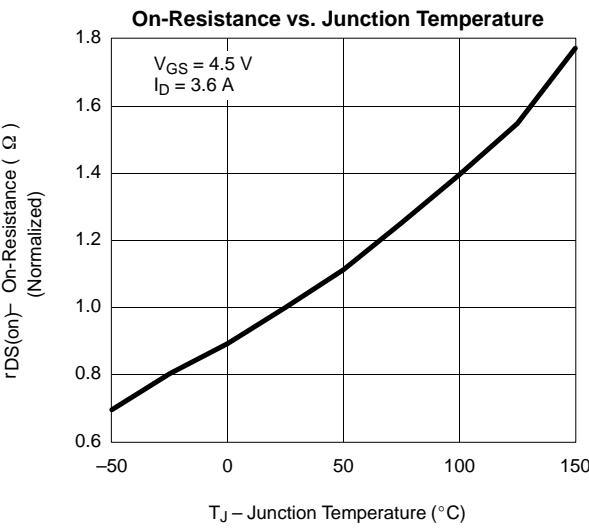
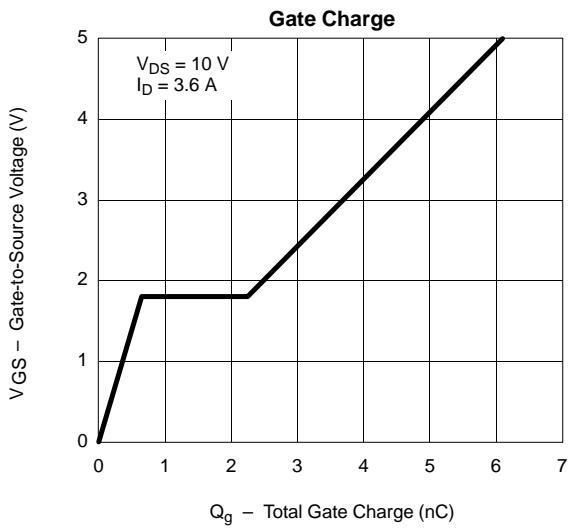
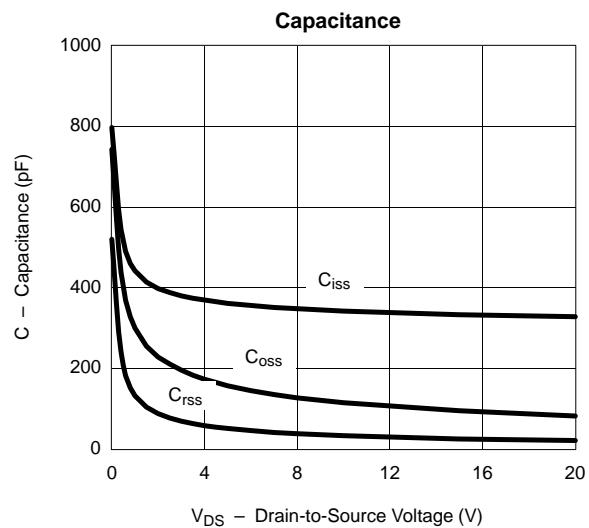
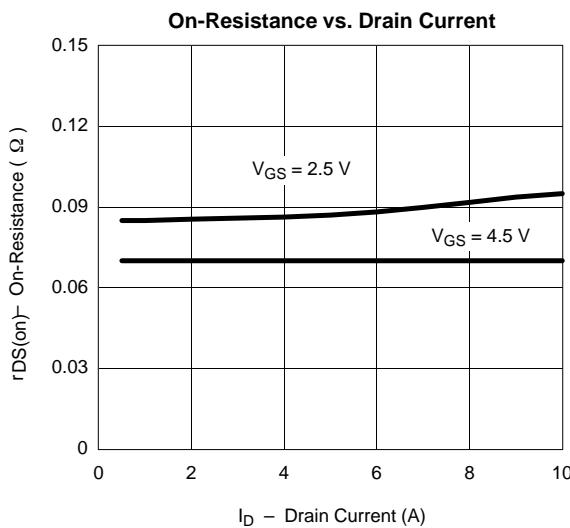
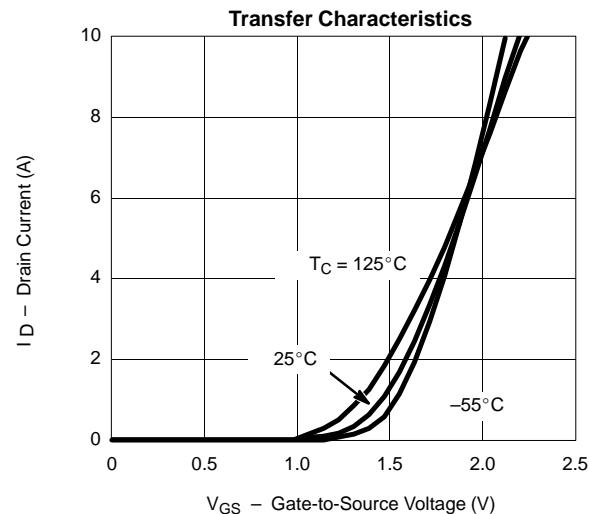
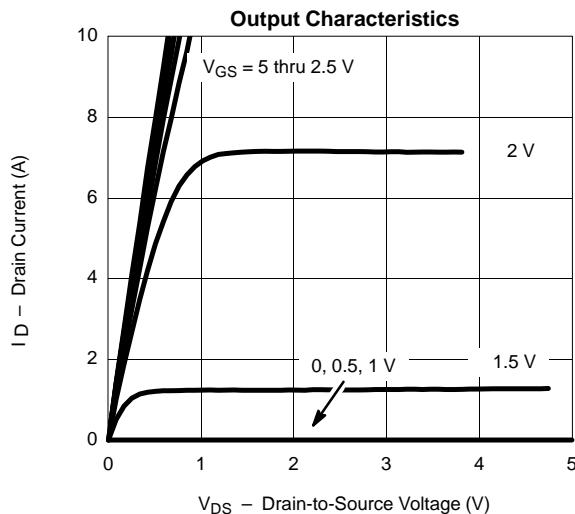
BM2302

SPECIFICATIONS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0 \text{ V}, I_D = 10 \mu\text{A}$	20			V
Gate-Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 50 \mu\text{A}$	0.65			
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA
		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$			10	
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} \geq 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	6			A
		$V_{DS} \geq 5 \text{ V}, V_{GS} = 2.5 \text{ V}$	4			
Drain-Source On-Resistance ^a	$r_{DS(\text{on})}$	$V_{GS} = 4.5 \text{ V}, I_D = 3.6 \text{ A}$		0.07	0.085	Ω
		$V_{GS} = 2.5 \text{ V}, I_D = 3.1 \text{ A}$		0.085	0.115	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 5 \text{ V}, I_D = 3.6 \text{ A}$	10			S
Diode Forward Voltage	V_{SD}	$I_S = 1.6 \text{ A}, V_{GS} = 0 \text{ V}$		0.76	1.2	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 3.6 \text{ A}$		5.4	10	nC
Gate-Source Charge	Q_{gs}			0.65		
Gate-Drain Charge	Q_{gd}			1.60		
Input Capacitance	C_{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		340		pF
Output Capacitance	C_{oss}			115		
Reverse Transfer Capacitance	C_{rss}			33		
Switching						
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = 10 \text{ V}, R_L = 5.5 \Omega$ $I_D \approx 3.6 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_G = 6 \Omega$		12	25	ns
Rise Time	t_r			36	60	
Turn-Off Delay Time	$t_{d(\text{off})}$			34	60	
Fall-Time	t_f			10	25	

Notes

a. Pulse test: PW $\leq 300 \mu\text{s}$ duty cycle $\leq 2\%$..

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

