DS04-28205-2E

ASSP

1 CHANNEL 8-BIT VIDEO A/D CONVERTER

MB40558

■ DESCRIPTION

The Fujitsu MB40558 is a low power ultra-high speed video A/D converter fabricated with Fujitsu Advanced Bipolar Technology. The MB40558 also adopts the fully-parallel comparision technique (flash method) for high speed conversion and can concert wide bandanalog signal such as video signal to digital signal at sampling rate of DC through 40 Mega-samples/sec. Because of such high speed operation, the MB40558 is suitable for digital video applications such as the digital TV, video processing with computer, or ladder signal processing.

■ FEATURES

Resolution: 8 bitsLinearity Error: ±0.15%

Maximum Conversion Rate: 40 MSPS (min.)
 Digital I/O Level: TTL Compatible
 Analog Input Voltage: 3.0V to 5.0V(2Vp-p)

• Single Power Supply: 5.0V

• Power dissipation: 350 mW (typ.)

• Further Function: On Chip Reference Voltage Generator

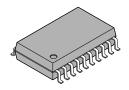
Package: Standard 20-pin Plastic DIP Package: Suffix: –P
 Standard 20-pin Plastic Flat Package: Suffix: –PF

■ PACKAGES

Plastic DIP, 20 pin

(DIP-20P-M01)

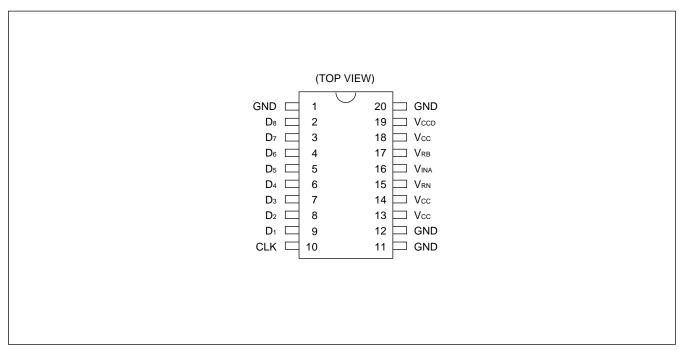
Plastic FPT, 20 pin



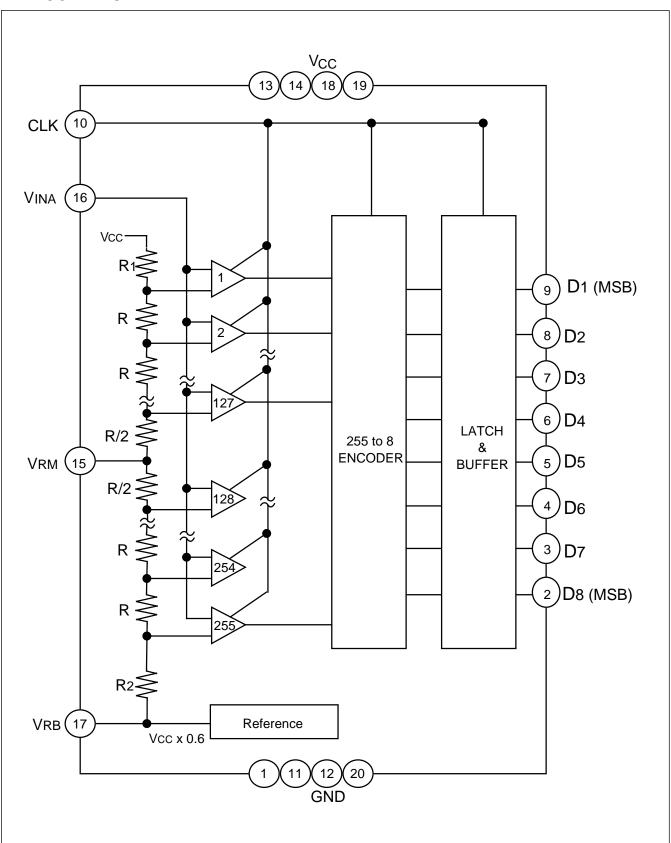
(FPT-20P-M02)

MB40558

■ PIN ASSIGNMENT



■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	nbol Value	
Power supply voltage	Vcc	-0.5 to +7.0	V
Digital input voltage	VIND	-0.5 to +7.0	V
Analog input voltage	VINA	-0.5 to Vcc +0.5	V
Storage temperature	Tstg	-55 to +150	°C

Note: Permanent device damage may occur if the above **Absolute Maximum Ratings** are exceeded. Functional operation should be restricted to the conditions as detailed in the operational sections of this data sheet. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

■ RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Value			Unit
	Symbol	Min.	Тур.	Max.	Oille
Power supply voltage	Vcc	4.75	5.00	5.25	V
Analog input voltage	VINA	Vrb	_	Vcc	V
Digital high-level output current	Іон	-400	_	_	μΑ
Digital low-level output current	lol	_	_	1.6	mA
Clock pulse width at high-level	tw+	11.5	_	_	ns
Clock pulse width at low-level	tw-	11.5	_	_	ns
Operating temperature	Та	-20	_	70	°C

■ ELECTRICAL CHARACTERISTICS

ANALOG DC CHARACTERISTICS

 $(Vcc = 5V \pm 5\%, GND = 0V, Ta = -20 \text{ to } +70^{\circ}C)$

_		,				
Parameter	Symbol	Min.	Тур.	Max.	Unit	
Resolution	_	_	8	_	bits	
Linearrity error	LE	_	±0.15	±0.3	%	
Differential linearrity error	DLE	_	±0.12	_	%	
Equivalent resistance for analog input	RINA	0.18	2.8	_	MΩ	
Analog input capacitance	CINA	_	40	_	pF	
Analog high-level input current	Ііна	_	_	195	μΑ	
Analog low-level input current	IILA	_	_	185	μΑ	
Reference voltage	Vrb	0.6 × Vcc -0.1	0.6 × Vcc	0.6 × Vcc +0.1	V	
Power supply current	Icc	_	70*	130	mA	

^{* :} Vcc = 5.0V, Ta = +25°C

DIGITAL DC CHARACTERISTICS

 $(Vcc = 5 V \pm 5\%, GND = 0V, Ta = -20 to +70^{\circ}C)$

Parameter	Symbol		Value		Unit
	Symbol	Min.	Тур.	Max.	Oilit
High-level output voltage	Vohd	2.7	_	_	V
Low-level output voltage	Vold	_	_	0.4	V
High-level input voltage	Vihd	2.0	_	_	V
Low-level input voltage	VILD	_	_	0.8	V
High-level input current	Інр	_	_	20	μΑ
Low-level input current	lild	-100	_	_	μΑ

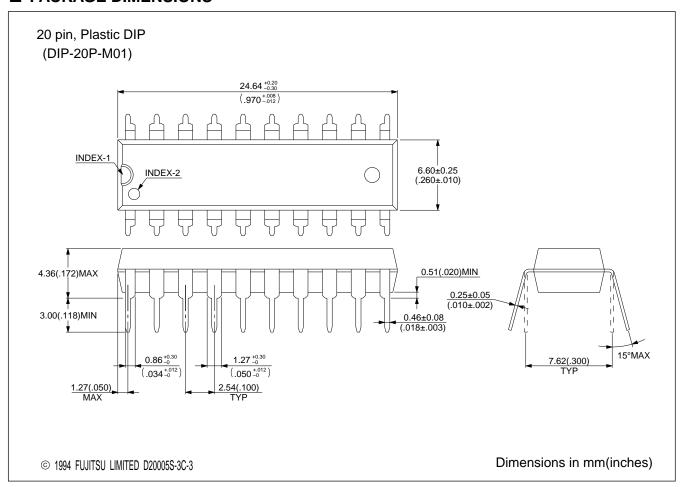
SWITCHING CHARACTERISTICS

 $(Vcc = 5 V \pm 5\%, GND = 0V, Ta = -20 to +70^{\circ}C)$

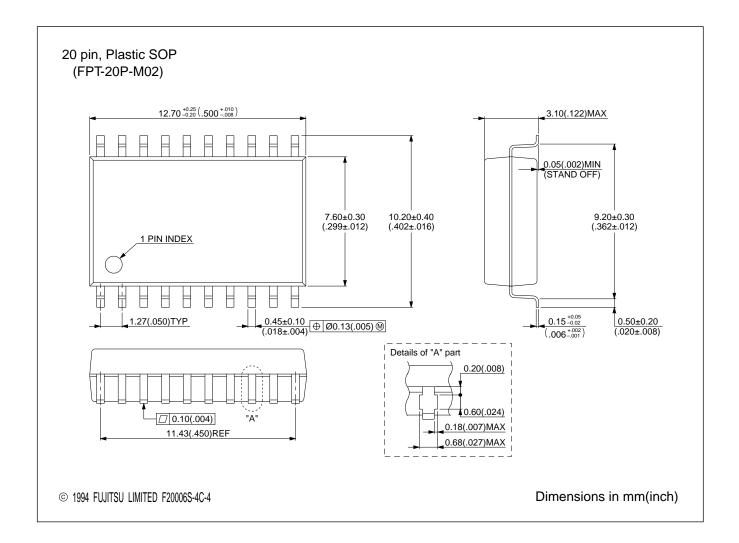
Parameter	Symbol			Unit	
	Symbol	Min.	Тур.	Max.	Onit
Maximum conversion rate	fs	40	_	_	MSPS
Digital output delay time	tpd	6	11	21	ns

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■ PACKAGE DIMENSIONS



MB40558



FUJITSU LIMITED

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