# XP04116 (XP4116)

### Silicon PNP epitaxial planer transistor

#### For switching/digital circuits

#### Features

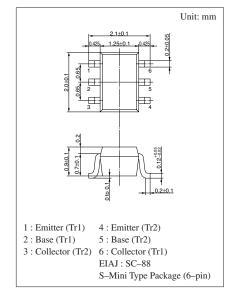
- Two elements incorporated into one package. (Transistors with built-in resistor)
- Reduction of the mounting area and assembly cost by one half.

#### Basic Part Number of Element

• UNR1116(UN1116) × 2 elements

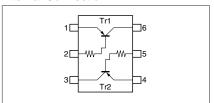
#### Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Ratings	Unit	
Rating of element	Collector to base voltage	$V_{CBO}$	-50	V	
	Collector to emitter voltage	V <sub>CEO</sub>	-50	V	
	Collector current	$I_{C}$	-100	mA	
Overall	Total power dissipation	$P_{T}$	150	mW	
	Junction temperature	$T_{j}$	150	°C	
	Storage temperature	$T_{stg}$	-55 to +150	°C	



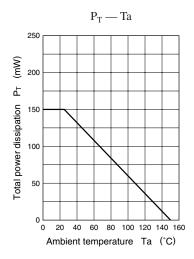
#### Marking Symbol: 6U

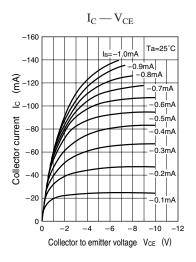
#### Internal Connection

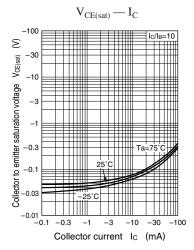


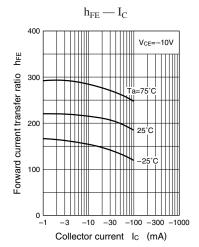
#### Electrical Characteristics (Ta=25°C)

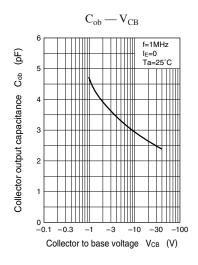
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector to base voltage	V <sub>CBO</sub>	$I_{\rm C} = -10 \mu A, I_{\rm E} = 0$	-50			V
Collector to emitter voltage	V <sub>CEO</sub>	$I_{\rm C} = -2mA, I_{\rm B} = 0$	-50			V
Collector cutoff current	$I_{CBO}$	$V_{CB} = -50V, I_{E} = 0$			- 0.1	μA
Collector cutoff current	$I_{CEO}$	$V_{CE} = -50V, I_B = 0$			- 0.5	μA
Emitter cutoff current	$I_{EBO}$	$V_{EB} = -6V, I_C = 0$			- 0.01	mA
Forward current transfer ratio	$h_{FE}$	$V_{CE} = -10V, I_{C} = -5mA$	160		460	
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = -10 \text{mA}, I_B = -0.3 \text{mA}$			- 0.25	V
Output voltage high level	V <sub>OH</sub>	$V_{CC} = -5V, V_B = -0.5V, R_L = 1k\Omega$	-4.9			V
Output voltage low level	V <sub>OL</sub>	$V_{CC} = -5V, V_B = -2.5V, R_L = 1k\Omega$			- 0.2	V
Transition frequency	$f_T$	$V_{CB} = -10V$ , $I_E = 1$ mA, $f = 200$ MHz		80		MHz
Input resistance	R <sub>1</sub>		-30%	4.7	+30%	kΩ

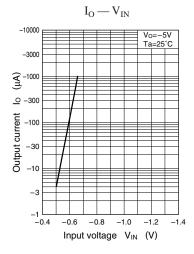


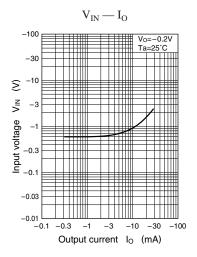












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