

**General Description**

Information from subscriber lines needs to be transferred to time-slots on system internal PCM communication highways. The EPIC-1 concentrates the circuitry necessary to do this interfacing for a large number of transmission lines on a single IC. Therefore, it is not necessary to repeat this PCM interface circuitry for every line. Since the system cost of the EPIC-1 is divided by the number of lines it controls, powerful and comfortable functions can be economically performed.

**Features**

- PCM interface controller for up to 32 ISDN or 64 analog subscribers
- Time-slot assignment freely programmable for all subscribers
- Non-blocking switch for 128 channels
- Switching of 16-, 32-, 64-kbit/s channels (128 kbit/s via two consecutive 64 kbit/s channels)
- Two serial interfaces: PCM and configurable (IOM-2, IOM-1, SLD, PCM).

| Type       | Package          |
|------------|------------------|
| PEB 2055-N | P-LCC-44-1 (SMD) |
| PEF 2055-N | P-LCC-44-1 (SMD) |

- Interfacing with four full-duplex PCM highways (up to 8 Mbit/s)
- Data rates of PCM and configurable interfaces independent of each other
- Change detection (“last look”) logic for C/I or signaling channels
- 16-byte FIFO for monitor or feature control channels
- Standard  $\mu$ P interface with multiplexed or demultiplexed address/data bus or separate address and data busses
- Advanced low power CMOS technology
- Also available with extended temperature range – 40 to 85 °C (PEF 2055-N)

