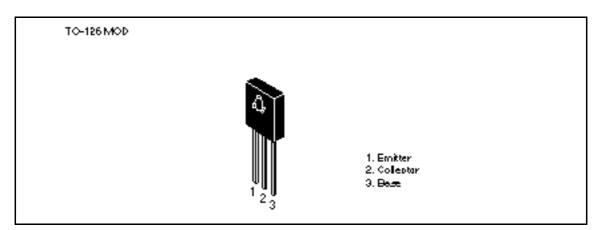
Silicon NPN Epitaxial

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Application

High frequency amplifier

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Collector to base voltage	V _{CBO}	30	V
Collector to emitter voltage	V _{CEO}	20	V
Emitter to base voltage	V _{EBO}	3.5	V
Collector current	I _c	0.3	A
Collector peak current	I _{C(peak)}	0.5	А
Collector power dissipation	P _c	0.8	W
	P _c * ¹	5	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

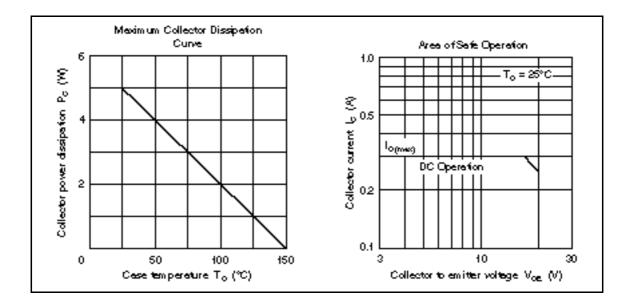
Note: 1. Value at $T_c = 25^{\circ}C$

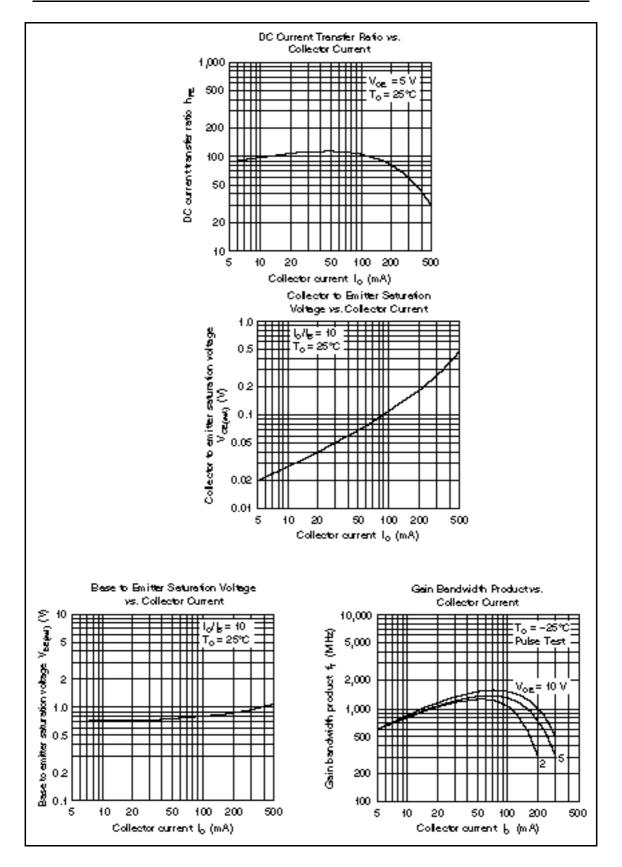


Electrical Characteristics (Ta = 25°C)

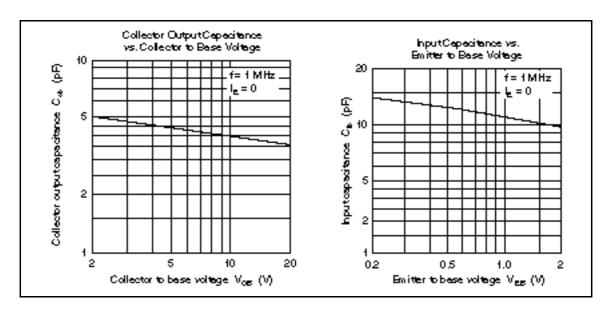
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to emitter breakdown voltage	$V_{(\text{BR})\text{CEO}}$	20	_	_	V	$I_c = 10$ mA, $R_{BE} =$
Collector cutoff current	I _{CBO}	_	_	1	mA	$V_{CB} = 25 \text{ V}, \text{ I}_{E} = 0$
Emitter cutoff current	I _{EBO}	—	—	1	mA	$V_{EB} = 3 V, I_{C} = 0$
DC current transfer ratio	h _{FE}	40	—	200		$V_{ce} = 5 \text{ V}, \text{ I}_{c} = 50 \text{ mA}^{*1}$
Base to emitter voltage	V_{BE}	—	—	1.2	V	$V_{ce} = 5 \text{ V}, I_c = 300 \text{ mA}^{*1}$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	_	_	2.0	V	$I_{c} = 300 \text{ mA}, I_{B} = 60 \text{ mA}^{*1}$
Gain bandwidth product	f _T	_	1.2	_	GHz	$V_{ce} = 5 \text{ V}, I_c = 100 \text{ mA}^{*1}$
Collector output capacitance	Cob	_	5	_	pF	$V_{CB} = 10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$
Input capacitance	Cib	_	10	_	pF	$V_{EB} = 2 \text{ V}, \text{ I}_{C} = 0, \text{ f} = 1 \text{ MHz}$
Natar A Dulas test						

Note: 1. Pulse test





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