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TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

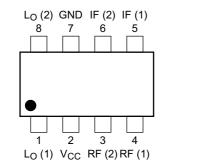
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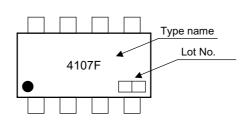
1 GHz Band Down Converter Application CATV Analog/Digital Tuner Terrestrial Digital TV Tuner

Features

- Low distortion at high RF signal input (IIP3): +13dBmW
- Performance at low Lo signal input: -5dBmW
- Double balanced Mix circuit
- Small package: SM8 (2.9×4.0)
- Recommended operating voltage: $VCC = 4.25 \sim 4.75 V$

Pin Connection, Marking





Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	5.5	V
Power dissipation	P _D (note)	375	mW
Operating temperature range	T _{opr}	-40~85	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: When mounted the glass epoxy board of 2.5 cm² \times 1.6 t

Caution

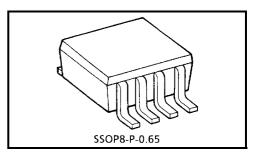
This device is electrostatic sensitivity. Please handle with caution.

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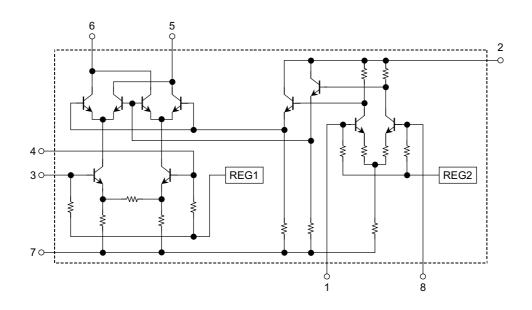
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Electrical Characteristics (V_{CC} = 4.5 V, Ta = 25°C, Zg = ZI = 50 Ω)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Circuit current	ICC	non carrier	22.5	29.5	40.5	mA
Conversion gain	C. Gain	RFin = 1 GHz/–15dBmW, Loin = 950 MHz/–5dBmW	-3.5	-0.5	3.5	dB
Input IP3	IIP3	RF (1) = 996 MHz/-15dBmw, RF (2) = 1000 MHz/-15dBmW, Loin = 950 MHz/-5dBmW	8	12	_	dBmW
Noise figure	NF	Loin = 950 MHz/-5dBmw, DSB	_	12	16	dB
$RF \to L_{O} \text{ Leakage power}$	$P_{RF \rightarrow Lo}$	RFin = 1 GHz/-15dBmW	_	-57		dBmW
$L_o \to RF \text{ Leakage power}$	$P_{Lo \ \rightarrow \ RF}$	Loin = 950 MHz/-5dBmW		-46		dBmW

Equivalent Circuit



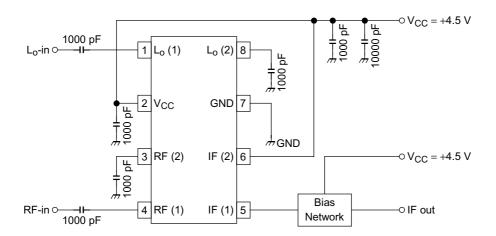
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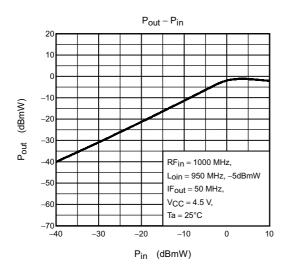
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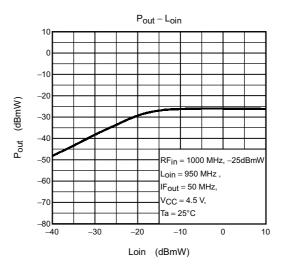
Test Circuit

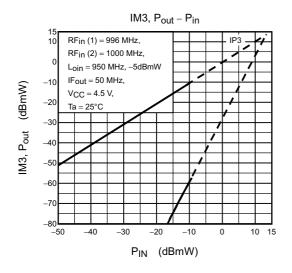


Please bias VCC, IF (1) and IF (2) terminals at the same time not to damage.

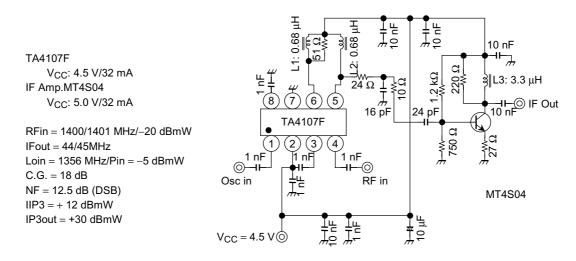
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Application circuit for CATV/DTV (VSB) Tuner



Notice

The circuits and measurements contained in this document are given only in the context of as examples of applications for these products.

Moreover, these example application circuits are not intended for mass production, since the high-frequency characteristics (the AC characteristics) of these devices will be affected by the external components which the customer uses, by the design of the circuit and by various other conditions.

It is the responsibility of the customer to design external circuits which correctly implement the intended application, and to check the characteristics of the design.

TOSHIBA assume no responsibility for the integrity of customer circuit designs or applications.

Package Dimensions

SSOP8-P-0.65

Unit : mm

