

OKI electronic components

OPA2048CA

Self-Scanning Line Sensor

GENERAL DESCRIPTION

The OPA2048CA is a 2,048-bit, one-dimensional diode array comprised of PN junction photodetector diodes and CCDs (charge coupled devices). By using a two-phase clock pulse, transfer pulse, and reset pulse, the OPA2048CA can measure incident light.

FEATURES

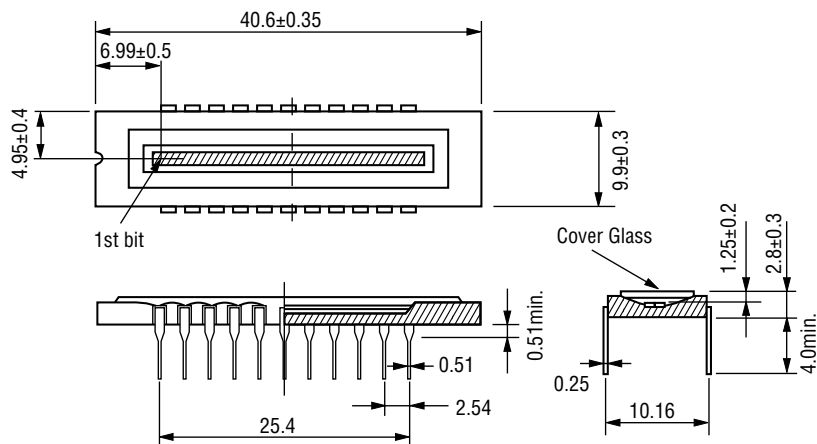
- Dynamic range: TYP 500
- CCD for high sensitivity
- CCD transfer efficiency greater than 99.995%
- Photodetector configured of PN junction photodetector diodes for good blue sensitivity and good output uniformity.
- Photodiodes highly integrated with 13 μm pitch for high resolution
- High-speed scanning
- Low-voltage operation
- Internal output amplifier and compensating amplifier

APPLICATIONS

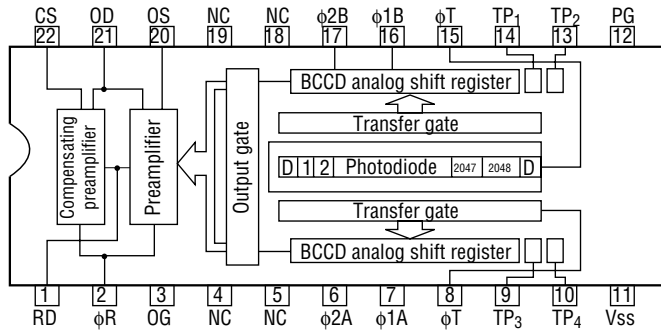
- Facsimiles
- OCRs
- Industrial control
- Monitoring devices
- Object recognition
- Measurement devices

PIN CONFIGURATION

• Dimensions

(Unit: mm)

• Pin Connection Block Diagram



Symbol	Name	Symbol	Name
RD	Reset transistor drain	V _{SS}	Substrate bias
φR	Reset transistor gate clock	TP ₁	Test pin (electrical input gate)
OG	Output gate	TP ₂	Test pin (electrical input diode)
φ1A	CCD register clock	TP ₃	Test pin (electrical input gate)
φ1B	CCD register clock	TP ₄	Test pin (electrical input diode)
φ2A	CCD register clock	PG	Photogate
φ2B	CCD register clock	OD	Output transistor drain
φT	Phototransfer gate clock	OS	Output transistor source (video output)
		CS	Compensating transistor source (noise output)

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Test Condition	Min.	Max.	Unit
Storage Temperature	T _{stg}	—	−40	+125	°C
Operating Temperature	T _{opr}	—	−20	+85	°C
Clock Voltage	V _φ	Ta=25°C	−0.3	+18	V
Applied Voltage	V _{DD}		−0.3	+18	V

ELECTRICAL AND OPTICAL CHARACTERISTICS

(Ambient Temperature Ta=25°C)

Parameter		Symbol		Guaranteed Value			Unit
				Min.	Typ.	Max.	
Reset Drain Voltage		V _{DD}	V _{RD}	11.4	12	12.6	V
Output Transistor Drain Voltage			V _{OD}	11.4	12	12.6	V
Output Gate Voltage		V _{OG}		4.75	5	5.25	V
Photo Gate Voltage		V _{PG}		4.75	5	5.25	V
Substrate Bias		V _{SS}		−1.5	−2	−2.5	V
Electrical Input Diode Voltage (TP _{2, 4})		V _{IS}		11.4	12	12.6	V
Electrical Input Gate Voltage (TP _{1, 3})		V _{IG}		−0.1	0	0.2	V
Spectral Sensitivity Range		λ _R		400	—	1000	nm
Reset Clock Pulse Frequency (Video Frequency)		φ _{fR} (fv)		0.25	1	*1 3	MHz
Clock Pulse Voltage	CCD register	"H"	V _{φ_{1,2}^{AB}H}	7	V _{DD}	V _{DD}	V
		"L"	V _{φ_{1,2}^{AB}L}	−0.3	0.4	0.8	V
	Phototransfer	"H"	V _{φ_TH}	7	V _{DD}	V _{DD}	V
		"L"	V _{φ_TL}	−0.3	0.4	0.8	V
	Reset	"H"	V _{φ_RH}	7	V _{DD}	V _{DD}	V
		"L"	V _{φ_RL}	−0.3	0.4	0.8	V
Clock Input Capacitance	CCD register	C _{φ_{1,2}^{AB}}		300	400	520	pF
	Phototransfer	C _{φ_T}		10	25	50	pF
	Reset	C _{φ_R}		2	4	8	pF
Load Resistance		R _L		0.9	1	1.5	kΩ
Output DC Level		V _{dc}		—	4.0	8.5	V
Power Consumption *2		P _D		50	70	100	mW

*1 When load capacitance C_L=31 pF

*2 When load resistance R_L=1 kΩ

POTOELECTRIC CHARACTERISTICS

(Ambient Temperature $T_a=25^{\circ}\text{C}$)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Saturation Exposure	Esat	—	0.23	—	$\ell x \cdot \text{sec}$	*1
Saturation Output Voltage	Vsat	180	250	—	mV	*2
Sensitivity	R_W	—	1.1	—	$\text{V}/\ell x \cdot \text{sec}$	*1
Output Nonuniformity	U_F	—	—	± 10	%	*3
Dark Output Voltage	Vd	—	0.5	3.1	mV	*4
Dynamic Range	DR	—	500	—	—	*2, *4

Test Conditions $\left\{ \begin{array}{l} V_{RD}=V_{OD}=V_{\phi}=12 \text{ V} \\ V_{PC}=V_{OG}=5 \text{ V} \\ V_{SS}=-2 \text{ V} \end{array} \right.$ TP2.4=12 V fV=1 MHz
 TP1.3=0 V TINT=10 ms
 $R_L=1 \text{ k}\Omega$

Conditions

*1 2856 K tungsten lamp

*3 Daylight fluorescent lamp 50% saturation exposure

*2 Daylight fluorescent lamp

*4 Dark state

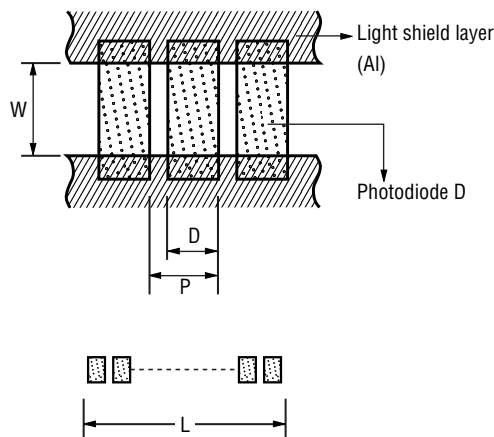
PIXEL CONFIGURATION

(Ambient Temperature $T_a=25^{\circ}\text{C}$)

Parameter	Symbol	Central Value	Accuracy	Unit
Arrangement	—	Straight line	—	—
Number of Pixels	—	2048	—	—
Pixel Pitch	P	13	± 2.0	μm
Photo Diode Width	D	8	± 2.0	μm
Aperture Width	W	13	± 2.5	μm
Sensor Length	L	26624	± 5.0	μm

* See "Output Timing" for information concerning dummy pixels.

Pixel Configuration Diagram

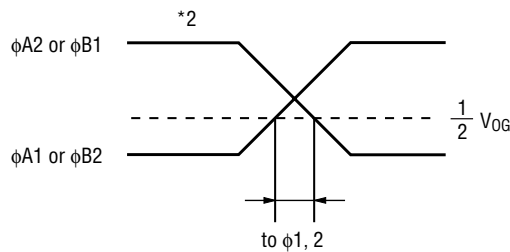
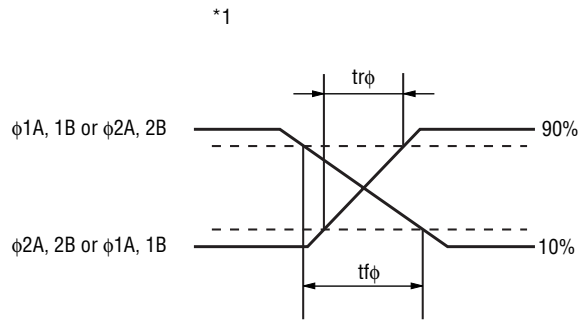


CLOCK INPUT TIMING CHARACTERISTICS

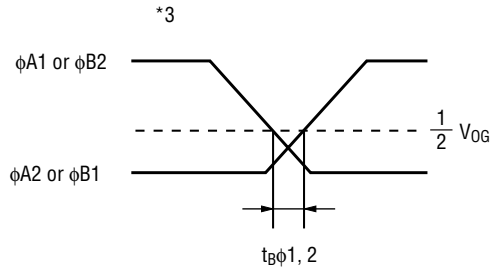
(Ambient Temperature Ta=25°C)

	Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Register	Rise time	$t_{r\phi}$	—	30	100	ns	*1
	Fall time	$t_{f\phi}$	—	20	100	ns	
	Overlap time	$t_{O\phi 1, 2}$	0	20	100	ns	*2
	Blank time	$t_{B\phi 1, 2}$	—	20	100	ns	*3
Phototransfer	Rise time	$t_{r\phi T}$	—	30	100	ns	*4
	Fall time	$t_{f\phi T}$	—	20	100	ns	
	Transfer time duration	$t_{W\phi r}$	5	10	15	μs	
	Setup time	$t_{S\phi T}$	0	1	10	μs	
	Hold time	$t_{H\phi T}$	0	1	10	μs	
Reset	Rise time	$t_{r\phi R}$	—	—	100	ns	*5
	Fall time	$t_{f\phi R}$	—	—	100	ns	
	Duration	$t_{W\phi R}$	80	—	2000	ns	
	Setup time	$t_{S\phi R}$	170	—	2000	ns	
	Hold time	$t_{H\phi R}$	0	—	2000	ns	

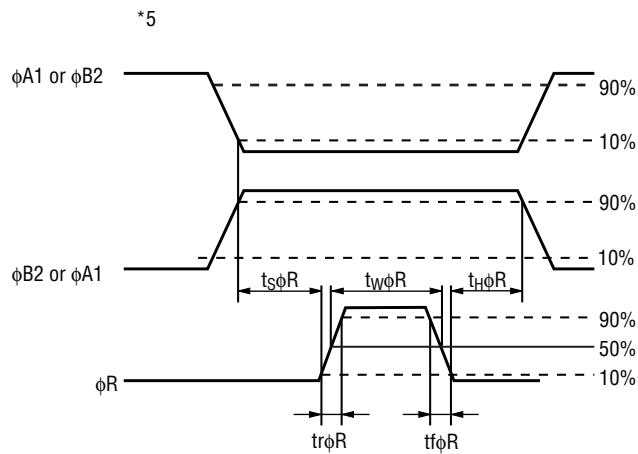
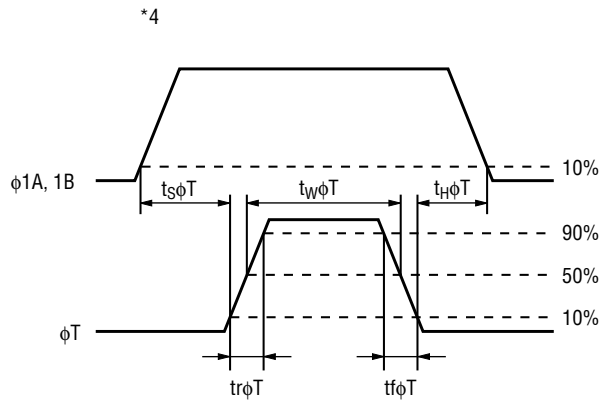
OPA2048CA Input Timing Diagrams



Limited to periods during $\phi A1$ rise and $\phi A2$ fall, and $\phi B2$ rise and $\phi B1$ fall.



Limited to periods during $\phi A2$ rise and $\phi A1$ fall, and $\phi B1$ rise and $\phi B2$ fall.



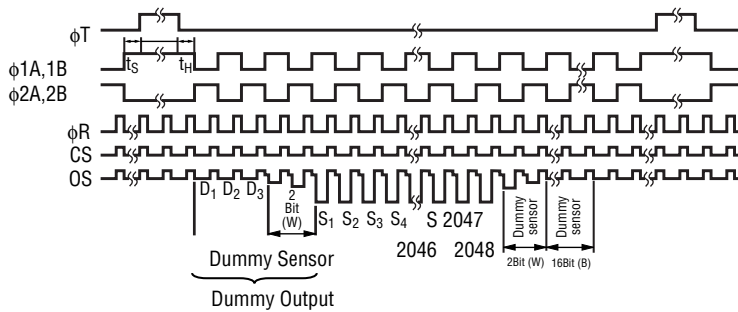
VIDEO OUTPUT TIMING CHARACTERISTICS

(Ambient Temperature Ta=25°C)

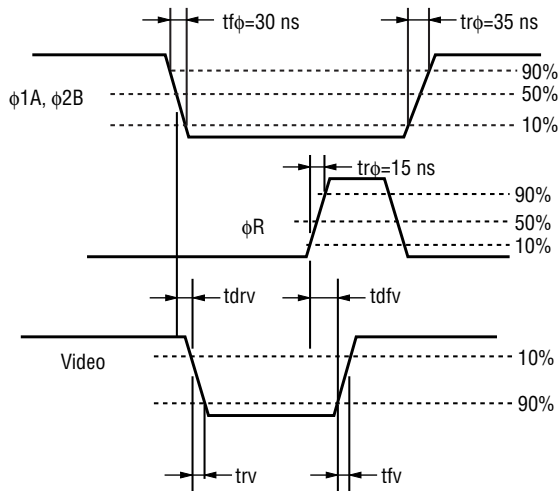
Parameter	Symbol	Gvaranteed Values			Unit	Note
		Min.	Typ.	Max.		
Video Rise Delay Time	tdrv	—	55	—	ns	—
Video Rise Time	trv	—	55	—	ns	—
Video Fall Delay Time	tdfv	—	15	—	ns	—
Video Fall Time	tfv	—	30	—	ns	—

Conditions $\left\{ \begin{array}{l} V_{DD}=V_{RD}=V_{\phi}=12\text{ V} \\ V_{DG}=V_{PG}=5\text{ V} \\ V_{SS}=-2\text{ V} \end{array} \right. \quad \begin{array}{l} R_L=1\text{ k}\Omega \\ C_L=31\text{ pF} \\ T_a=25^\circ\text{C} \end{array} \quad \begin{array}{l} t_{r\phi}=30\text{ ns} \\ t_{r\phi}=35\text{ ns} \\ t_{r\phi R}=15\text{ ns} \end{array}$

Input Timing Chart

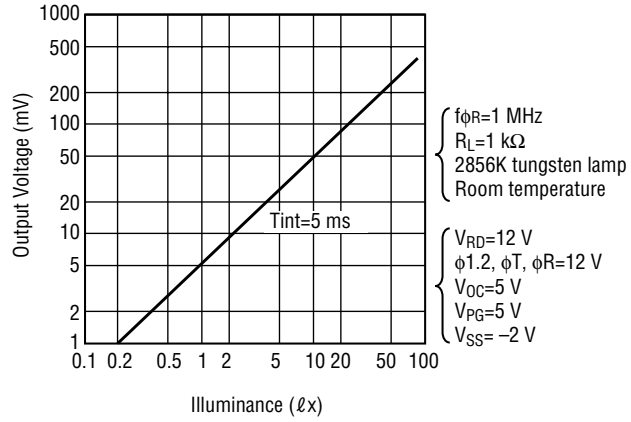


Output Timing Diagrams

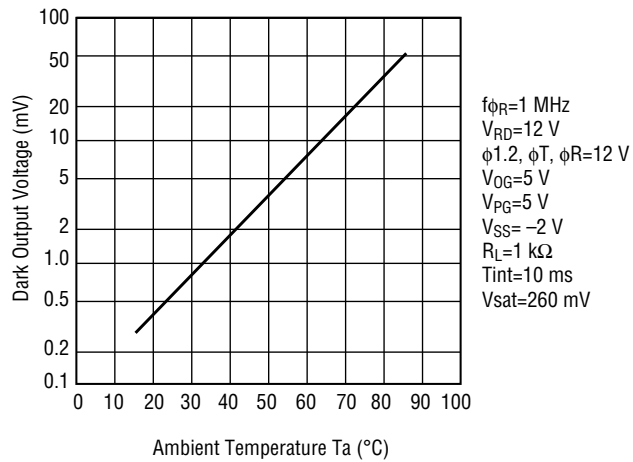


TYPICAL CHARACTERISTICS

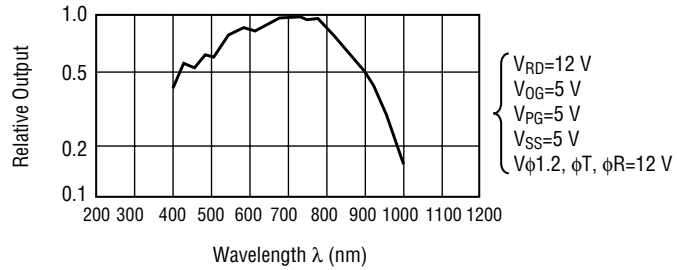
• Photoelectric Conversion Characteristic



• Dark Output Voltage vs. Temperature Characteristic



- Spectral Sensitivity Characteristic



- M.T.F. Characteristics (White Fluorescent Lamp)

