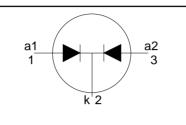
PBYR345CTD series

FEATURES

- · Low forward volt drop
- Fast switching
- Reverse surge capability
- High thermal cycling performance
- Low thermal resistance



SYMBOL

1

2

3

tab

QUICK REFERENCE DATA

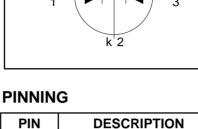
$$V_{R} = 40 \text{ V/ } 45 \text{ V}$$

 $I_{O(AV)} = 3 \text{ A}$
 $V_{F} \le 0.57 \text{ V}$

GENERAL DESCRIPTION

Dual schottky rectifier diodes intended for use as output rectifiers in low voltage, high frequency switched mode power supplies.

The PBYR345CTD series is supplied in the SOT428 surface mounting package.



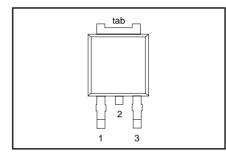
anode 1

cathode1

anode 2

cathode

SOT428



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT
.,		PBYR3		40CTD	45CTD	
V_{RRM}	Peak repetitive reverse voltage		-	40	45	V
V_{RWM}	Working peak reverse voltage		-	40	45	V
V _R	Continuous reverse voltage	$T_{mb} \le 115 \ ^{\circ}C$	-	40	45	V
I _{O(AV)}	Average rectified output current (both diodes conducting)	square wave; $\delta = 0.5$; $T_{mb} \leq 142 \ ^{\circ}C$	-	3		A
I _{FRM}	Repetitive peak forward current per diode	square wave; δ = 0.5; T _{mb} ≤ 142 °C	-	3		A
I _{FSM}	Non-repetitive peak forward	t = 10 ms	-	55		Α
	current per diode	t = 8.3 ms sinusoidal; T_j = 125 °C prior to surge; with reapplied V _{RRM(max)}	-	60		A
I _{RRM}	Peak repetitive reverse surge current per diode	pulse width and repetition rate limited by T _{i max}	-	1		A
T _j	Operating junction temperature	jinax	-	150		°C
T _{stg}	Storage temperature		- 65	175		°C

¹ it is not possible to make connection to pin 2 of the SOT428 package

PBYR345CTD series

THERMAL RESISTANCES

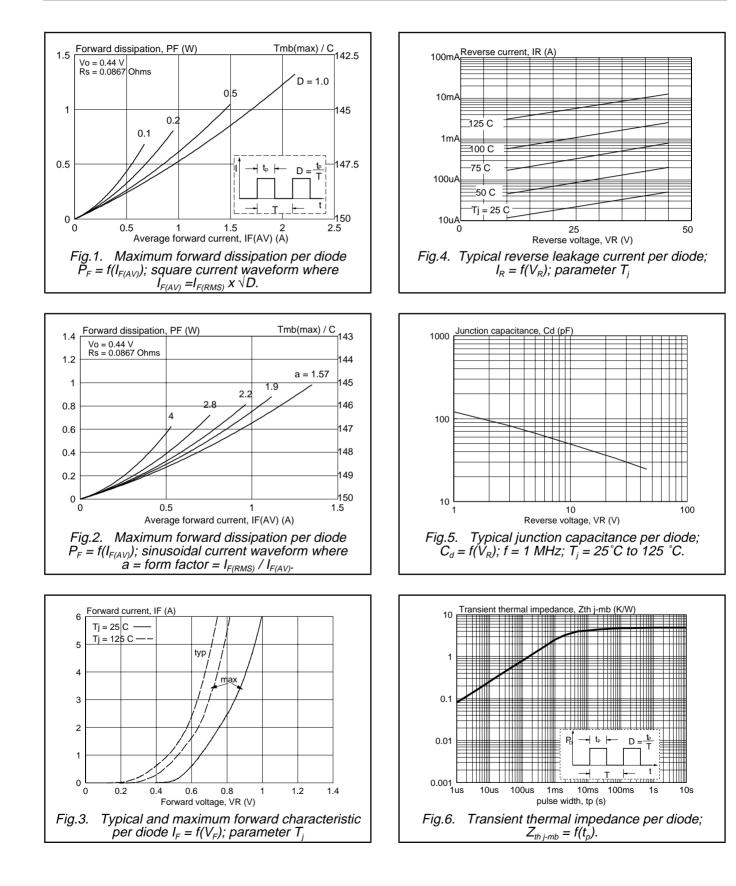
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R _{th j-mb} R _{th j-a}	Thermal resistance junction to mounting base Thermal resistance junction to ambient	per diode both diodes pcb mounted, minimum footprint, FR4 board	- -	- - 50	5 4 -	K/W K/W K/W

ELECTRICAL CHARACTERISTICS

All characteristics are per diode at $T_i = 25$ °C unless otherwise specified

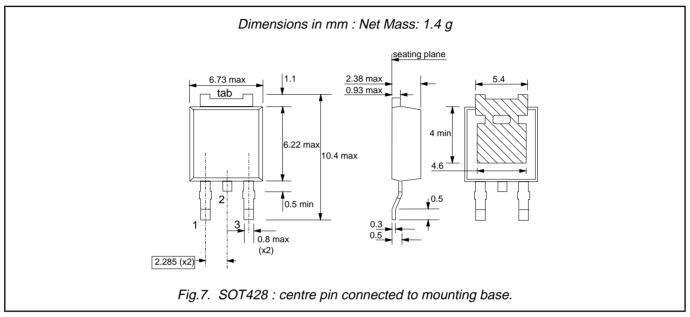
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _F	Forward voltage	I _F = 1.5 A; T _i = 125°C	-	0.51	0.57	V
		$I_{\rm F} = 3 \text{ A}; T_{\rm i} = 125 ^{\circ} \text{C}$	-	0.62	0.7	V
		$I_F = 3 A$	-	0.71	0.84	V
I _R	Reverse current	$\dot{V}_{R} = V_{RWM}$	-	0.05	0.2	mA
		V _R = V _{RWM} ; T _j = 100°C V _R = 5 V; f = 1 MHz, T _i = 25°C to 125°C	-	3.2	8	mA
C _d	Junction capacitance	$V_{R} = 5 \text{ V}; \text{ f} = 1 \text{ MHz}, \text{ T}_{j} = 25 ^{\circ}\text{C} \text{ to } 125 ^{\circ}\text{C}$	-	65	-	pF

PBYR345CTD series

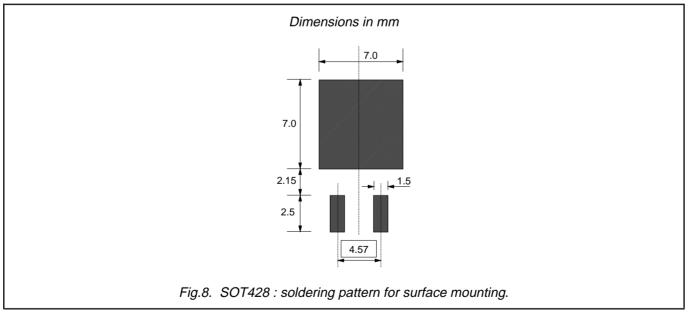


PBYR345CTD series

MECHANICAL DATA



MOUNTING INSTRUCTIONS



Notes

- Observe the general handling precautions for electrostatic-discharge sensitive devices (ESDs) to prevent damage to MOS gate oxide.
 Epoxy meets UL94 V0 at 1/8".

PBYR345CTD series

DEFINITIONS

Data sheet status				
Objective specification	bjective specification This data sheet contains target or goal specifications for product development.			
Preliminary specification	specification This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
Limiting values				
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.				
Application information				
Where application information is given, it is advisory and does not form part of the specification.				
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