Silicon N-Channel MOS FET

# HITACHI

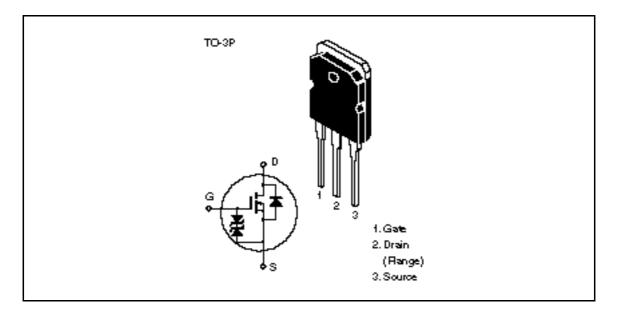
### Application

High speed power switching

#### Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

#### Outline





# Absolute Maximum Ratings (Ta = $25^{\circ}$ C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	900	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	I <sub>D</sub>	6	А
Drain peak current	I D(pulse) *1	15	А
Body to drain diode reverse drain current	I <sub>DR</sub>	6	А
Channel dissipation	Pch*2	100	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

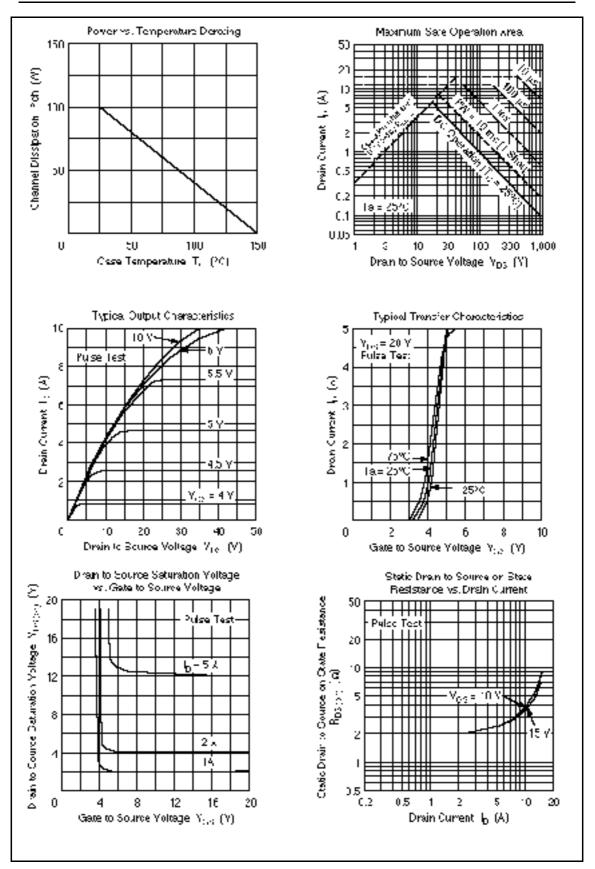
Notes: 1. PW 10 µs, duty cycle 1%

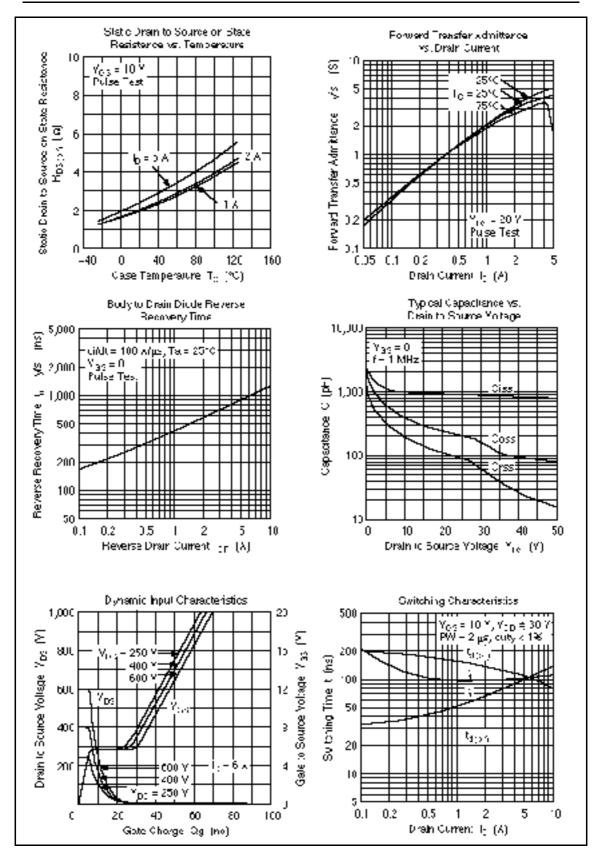
2. Value at  $T_c = 25^{\circ}C$ 

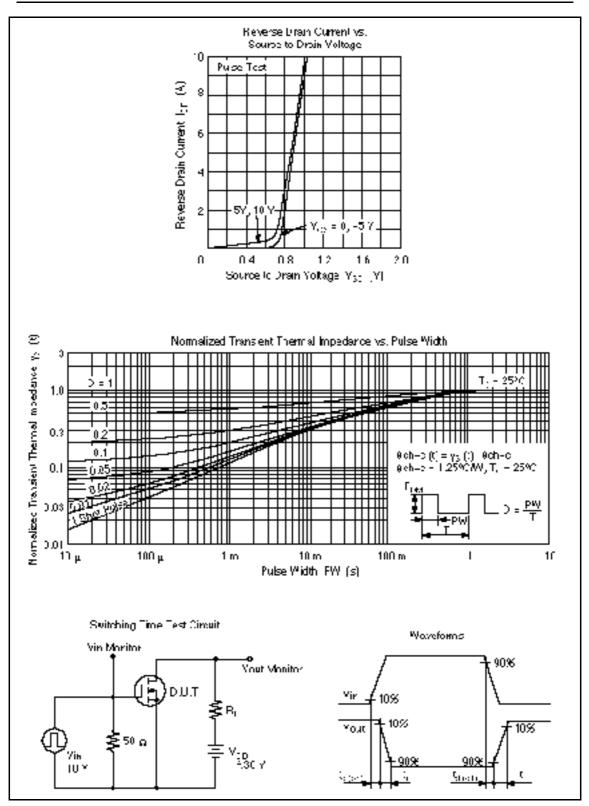
# **Electrical Characteristics** (Ta = $25^{\circ}$ C)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	900	_	_	V	$I_{\rm D} = 10$ mA, $V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(\text{BR})\text{GSS}}$	±30	_	_	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—	_	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>		_	250	μA	$V_{\rm DS} = 720 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	2.0	_	3.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{\text{DS(on)}}$	_	2.0	3.0		$I_{D} = 3 \text{ A}, \text{ V}_{GS} = 10 \text{ V}^{*1}$
Forward transfer admittance	yfs	2.3	3.7		S	$I_{\rm D} = 3$ A, $V_{\rm DS} = 20$ V * <sup>1</sup>
Input capacitance	Ciss	_	980	_	pF	$V_{\rm DS} = 10 \ V, \ V_{\rm GS} = 0,$
Output capacitance	Coss		400		pF	f = 1 MHz
Reverse transfer capacitance	Crss		195		pF	_
Turn-on delay time	t <sub>d(on)</sub>		20		ns	$I_{D} = 3 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time	t,		80		ns	R <sub>L</sub> = 10
Turn-off delay time	$t_{d(off)}$	_	125		ns	—
Fall time	t <sub>f</sub>		100		ns	_
Body to drain diode forward voltage	$V_{DF}$	_	0.9	_	V	$I_{\rm F} = 6 \text{ A}, V_{\rm GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	_	1000	_	ns	I <sub>F</sub> = 6 A, V <sub>GS</sub> = 0, di <sub>F</sub> /dt = 100 A/μs
Nata: 1 Dulas test						

Note: 1. Pulse test







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