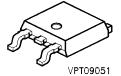
SPUX3N60S5 SPDX3N60S5

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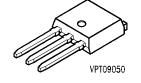
Cool MOS™ Power Transistor

- Worldwide best RDS(on) in D-Pack
- N-Channel
- Enhancement mode
- Ultra low gate charge
- Avalanche rated
- dv/dt rated
- 150°C operating temperature



1

G



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S

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D

Туре	V _{DS}	I _D	R _{DS(on)}	Marking	Package	Ordering Code
SPUX3N60S5	600 V	7.3 A	$600~\text{m}\Omega$	X3N60S5	P-TO251-3-1	-
SPDX3N60S5					P-TO252	_

Maximum Ratings, at T_i = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Drain source voltage	V _{DSS}	600	V
Continuous drain current	I _D		А
$T_{\rm C}$ = 25 °C		7.3	
$T_{\rm C} = 100 {\rm ^{\circ}C}$		4.6	
Pulsed drain current	I _{D puls}	14.6	
$T_{\rm C}$ = 25 °C			
Avalanche energy, single pulse	E _{AS}	230	mJ
$I_{\rm D} = 7.3 \; {\rm A}, \; V_{\rm DD} = 50 \; {\rm V}, \; R_{\rm GS} = 25 \; {\rm \Omega}$			
Avalanche current (periodic, limited by T_{jmax})	I _{AR}	tbd	А
Avalanche energy (10 kHz, limited by T _{jmax})	E _{AR}	tbd	mJ
Reverse diode dv/dt	d <i>v</i> /d <i>t</i>	6	KV/µs
$I_{S} = 7.3 \text{ A}, V_{DS} < V_{DSS}, di/dt = 100 \text{ A/}\mu\text{s},$			
$T_{\text{jmax}} = 150 ^{\circ}\text{C}$			
Gate source voltage	V_{GS}	±20	V
Power dissipation, $T_{\rm C}$ = 25 °C	P _{tot}	83	W
Operating temperature	$T_{\rm j}$	-55+150	°C
Storage temperature	$T_{ m stg}$	-55 +150	
IEC climatic category; DIN IEC 68-1		55/150/56	

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Electrical Characteristics

Parameter	Symbol	Values			Unit
at $T_i = 25$ °C, unless otherwise specified		min.	typ.	max.	
Thermal Characteristics	•				•
Thermal resistance, junction - case	R _{thJC}	ı	-	1.5	K/W
Thermal resistance, junction - ambient	R _{thJA}	-	100	-	
(Leaded and through-hole packages)					
SMD version, device on PCB:	R _{thJA}				
@ min. footprint		-	tbd	-	
@ 6 cm ² cooling area ¹⁾		-	tbd	-	
Static Characteristics					
Drain- source breakdown voltage	V _{(BR)DSS}	600	-	-	V
$V_{GS} = 0 \text{ V}, I_{D} = 0.25 \text{ mA}$					
Gate threshold voltage, $V_{GS} = V_{DS}$	V _{GS(th)}				
$I_{\rm D} = 350 \ \mu {\rm A}, \ T_{\rm j} = 25 \ {\rm ^{\circ}C}$		3.5	4.5	5.5	
$I_{\rm D} = 350 \ \mu {\rm A}, \ T_{\rm j} = 150 \ {\rm ^{\circ}C}$		tbd	-	-	
Zero gate voltage drain current, $V_{\rm DS}=V_{\rm DSS}$	I _{DSS}				μΑ
$V_{GS} = 0 \text{ V}, T_{j} = -40 ^{\circ}\text{C}$		-	-	0.1	
$V_{GS} = 0 \text{ V}, T_{j} = 25 \text{ °C}$		-	0.5	1	
$V_{\rm GS}$ = 0 V, $T_{\rm j}$ = 150 °C		-	-	tbd	
Gate-source leakage current	I _{GSS}	-	10	100	nA
$V_{GS} = 20 \text{ V}, \ V_{DS} = 0 \text{ V}$					
Drain-Source on-state resistance	R _{DS(on)}	-	tbd	600	mΩ
$V_{GS} = 10 \text{ V}, I_{D} = 4.6 \text{ A}$					

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Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6 cm2 (one layer, 70μm thick) copper area for drain connection. PCB is vertical without blown air.

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Electrical Characteristics

Parameter	Symbol	Values			Unit
at T_i = 25 °C, unless otherwise specified		min.	typ.	max.	1
Characteristics	<u>, </u>	•	•	•	-1
Transconductance	g _{fs}	-	tbd	-	S
$V_{\text{DS}} \ge 2 * I_{\text{D}} * R_{\text{DS(on)max}}$, $I_{\text{D}} = 4.6 \text{ A}$					
Input capacitance	C_{iss}	-	980	tbd	pF
$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$					
Output capacitance	$C_{ m oss}$	-	630	tbd	
$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$					
Reverse transfer capacitance	C _{rss}	-	33	tbd	
$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$					
Turn-on delay time	$t_{d(on)}$	-	tbd	tbd	ns
$V_{\text{DD}} = 350 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 7.3 \text{ A},$					
$R_{\rm G}$ = 12 Ω					
Rise time	t _r	-	tbd	-	
$V_{\text{DD}} = 350 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 7.3 \text{ A},$					
$R_{\rm G}$ = 12 Ω					
Turn-off delay time	t _{d(off)}	-	tbd	tbd	
$V_{\rm DD}$ = 350 V, $V_{\rm GS}$ = 10 V, $I_{\rm D}$ = 7.3 A,					
R_{G} = 12 Ω					
Fall time	t _f	-	tbd	-	
$V_{\rm DD} = 350 \; \rm V, \; V_{\rm GS} = 10 \; \rm V, \; I_{\rm D} = 7.3 \; \rm A,$					
R_{G} = 12 Ω					

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Electrical Characteristics

Parameter	Symbol	Values			Unit
at T_{j} = 25 °C, unless otherwise specified		min.	typ.	max.	
Gate Charge Characteristics		•	•		•
Gate-source charge	Q_{gs}	-	tbd	-	nC
$I_{D} = 7.3 \text{ A}, \ V_{DD} = 400 \text{ V}$					
Gate-drain charge	$Q_{ m gd}$	-	tbd	-	
$I_{D} = 7.3 \text{ A}, \ V_{DD} = 400 \text{ V}$					
Total gate charge	Q_G	-	32	tbd	
$V_{\rm DD} = 400 \ {\rm V}, \ I_{\rm D} = 7.3 \ {\rm A}, \ V_{\rm GS} = 0 \ {\rm to} \ 10 \ {\rm V}$					
Reverse Diode Continuous source current	I _S	-	-	7.3	А
T _C = 25 °C				44.0	-
Pulsed source current $T_{\rm C}$ = 25 °C	I _{SM}	-	-	14.6	
Inverse diode forward voltage	V _{SD}	-	tbd	1.2	V
$V_{GS} = 0 \text{ V}, I_{F} = 7.3 \text{ A}$					
Reverse recovery time	t _{rr}	-	tbd	-	ns
$V_{R} = 100 \text{ V}, I_{F} = I_{S}, di_{F}/dt = 100 \text{ A/}\mu\text{s}$					
Reverse recovery charge	Q _{rr}	-	tbd	-	μC
$V_{R} = 100 \text{ V}, I_{F} = I_{S}, di_{F}/dt = 100 \text{ A/}\mu\text{s}$					

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