

B82790C0*/K0*N2

Double Chokes

B82790S0*/L0*N2



Rated voltage 42 VAC/80 DC Rated current 200 to 1000 mA Rated inductance 5 µH to 4,7 mH



Construction

- Current-compensated ring core choke with ferrite core
- Bifilar winding (B82790C0*/K0*)
- Sector winding (B82790S0*/L0*)

Features

- Case flame-retardant as per UL 94 V-0
- Suitable for reflow soldering

Special types for conductive adhesion and ambient temperatures up to 150°C on request

Applications

- B82790C0*/K0*: Suppression of asymmetrical interference coupled in on lines, whereas data signals up to some MHz can pass unaffectedly
- B82790S0*/L0*: Suppression of asymmetrical and symmetrical interference coupled in on lines. The high-frequency portions of the symmetrical data signal are decreased so far that EMC problems can be significantly reduced.

Terminals

Lead-free tinned

Marking

Manufacturer, ordering code (short form), date of manufacture, coded (year, day of week, calender week)

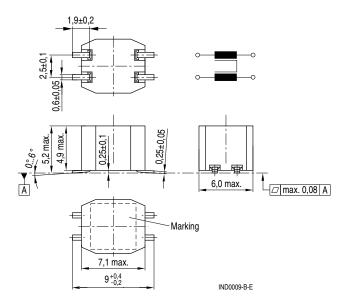
Delivery mode

Blister tape, reel packing For details on taping, packing and packing units see data book 2000 "Chokes and Inductors", page 302

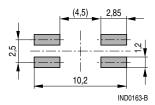




Dimensional drawing



Layout recommendation





Chokes for Data and Signal Lines	B82790C0*/K0*N2
Double Chokes	B82790S0*/L0*N2

Technical data and measuring conditions

Poted voltage V	40 VAC (E0/60 Hz)		
Rated voltage V_{R}	42 VAC (50/60 Hz)		
	80 VDC		
Rated current I _R	Referred to 50 Hz and 60 °C ambient temperature		
Rated inductance $L_{\rm R}$	Measured with HP 4275A		
	at $L \le 1 \text{ mH} = 100 \text{ kHz}, 0,1 \text{ mA}$		
	L > 1 mH = 10 kHz, 0,1 mA		
	(specified per winding)		
Inductance tolerance	<i>L</i> ≤ 0,47 mH: ± 30 %		
	L > 0,47 mH: - 30/+ 50 %		
Inductance decrease $\Delta L/L_0$	< 10 % at dc magnetic bias with I _R		
Stray inductance L _S	Measured with HP 4275A.		
	Measuring frequency at $L \le 11 \mu H = 1 \text{ MHz}$, 5 mA		
	$L > 11 \mu H = 100 \text{kHz}, 5 \text{mA}$		
DC resistance R _{typ}	Typical values, measured at 20 °C ambient temperature		
Solderability	(215 ± 3) °C, (3 ± 0,3) s		
	wetting of soldering area ≥ 95 %		
	in accordance with IEC 60068-2-58		
Climatic category	40/125/56 (- 40 °C/+ 125 °C/56 days damp heat test)		
5 .	in accordance with IEC 60068-1		
Weight	Approx. 0,3 g		

Characteristics and ordering codes

L _R mH	L _{S, typ} nH	I _R mA	$R_{ m typ}$ m Ω	V _T VDC, 2 s	Ordering code ¹⁾
0,005	50	1000	100	250	B82790C0502N201
0,011	50	500	120	250	B82790C0113N201
0,025	150	500	130	250	B82790C0253N201
0,025	1500	500	130	250	B82790S0253N201
0,051	200	500	160	250	B82790C0513N201
0,051	2000	500	160	250	B82790S0513N201
0,470	200	500	200	750	B82790C0474N215
1,0	250	500	200	750	B82790C0105N240
2,2	250	400	400	750	B82790C0225N265
4,7	300	200	550	750	B82790C0475N265

¹⁾ Special types for conductive adhesion and ambient temperatures of up to 150°C upon request.



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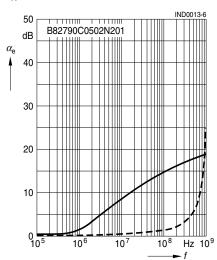
SMD

Insertion loss α_e (typical values at $Z = 50 \Omega$)

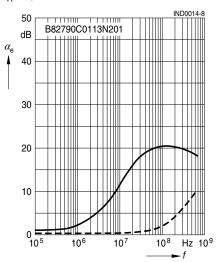
asymmetrical, all branches in parallel (common mode)

symmetrical (differential mode)

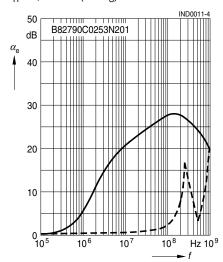
$$L_{\rm R} = 0,005 \text{ mH}$$



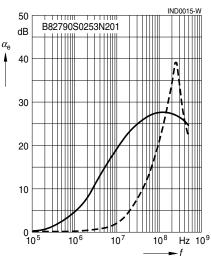
$$L_{\rm R} = 0.011 \, \rm mH$$







 $L_{\rm R} = 0.025 \text{ mH (high } L_{\rm S})$





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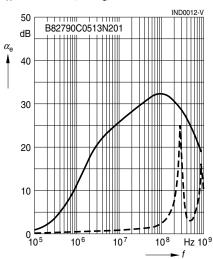
SMD

Insertion loss α_e (typical values at $Z = 50 \Omega$)

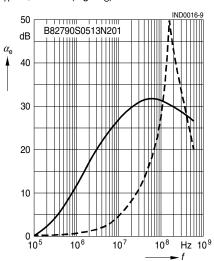
asymmetrical, all branches in parallel (common mode)

symmetrical (differential mode)

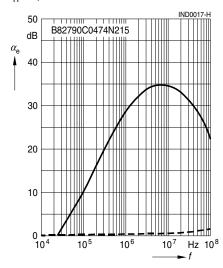
$$L_{\rm B} = 0.051 \text{ mH (low } L_{\rm S})$$



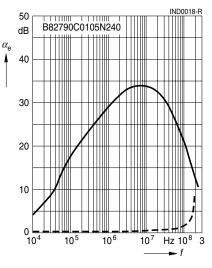
$$L_{\rm R} = 0.051 \text{ mH (high } L_{\rm S})$$







 $L_{\rm R} = 1.0 \, {\rm mH}$





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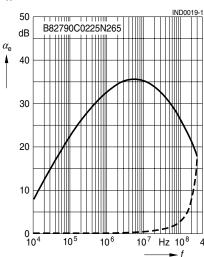
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Insertion loss α_e (typical values at $Z = 50 \Omega$)

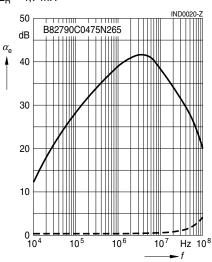
asymmetrical, all branches in parallel (common mode)

- - - - - - symmetrical (differential mode)





$$L_{\rm R} = 4.7 \, \rm mH$$



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