

## ADJUSTABLE 3-TERMINAL POSITIVE VOLTAGE REGULATOR

### ■ GENERAL DESCRIPTION

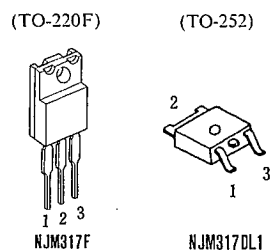
The NJM317 is adjustable 3-terminal positive voltage regulator IC. It is capable of adjustment from typical 1.25V to 37V output voltage range with two resistors. It is capable of supplying in excess of 1.5A with heat sink.

The NJM317 is suitable for the power supply of VCR, CD player and others.

### ■ FEATURES

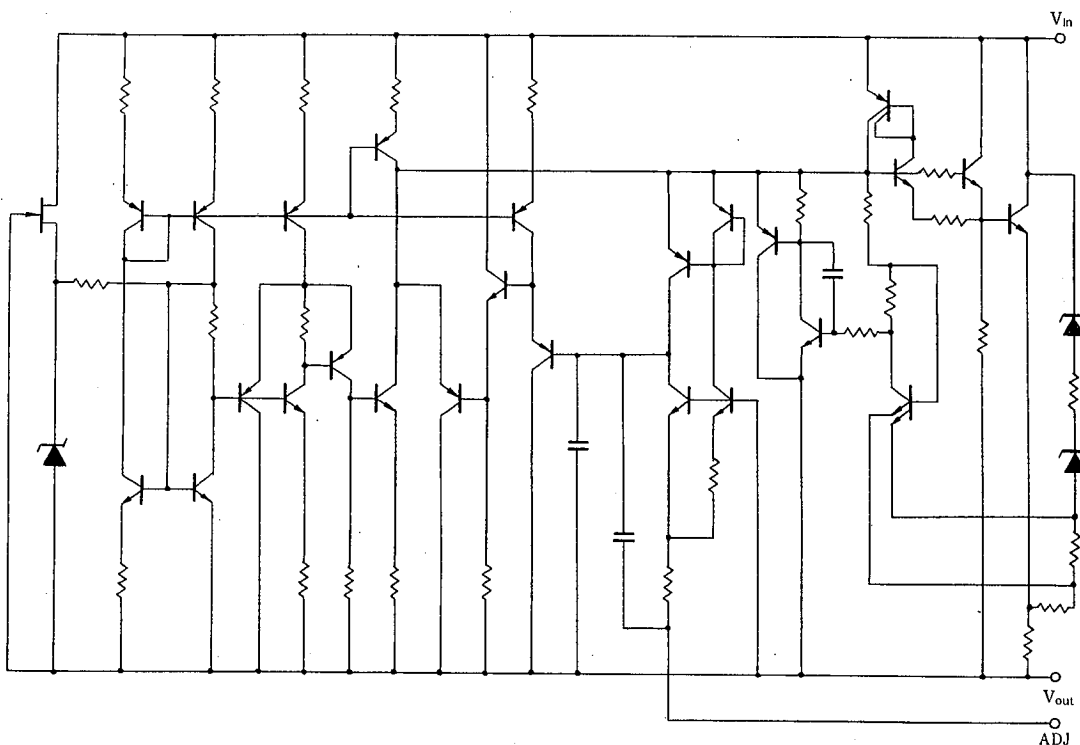
- Operating Voltage (+4.25V~+40V)
- Adjustable Output Down to 1.2V
- Guarantee'd 1.5A Output Current
- Line Regulation typically (0.01%/V)
- Load Regulation typically (0.1%)
- 80dB Ripple Rejection
- Package Outline TO-220F
- Bipolar Technology

### ■ PACKAGE OUTLINE



1. Adjustment
2. Output
3. Input

### ■ EQUIVALENT CIRCUIT



## ■ ABSOLUTE MAXIMUM RATINGS

(T<sub>a</sub>=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Input-Output Differential Voltage	V <sub>IN</sub> -V <sub>O</sub>	40(T <sub>a</sub> =25°C)	V
Power Dissipation	P <sub>D</sub>	TO-220F 16(T <sub>C</sub> ≤70°C) TO-252 10(T <sub>C</sub> ≤25°C) 1(T <sub>a</sub> ≤25°C)	W
Operating Temperature Range(Junction) (Ambient)	T <sub>opr(j)</sub> T <sub>opr(a)</sub>	-40~+150 -40~+85	°C
Storage Temperature Range	T <sub>stg</sub>	-50~+150	°C

## ■ THERMAL CHARACTERISTICS

		TO-220F	TO-252	°C/W
Thermal Resistance	Junction-To-Ambient	60	125	
	Junction-To-Case	5	12.5	

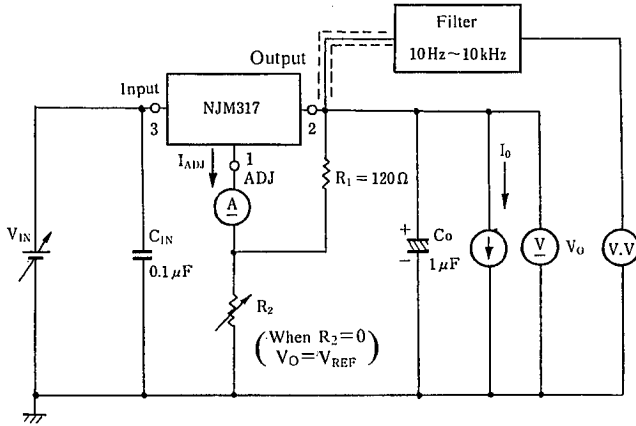
## ■ ELECTRICAL CHARACTERISTICS (V<sub>IN</sub>-V<sub>O</sub>=5V, I<sub>O</sub>=500mA, C<sub>IN</sub>=0.1 μF, C<sub>O</sub>=1 μF, T<sub>J</sub>=25°C)

Measurement is to be conducted in pulse testing.

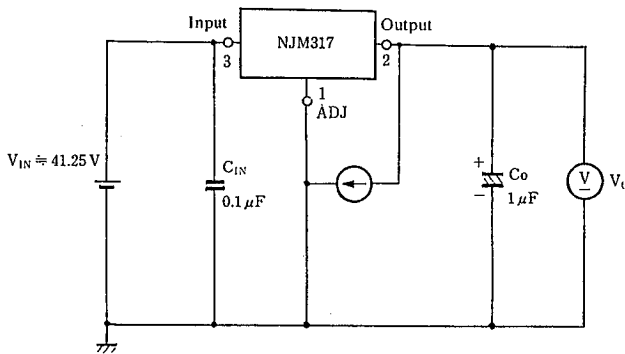
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Reference Voltage	V <sub>REF</sub>		1.2	1.25	1.3	V
	V <sub>REF</sub> -V <sub>IN</sub>	3V ≤ (V <sub>IN</sub> -V <sub>O</sub> ) ≤ 40V, I <sub>O</sub> =100mA	1.2	1.25	1.3	
	V <sub>REF</sub> -I <sub>O</sub>	10mA ≤ I <sub>O</sub> ≤ 1.5A (TO-220F) 10mA ≤ I <sub>O</sub> ≤ 500mA (TO-252)	1.2	1.25	1.3	
Reference Voltage Thermal Change	ΔV <sub>REF</sub> -T	0 ≤ T <sub>J</sub> ≤ 125°C	-	5	-	mV
Adjustment Pin Current	I <sub>ADJ</sub>		-	50	100	μA
Adjustment Pin Current Change	ΔI <sub>ADJ</sub> -V <sub>IN</sub>	3V ≤ (V <sub>IN</sub> -V <sub>O</sub> ) ≤ 40V, I <sub>O</sub> =100mA	-	0.2	5	μA
	ΔI <sub>ADJ</sub> -I <sub>O</sub>	10mA ≤ I <sub>O</sub> ≤ 1.5A (TO-220F) 10mA ≤ I <sub>O</sub> ≤ 500mA (TO-252)	-	0.2	5	
			-	0.2	5	
Line Regulation	ΔV <sub>O</sub> -V <sub>IN</sub>	3V ≤ (V <sub>IN</sub> -V <sub>O</sub> ) ≤ 40V, I <sub>O</sub> =100mA	-	0.01	0.04	%/V
Load Regulation	ΔV <sub>O</sub> -I <sub>O</sub>	10mA ≤ I <sub>O</sub> ≤ 1.5A (TO-220F)	-	-	-	mV %
		10mA ≤ I <sub>O</sub> ≤ 500mA (TO-252)	-	-	-	
		V <sub>O</sub> ≤ 5V V <sub>O</sub> > 5V	-	5 0.1	25 0.5	
Minimum Load Current	I <sub>O(MIN)</sub>	(V <sub>IN</sub> -V <sub>O</sub> )=40V	-	3.5	10	mA
Peak Output Current	I <sub>O(PEAK)</sub>	5V ≤ (V <sub>IN</sub> -V <sub>O</sub> ) ≤ 15V	1.5	2.2	-	A
		(V <sub>IN</sub> -V <sub>O</sub> )=40V	0.15	0.4	-	
RMS Output Noise Voltage	V <sub>NO</sub>	10Hz ≤ f ≤ 10kHz(RMS)	-	0.001	-	%/V <sub>O</sub>
Ripple Rejection Ratio	RR	V <sub>O</sub> =10V, f=120Hz, ΔV <sub>IN</sub> =1Vrms	-	65	-	dB
		C <sub>ADJ</sub> =0	-	66	-	
		C <sub>ADJ</sub> =10 μF	66	80	-	

■ TEST CIRCUIT

- 1) (Reference Voltage Thermal Change), (Adjustment Pin Current Change), (Line Regulation), (Load Regulation), (Peak Output Current), (RMS Output Noise Current)

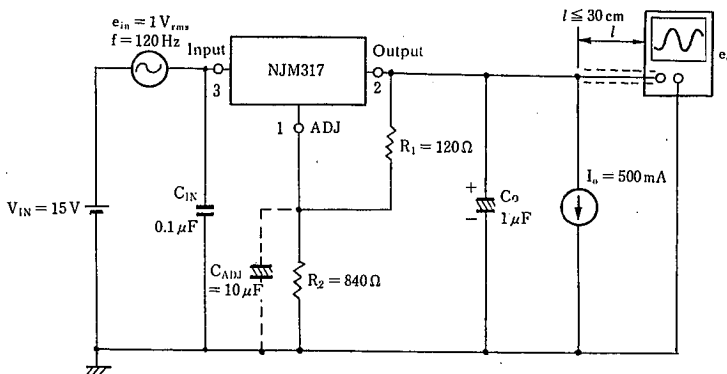


- 2) Minimum Load Current



I<sub>OMIN</sub>: Minimum I<sub>O</sub> for  
 $V_O = V_{REF}$  (Typical 1.25V)  
 $(V_{IN} = 40 + V_{REF})$

- 3) Ripple Rejection

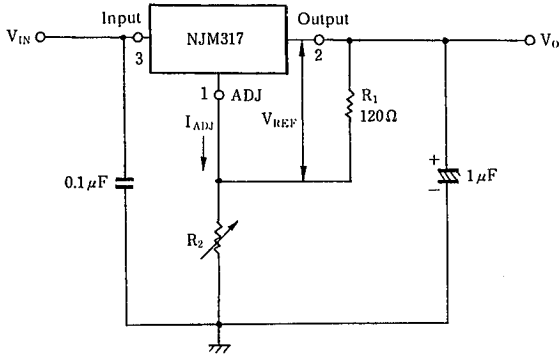


$$\text{Ripple Rejection} = 20 \log_{10} \left( \frac{e_{IN}}{e_o} \right) \text{ [dB]}$$

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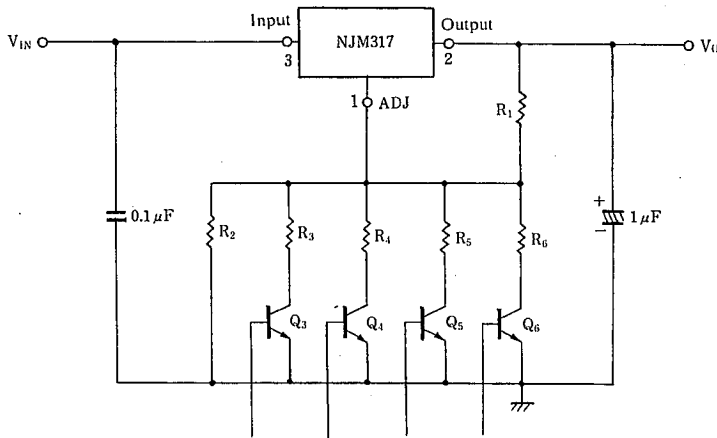
## ■ TYPICAL APPLICATIONS

1).  $V_O = 1.25V \sim 37V$  Adjustable Voltage Regulator



$$V_O = V_{REF} \times \left(1 + \frac{R_2}{R_1}\right) + R_2 \times I_{ADJ}$$

2) Selected Output Voltage



The transistors  $Q_3$  are switched by selective signal inputs and the output voltage  $V_O$  is controlled by the transistor on or off.

(Example)

When all transistor is off,

$$V_O \approx V_{REF} \times \left(1 + \frac{R_2}{R_1}\right)$$

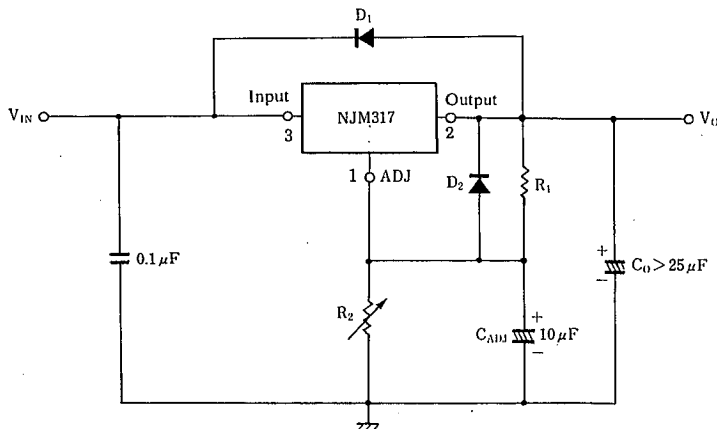
When the transistor  $Q_3$  is on, and others are off.

$$V_O \approx V_{REF} \times \left\{1 + \frac{R_2 \times R_3}{(R_2 + R_3) \times R_1}\right\}$$

※  $I_{ADJ}$  ignore.

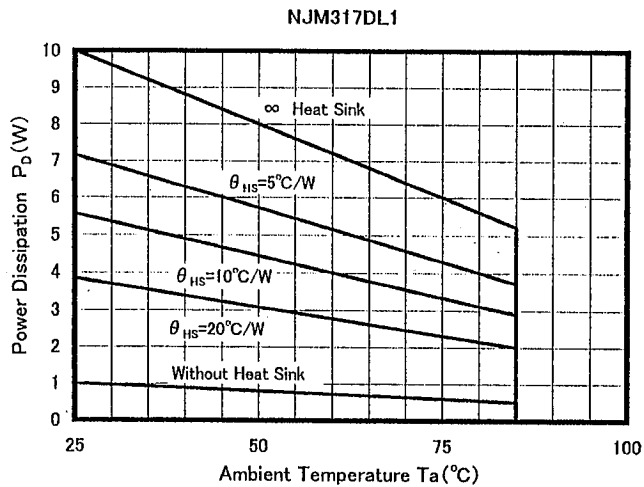
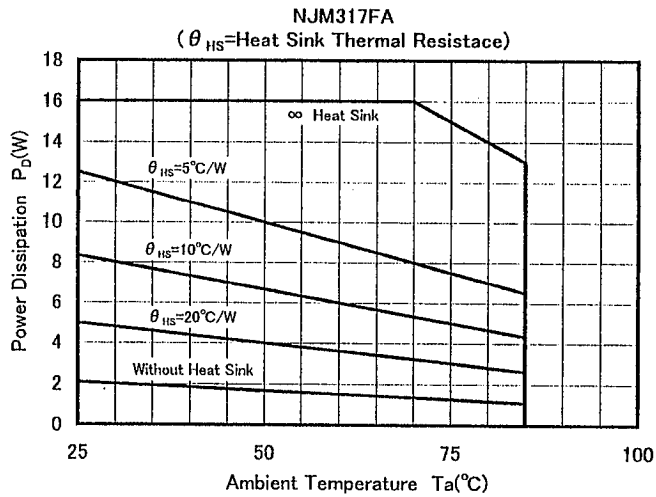
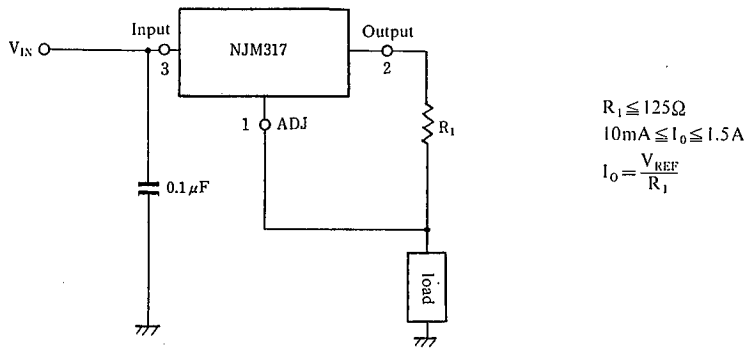
Selective Signal Inputs

3). Regulator with Protection Diodes

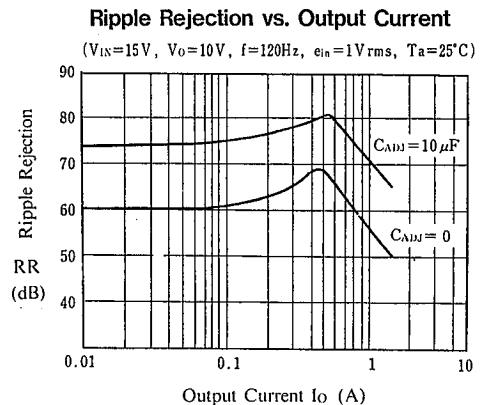
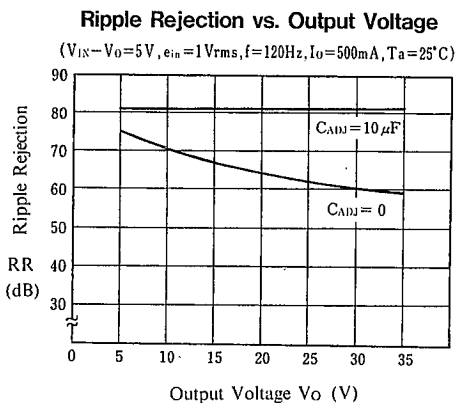
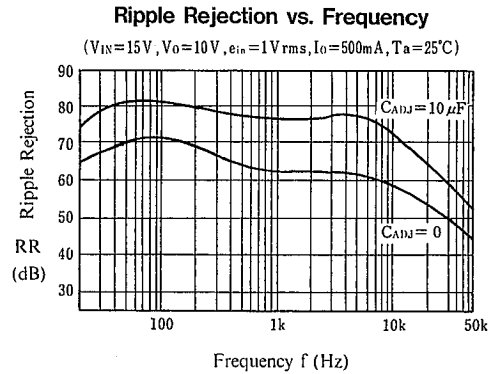
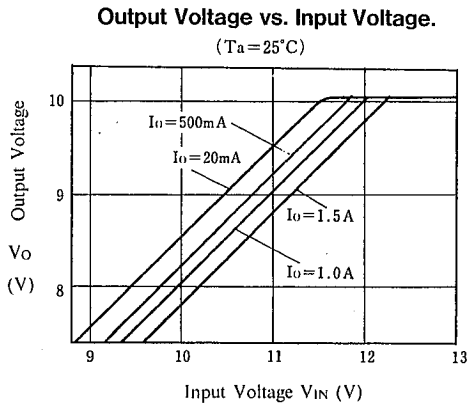
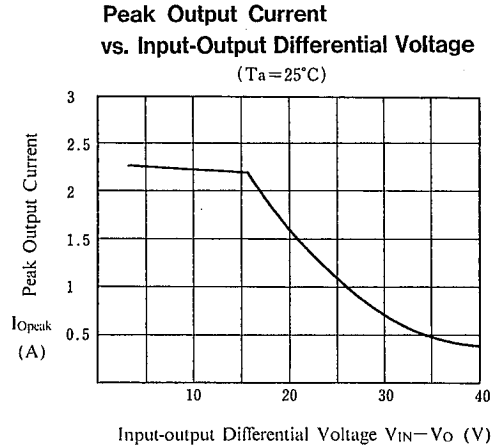
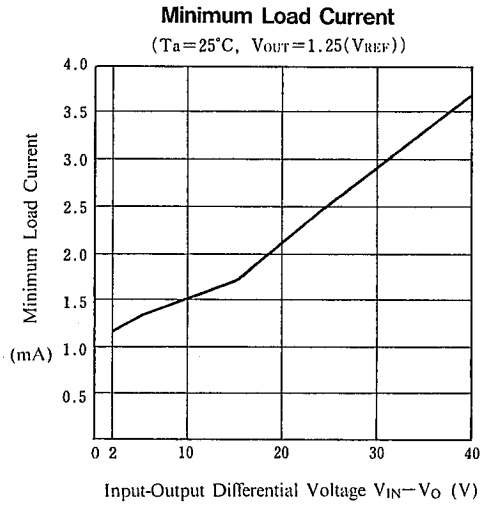


$D_1$  protects about  $C_O$   
 $D_2$  protects about  $C_{ADJ}$

## 4) Constant Current Regulator



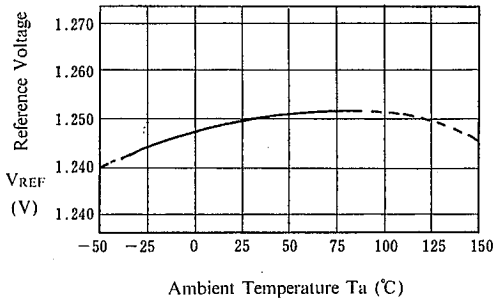
## ■ TYPICAL CHARACTERISTICS



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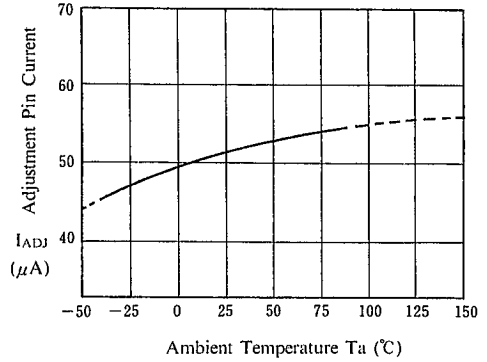
Reference Voltage vs. Temperature

( $V_{IN} - V_0 = 5V$ ,  $I_0 = 500nA$ , Pulse Test)



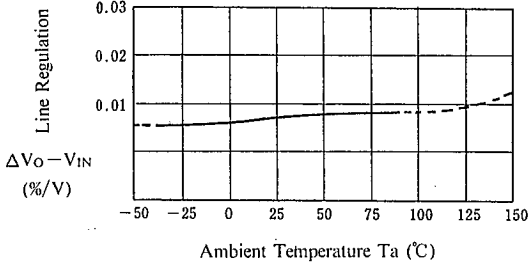
Adjustment Pin Current vs. Temperature

( $V_{IN} - V_0 = 5V$ ,  $I_0 = 500mA$ )



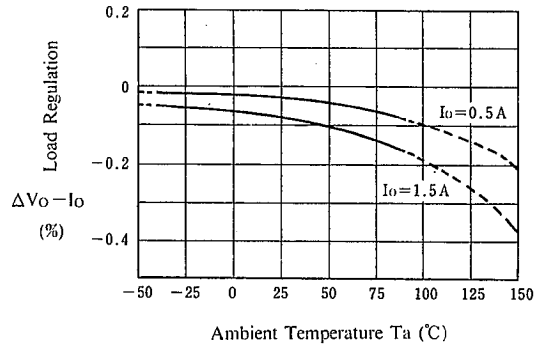
Line Regulation vs. Temperature

( $V_{IN} = 8V \sim 45V$ ,  $V_0 = 5V$ ,  $I_0 = 100mA$ , Pulse Test)



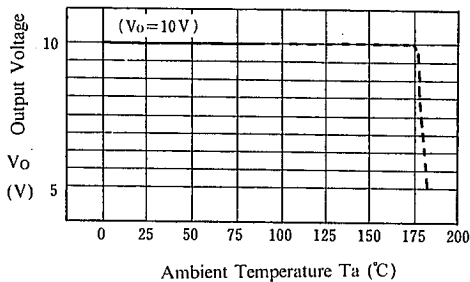
Load Regulation vs. Temperature

( $V_{IN} = 15V$ ,  $V_0 = 10V$ , Pulse Test)



Thermal Shutdown

( $V_{IN} = 15V$ ,  $V_0 = 10V$ ,  $I_0 = 0mA$ )



## MEMO

[CAUTION]

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