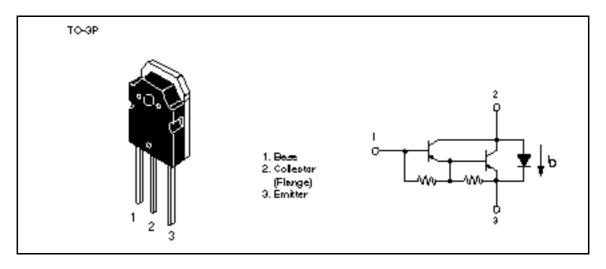
Silicon PNP Triple Diffused

# HITACHI

### Application

Low frequency power amplifier complementary Pair with 2SD2256

### Outline





# **Absolute Maximum Ratings** (Ta = $25^{\circ}$ C)

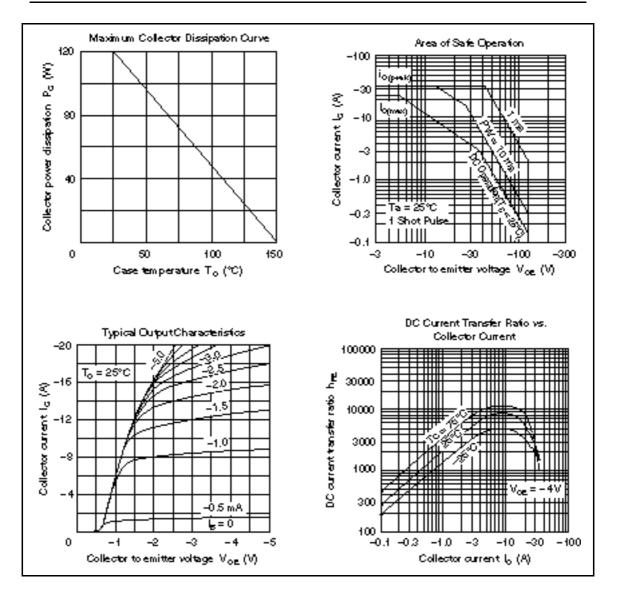
Item	Symbol	Ratings	Unit	
Collector to base voltage	V <sub>CBO</sub>	-120	V	
Collector to emitter voltage	V <sub>CEO</sub>	V <sub>CEO</sub> -120		
Emitter to base voltage	$V_{EBO}$	-7	V	
Collector current	Ι <sub>c</sub>	-25	А	
Collector peak current	I <sub>C(peak)</sub>	-35	А	
Collector power dissipation	P <sub>c</sub> *1	120	W	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	
C to E diode forward current	۱ <sub>D</sub> *1	25	А	

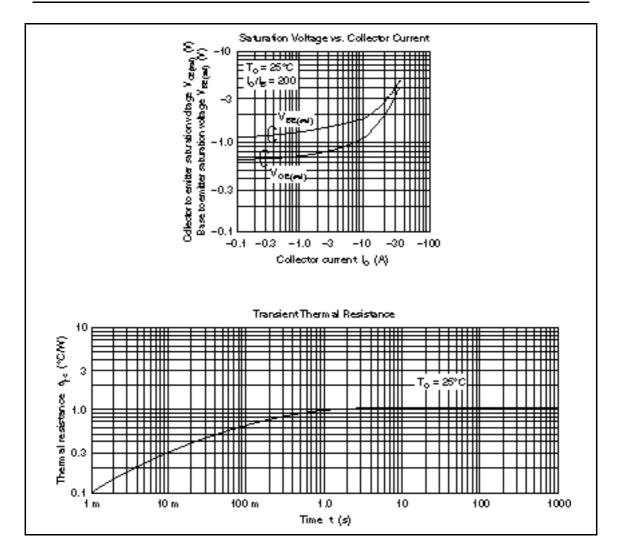
Note: 1. Value at  $T_c = 25^{\circ}C$ .

# **Electrical Characteristics** (Ta = $25^{\circ}$ C)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(\text{BR})\text{CBO}}$	-120	_	_	V	$I_{c} = -0.1 \text{ mA}, I_{E} = 0$
Collector to emitter breakdown voltage	$V_{(\text{BR})\text{CEO}}$	-120	_	_	V	$I_c = -25$ mA, $R_{BE} =$
Collector to emitter sustain voltage	$V_{\text{CEO}(\text{sus})}$	-120	_	_	V	$I_c = -200$ mA, $R_{BE} =$
Emitter to base breakdown voltage	$V_{(BR)EBO}$	-7	—	_	V	$I_{\rm E} = -50$ mA, $I_{\rm C} = 0$
Collector cutoff current	I <sub>CBO</sub>		—	-10	μA	$V_{CB} = -100 \text{ V}, I_{E} = 0$
	I <sub>CEO</sub>		—	-10	-	$V_{ce} = -100 \text{ V}, \text{ R}_{be} =$
DC current transfer ratio	$h_{\text{FE1}}$	2000	—	20000		$V_{ce} = -4 V, I_c = -12 A^{*1}$
	$h_{\text{FE2}}$	500	—	—	-	$V_{ce} = -4 V, I_c = -25 A^{*1}$
Collector to emitter saturation	$V_{\text{CE}(\text{sat})1}$	_	_	-2.0	V	$I_{\rm c} = -12$ A, $I_{\rm B} = -24$ mA <sup>*1</sup>
voltage	$V_{\text{CE(sat)2}}$	_	_	-3.5	-	$I_{\rm c} = -25$ A, $I_{\rm B} = -250$ mA <sup>*1</sup>
Base to emitter saturation	$V_{BE(sat)1}$	_	_	-3.0	V	$I_{\rm c} = -12$ A, $I_{\rm B} = -24$ mA
voltage	$V_{BE(sat)^2}$		—	-4.5	-	$I_{\rm C} = -25$ A, $I_{\rm B} = -250$ mA <sup>*1</sup>

Note: 1. Pulse test.





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