

# Cypress Cadence Bolt-in Kit

## Features

- Seamless integration with your Cadence Concept™ and simulation tools
- Supports pre-synthesis simulation using Leapfrog™
- Powerful schematic symbol library
- IEEE-compliant VHDL
- Supports the full UltraLogic™ family of SPLDs and CPLDs
- Industry-leading synthesis for programmable logic
- 100% automatic fitting
- VHDL and Verilog post-layout timing models
- Complete solution from design entry to programming

## Introduction

Cypress offers powerful integrated solutions for programmable logic. The Cypress Cadence Bolt-in Kit gives you everything to design with Cypress's UltraLogic PLDs in one seamless device-independent design environment. It allows you to take advantage of Cadence's powerful Concept™ schematic entry tool, Cypress's industry-leading Warp™ VHDL synthesis tool, and a wide variety of simulators.

## Functional Description

### Design Entry and Pre-Synthesis Simulation

Design with ease using schematic symbols, VHDL, or a combination of both, supported with Cadence's Concept tool available with your Cadence flow. Prior to synthesis, you can use Leapfrog™ to verify your functionality.

### Synthesis

Your entire design is automatically converted into VHDL and efficiently synthesized into a SPLD or CPLD device using Warp. UltraGen™ synthesis technology will ensure that you achieve the best results for every Cypress device. For a description of UltraGen and synthesis, see the Warp datasheets.

### Fitting

Easily retarget your design to different devices. The 100% automatic fitting tools produce optimal results in minutes.

### Post-Synthesis Simulation

Warp outputs VHDL and Verilog timing simulation models. Verify your design with timing using your choice of Cadence's Leapfrog™ VHDL or Verilog-XL™ simulators or any other VHDL or Verilog simulator.

## Programming

Warp generates JEDEC programming files for all Cypress devices which can be used for in-system reprogramming (ISR™) or with various device programmers.

## System Requirements

### For Sun Workstations

SPARC CPU  
SunOS 4.1.3 or Solaris 2.5  
CD-ROM drive

## Ordering Information

CY3148 Cypress Cadence Bolt-in Kit includes:

- CD-ROM with Bolt-in software and on-line documentation
- CD-ROM with Warp software and on-line documentation
- Warp User's Guide and Reference Manual
- Release Notes
- VHDL for Programmable Logic Textbook

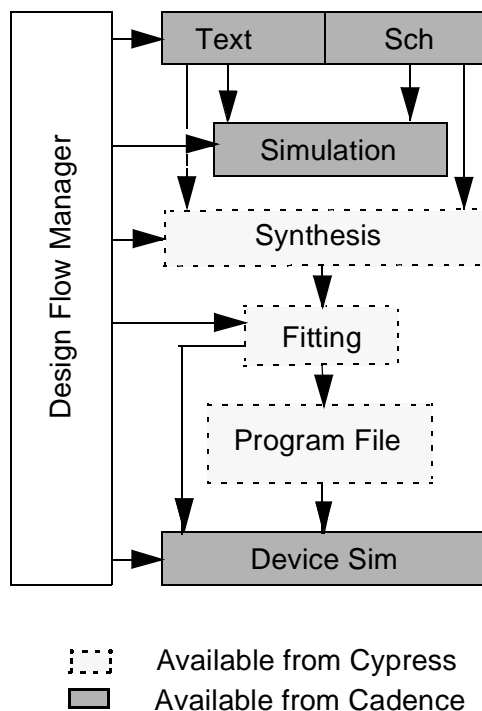


Figure 1. Cypress Cadence Design Flow

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