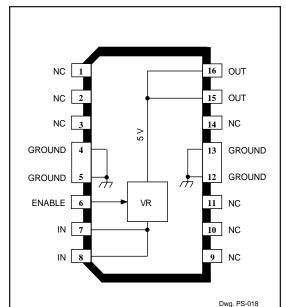
# 8181

# LOW-DROPOUT, 5 V REGULATOR — HIGH EFFICIENCY



Especially suited for hand-held, portable, battery-operated equipment such as cellular telephones, the A8181SLB low dropout voltage regulator provides high efficiency for maximum battery life in a minimum package size. Equally applicable to camcorders and portable computers, the device provides a fixed 5 V regulated continuous output at almost 200 mA of load current under worst-case conditions. Under normal operating conditions, output currents over 500 mA are permitted.

A MOSFET pass element delivers high output current with an input-output differential of less than 300 mV. For high efficiency, the low dropout voltage allows a longer battery discharge before output voltage regulation is lost. A low quiescent current, even during high load conditions, makes the device ideal for standby power systems. High regulator accuracy and excellent temperature characteristics are provided by a bandgap reference. An enable input gives the designer complete control over sequential power-up or emergency shutdown.

This device is supplied in a 16-lead wide-body, small-outline plastic power package (SOIC) for surface-mount applications. The copper batwing provides for maximum package power dissipation in the smallest possible construction. The A8181SLB is rated for operation over a temperature range of -20°C to +85°C.

# ABSOLUTE MAXIMUM RATINGS at $T_A = +25^{\circ}C$

T<sub>S</sub> ...... -40°C to +150°C

- \* Output current rating is limited by input voltage, duty cycle, and ambient temperature. Under any set of conditions, do not exceed a junction temperature of +150°C. See next page.
- † Fault conditions that produce excessive junction temperature will activate device thermal shutdown circuitry. These conditions can be tolerated but should be avoided.

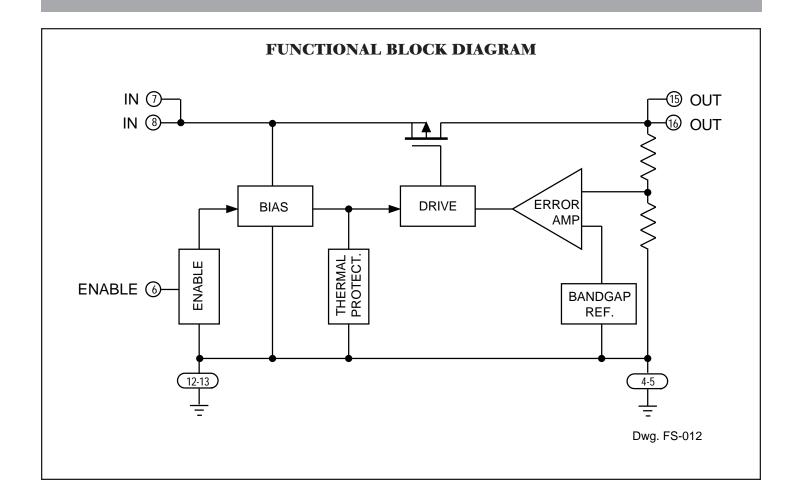
#### FEATURES AND BENEFITS

- High Efficiency Provides Extended Battery Life
- Less Than 300 mV Dropout Voltage
- Low Quiescent Current
- >200 mA Output Current
- LSTTL-Compatible ON/OFF Control For Sequential Power-up or Emergency Shutdown
- Internal Thermal Protection
- SOIC Surface-Mount Package

Always order by complete part number:

A8181SLB





# MAXIMUM ALLOWABLE OUTPUT CURRENT with device mounted on 2.24" x 2.24" ( $56.9 \ mm \ x \ 56.9 \ mm$ ) solder-coated copper-clad board in still air.

	Maximum Allowable Output Current in Milliamperes with $V_I$ = 10 V, $T_J$ = 150°C*												
	dc (Duty Cycle)												
T <sub>A</sub>	100%	90%	80%	70%	60%	50%	40%	30%	20%				
25°C	370	415	465	530	620	745	930	1000	1000				
50°C	295	330	370	425	495	595	745	995	1000				
70°C	235	265	295	340	395	475	595	795	1000				
85°C	190	215	240	275	320	385	485	645	970				

<sup>\*</sup>  $I_O = (T_J - T_A)/([V_I - V_O] R_{\theta JA} \bullet dc) = (150 - T_A)/(5 \bullet 67 \bullet dc)$ 

Output current rating can be increased (to 1 A maximum) by heat sinking or reducing the input voltage. With an infinite heat sink,  $R_{-JA} = R_{-JT} = 6$ °C/W. Conditions that produce excessive junction temperature will activate device thermal shutdown circuitry. These conditions can be tolerated but should be avoided.



## ELECTRICAL CHARACTERISTICS at $T_A$ +25°C (unless otherwise noted).

			Limits				
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units	
Output Voltage	Vo	$T_A = 25^{\circ}C$ , 5.5 V $\leq$ V <sub>I</sub> $\leq$ 10 V, 0 mA $\leq$ I <sub>O</sub> $\leq$ 500 mA†	4.90	5.00	5.10	V	
		$T_{A} = 85^{\circ}C, 5.5 \text{ V} \leq V_{I} \leq 10 \text{ V}, \\ 0 \text{ mA} \leq I_{O} \leq 500 \text{ mA*} \dagger$	4.85	_	5.15	V	
Output Volt. Temp. Coeff.	$\alpha_{VO}$	I <sub>O</sub> = 0	_	±100	_	μV/°C	
Line Regulation	$\Delta V_{O(\Delta VI)}$	$5.5 \text{ V} \le \text{V}_{\text{I}} \le 10 \text{ V}$ , Output open	_	10	30	mV	
Load Regulation	$\Delta V_{O(\Delta IO)}$	$0 \text{ mA} \le I_0 \le 500 \text{ mA}$ †, $V_1 = 6 \text{ V}$	_	40	100	mV	
Dropout Voltage	V <sub>I</sub> min - V <sub>O</sub>	I <sub>O</sub> = 500 mA†	_	_	300	mV	
Quiescent Current	IQ	V <sub>I</sub> = 10 V, I <sub>O</sub> = 500 mA†	_	87	120	μΑ	
(GND terminal current)		V <sub>I</sub> = 10 V, Output open	_	86	120	μΑ	
	I <sub>Q(off)</sub>	V <sub>I</sub> = 10 V, Output open, V <sub>E</sub> = 0.4 V	_	_	20	μА	
ENABLE Input Voltage	V <sub>EH</sub>	Output ON, V <sub>I</sub> = 10 V	2.4	_	_	V	
	V <sub>EL</sub>	Output OFF, V <sub>I</sub> = 10 V	_	_	0.4	V	
ENABLE Input Current	Ι <sub>Ε</sub>	V <sub>E</sub> = V <sub>I</sub> = 10 V	_	_	±0.1	μΑ	
Thermal Shutdown Temp.	TJ		_	165	_	°C	
Thermal Resistance	R <sub>θJA</sub>	Mounted on 2.24" x 2.24" solder-coated copper-clad board in still air	_	67	_	°C/W	
	$R_{ heta JT}$		_	6.0	_	°C/W	

Typical values are given for circuit design information only.

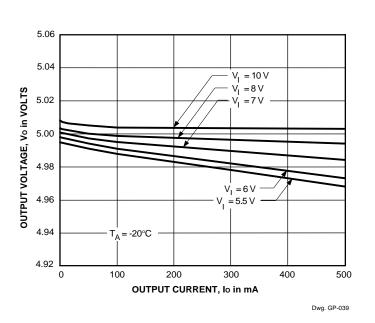
<sup>\*</sup> This parameter is tested to a lot sample plan only.

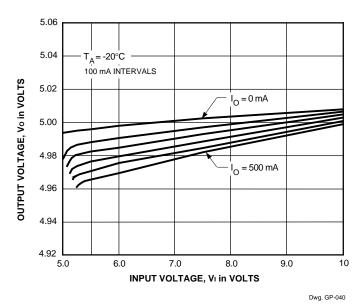
<sup>†</sup> Pulse test (<20 ms).

#### **TYPICAL CHARACTERISTICS**

#### **LOAD REGULATION**

#### LINE REGULATION





5.06 5.04 OUTPUT VOLTAGE, Vo in VOLTS = 10 V5.02

5.00

4.98

4.96

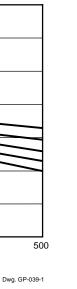
4.94

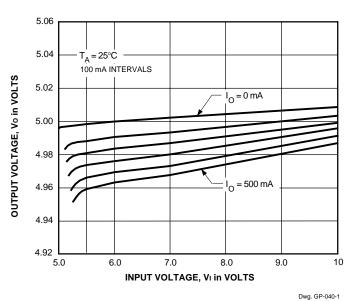
4.92

T<sub>A</sub>= 25°C

=8V

 $V_1 = 6 V$ 





CAUTION: Maximum allowable duty cycle will be significantly less than 100% at high temperatures, at high input voltages, or at high output currents. See Maximum Allowable Output Current table.



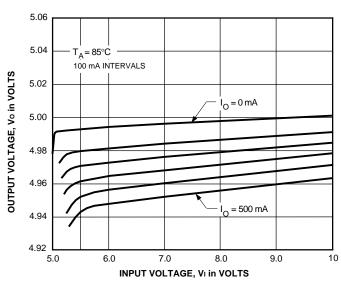
**OUTPUT CURRENT, Io in mA** 

#### TYPICAL CHARACTERISTICS (cont'd)

#### **LOAD REGULATION**

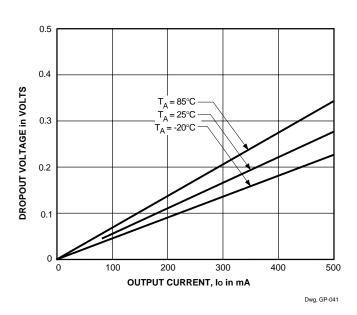
#### 5.06 5.04 T<sub>A</sub>=85°C OUTPUT VOLTAGE, Vo in VOLTS 5.02 V<sub>I</sub> = 10 V =8V 5.00 4.98 4.96 4.94 4.92 100 300 400 200 500 0 **OUTPUT CURRENT, Io in mA** Dwg. GP-039-2

#### LINE REGULATION

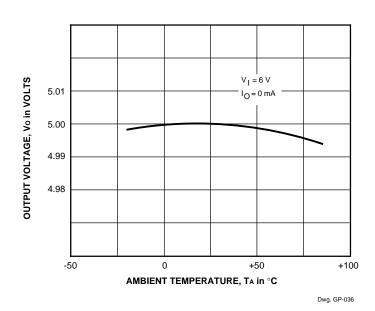


#### Dwg. GP-040-2

#### **DROPOUT VOLTAGE**



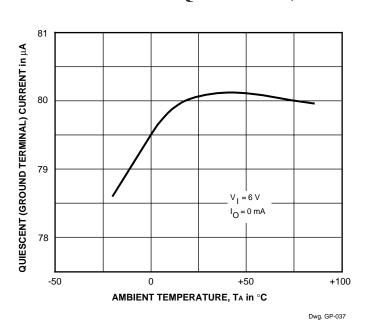
#### **OUTPUT VOLTAGE vs TEMP.**

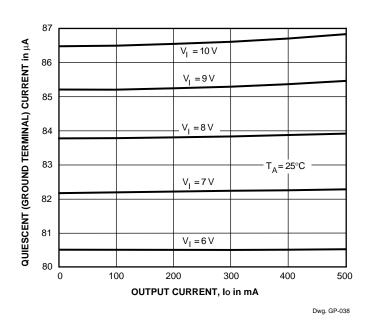


**CAUTION:** Maximum allowable duty cycle will be significantly less than 100% at high temperatures, at high input voltages, or at high output currents. See Maximum Allowable Output Current table.

#### **TYPICAL CHARACTERISTICS (cont'd)**

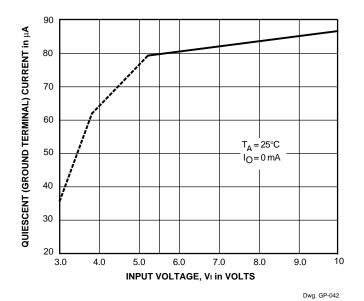
#### **QUIESCENT (GROUND TERMINAL) CURRENT**

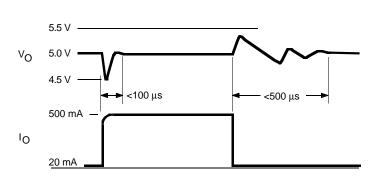




# TRANSIENT PERFORMANCE

 $V_{I}$  = 5.5 V to 10 V,  $T_{A}$  = -20°C to +85°C,  $C_{O}$  = 4.7  $\mu F$ 



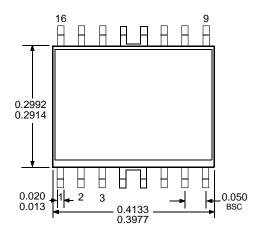


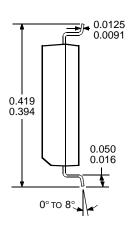
Dwg. WP-018

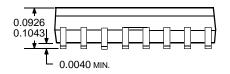
**CAUTION:** Maximum allowable duty cycle will be significantly less than 100% at high temperatures, at high input voltages, or at high output currents. See Maximum Allowable Output Current table.



## Dimensions in Inches (Based on 1 mm = 0.3937")

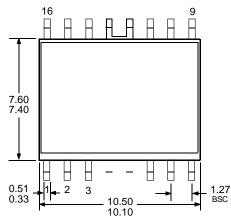


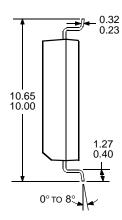


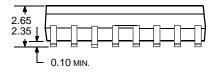


Dwg. MA-008-17A in

#### **Dimensions in Millimeters**







Dwg. MA-008-17A mm

NOTES: 1. Webbed lead frames. Leads 4, 5, 12, and 13 are internally one piece.

- 2. Lead spring tolerance is non-cumulative.
- ${\it 3. } \ \, {\it Exact body and lead configuration at vendor's option within limits shown}.$

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