

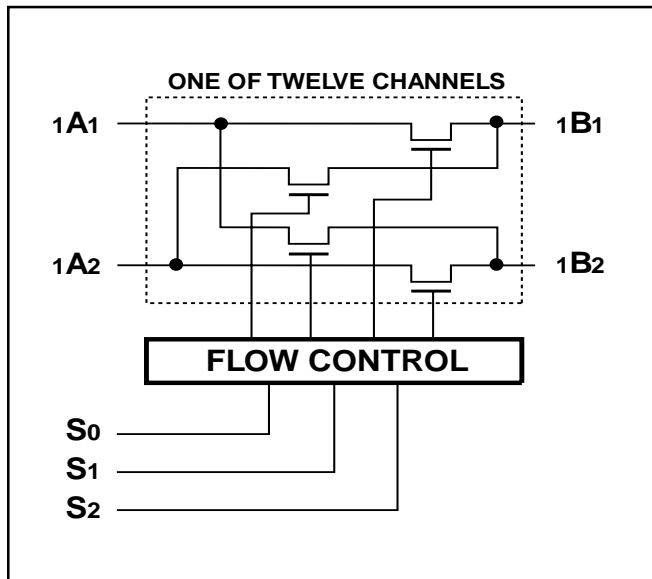
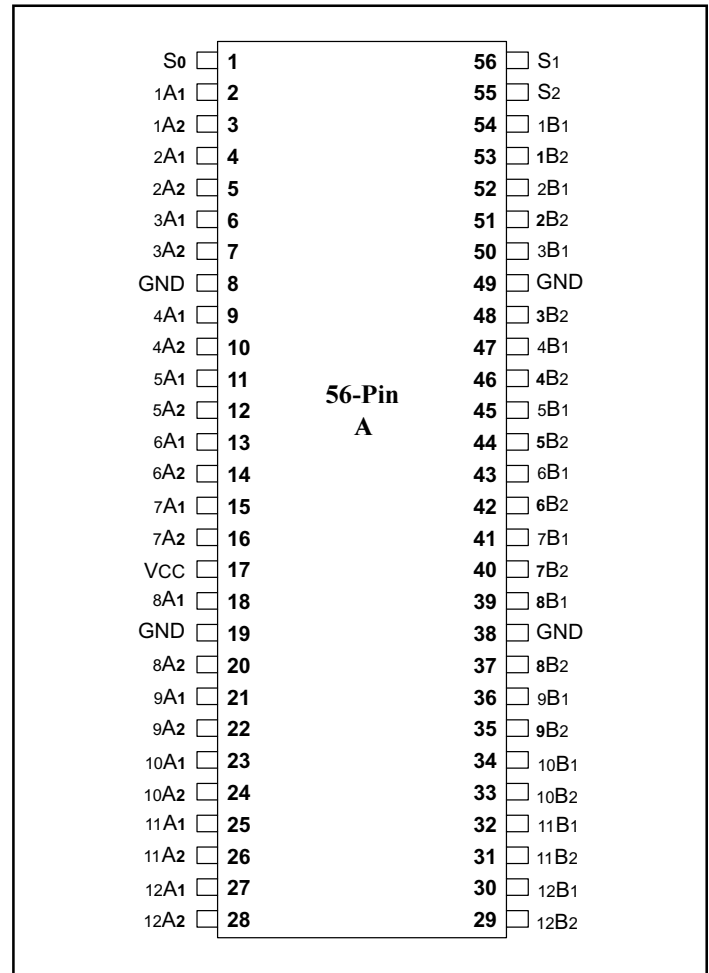
3.3V, High Bandwidth, 24-Bit Bus Exchange Switch
Product Features

- Near-zero propagation delay
- 6Ω switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra-low quiescent power (0.2mA typical)
 - Ideally suited for notebook applications
- Industrial operating temperature: -40°C to +85°C
- Packages available:
 - 56-pin 240-mil wide thin plastic TSSOP (A)

Product Description

Pericom Semiconductor's PI3C series of bus switch circuits are produced using the Company's advanced submicron CMOS technology.

The PI3C16212 is 24-bit bus exchange switch designed with a low ON resistance allowing inputs to be connected directly to outputs. This device operates as a 24-bit bus switch or a 12-bit exchanger, which provides data exchanging between the four signal ports via the data select pins (S0-S2).

Logic Block Diagram

Product Pin Configuration

Truth Table

Function	S2	S1	S0	A1	A2
Disconnect	L	L	L	Z	Z
A1 to B1	L	L	H	B1	Z
A1 to B2	L	H	L	B2	Z
A2 to B1	L	H	H	Z	B1
A2 to B2	H	L	L	Z	B2
Disconnect	H	L	H	Z	Z
A1 to B1, A2 to B2	H	H	L	B1	B2
A1 to B2, A2 to B1	H	H	H	B2	B1

Note:

1. H = High Voltage Level
L = Low Voltage Level
Z = High Impedance

Product Pin Description

Pin Name	I/O	Description
S0-S2	I	Select Inputs
xAx	I/O	Bus A
xBx	I/O	Bus B

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-65°C to +150°C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential	-0.5V to +4.6V
DC I/O Voltage	-0.5V to +5.5V
DC Output Current	120mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$, $V_{CC} = 3.3\text{V} \pm 10\%$)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5	—	0.8	
I_{IH}	Input HIGH Current	$V_{CC} = \text{Max.}, V_{IN} = V_{CC}$	—	—	± 1	μA
I_{IL}	Input LOW Current	$V_{CC} = \text{Max.}, V_{IN} = \text{GND}$	—	—	± 1	
I_{OZH}	High-Impedance Output Current	$0 \leq A, B \leq V_{CC}$	—	—	± 1	
V_{IK}	Clamp diode Voltage	$V_{CC} = \text{Min.}, I_{IN} = -18\text{mA}$	—	-0.73	-1.2	V
R_{ON}	Switch On Resistance ⁽⁴⁾	$V_{CC} = \text{Min.}, V_{IN} = 0.0\text{V}$ $I_{ON} = 48\text{mA}$ or 64mA	—	6.5	8.5	Ω
		$V_{CC} = \text{Min.}, V_{IN} = 2.4\text{V}$ $I_{ON} = 15\text{mA}$	—	10	15	

Capacitance ($T_A = 25^\circ\text{C}$ $f = 1\text{MHz}$)

Parameters ⁽⁵⁾	Description	Test Conditions	Typ.	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$	3.5	pF
C_{OFF}	A/B Capacitance, Switch Off		4.0	
C_{ON}	A/B Capacitance, Switch On		12.0	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $V_{CC} = 3.3\text{V}$, $T_A = 25^\circ\text{C}$ ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
5. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max., V _{IN} = GND or V _{CC}	—	260	850	μA
ΔI _{CC}	Supply Current per Input @ TTL HIGH	V _{CC} = Max., V _{IN} = 3.0V ⁽³⁾	—	—	750	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 3.3V, +25°C ambient.
3. Per TTL driven input (control inputs only); A and B pins do not contribute to I_{CC}.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

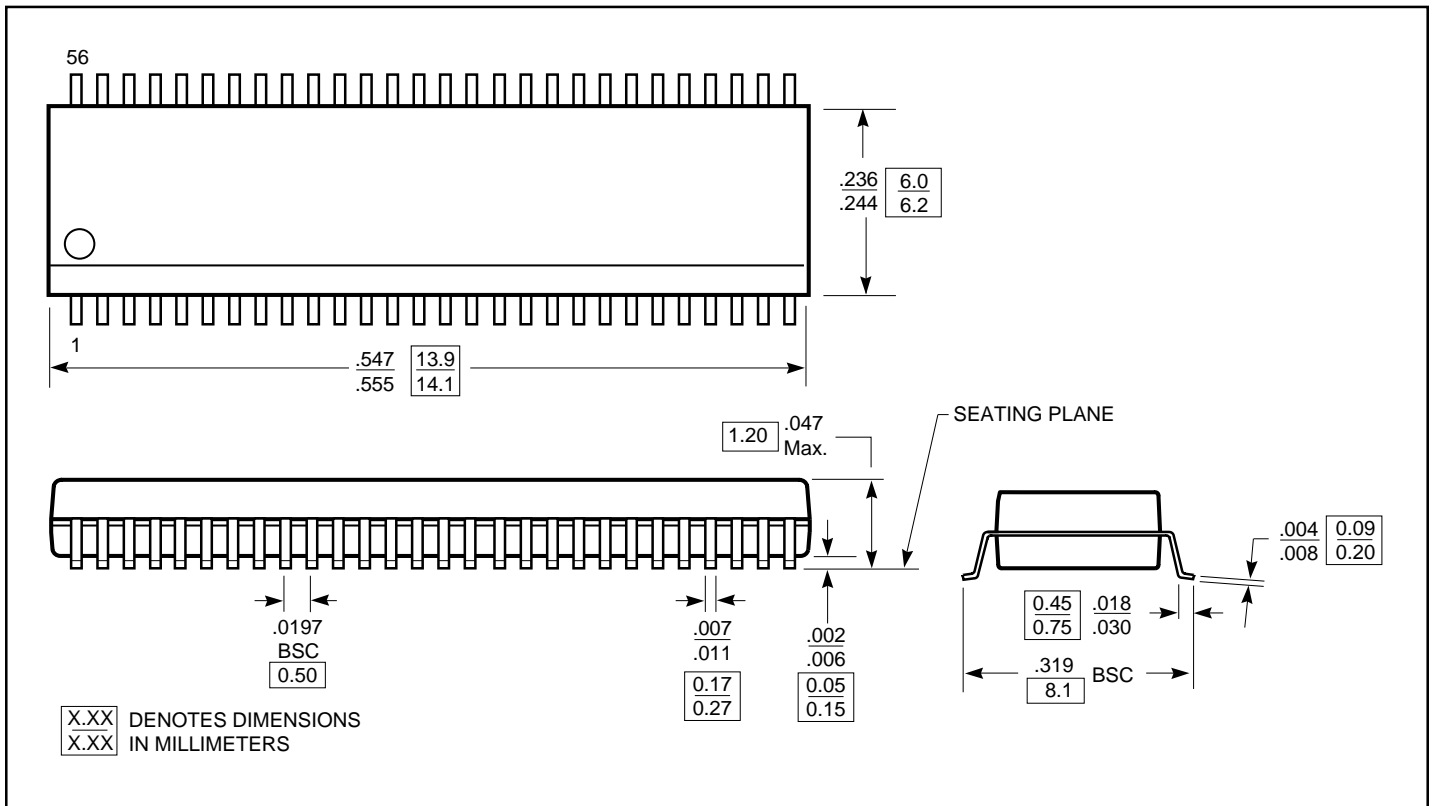
PI3C16212 Switching Characteristics Over Operating Range

Parameters	Description	Conditions ⁽¹⁾	Com.		Units
			Min.	Max.	
t _{PLH} t _{PHL}	Propagation Delay ^(2,3) Ax to Bx, Bx to Ax	C _L = 50pF R _L = 500Ω	—	0.50	ns
t _{PZH} t _{PZL}	Bus Enable Time Sn to Ax or Bx	C _L = 50pF R _L = 500Ω	1.5	6.5	
t _{PHZ} t _{PLZ}	Bus Disable Time Sn to Ax or Bx		1.5	5.5	

Notes:

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

56-Pin TSSOP (A) Package



Ordering Information

Part	Pin - Package	Width	Temperature
PI3C16212A	56 - TSSOP (A)	240-mil	0°C to 85°C