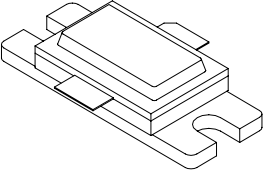




10502

500 Watts, 50 Volts, Pulsed
Avionics 1030 / 1090 MHz

<p>GENERAL DESCRIPTION</p> <p>The 10502 is a high power COMMON BASE bipolar transistor. It is designed for pulsed systems in the frequency band 1030/1090 MHz, with the pulse width and duty required for MODE-S & TCAS applications. The device has gold thin-film metallization and diffused ballasting for proven highest MTTF. The transistor includes input and output prematch for broadband capability. Low thermal resistance package reduces junction temperature, extends life.</p>	<p>CASE OUTLINE 55SM-1 Common Base</p> 
<p>ABSOLUTE MAXIMUM RATINGS</p> <p>Maximum Power Dissipation Device Dissipation @ 25°C¹ 1458 Watts</p> <p>Maximum Voltage and Current</p> <p>BVces Collector to Emitter Voltage 65 Volts BVebo Emitter to Base Voltage 3.5 Volts Ic Collector Current 40 Amps</p> <p>Maximum Temperatures</p> <p>Storage Temperature - 65 to + 200°C Operating Junction Temperature + 230°C</p>	

ELECTRICAL CHARACTERISTICS @ 25 °C

SYMBOL	CHARACTERISTICS	TEST CONDITIONS	MIN	TYP	MAX	UNITS
P _{out}	Power Output	F = 1030/1090 MHz	500			W
P _g	Power Gain	V _{cc} = 50 Volts	8.5			dB
P _{in}	Power Input	PW = 32 μsec, DF = 2%			70	W
η _c	Collector Efficiency		40			%
R _L	Return Loss		-10			dB
VSWR	Load Mismatch Tolerance ¹	F = 1090 MHz	10:1			
BVebo	Emitter to Base Breakdown	I _e = 50 mA	3.5			Volts
BVces	Collector to Emitter Breakdown	I _c = 100 mA	65			Volts
h_{FE}	DC - Current Gain	I _c = 5 A, V _{ce} = 5 V	20			
θ_{jc} ¹	Thermal Resistance				0.12	°C/W

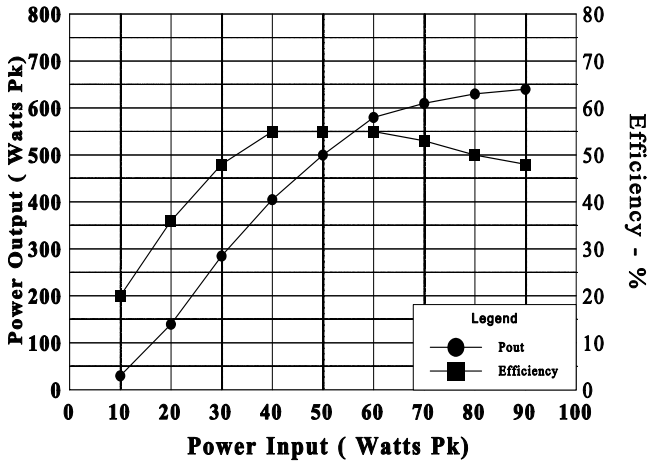
Note 1: At rated output power and pulse conditions

Rev. - Sep 1998

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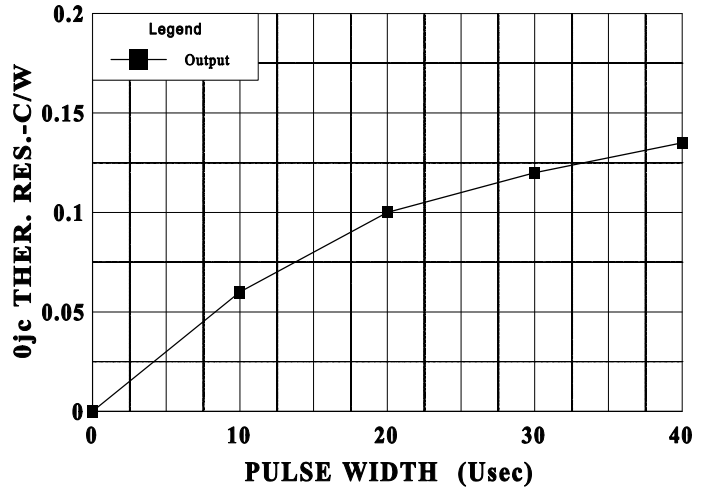
Power Output & Efficiency vs Pin

1090 MHz, 50 V, PW 0.5us, 50%, 128 us,



THERMAL RESISTANCE VS PULSE WIDTH

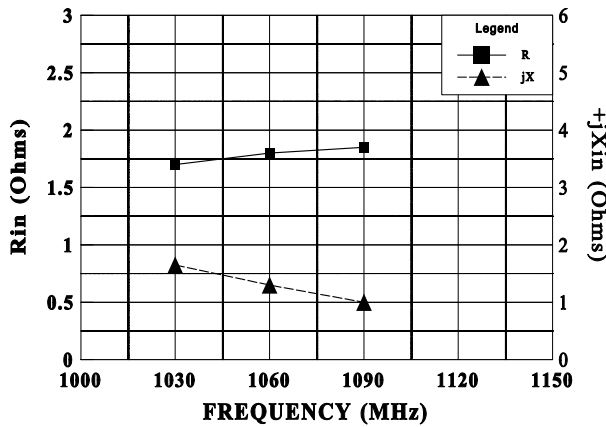
Vcc - 50 V, Tf = 30 C



Burst Width = 128 μs, L.T.D. = 1%

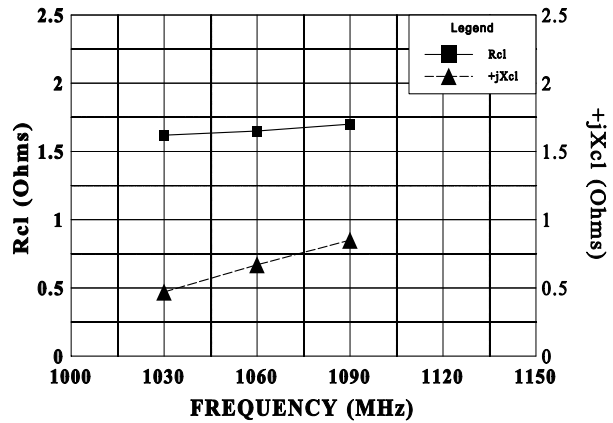
SERIES INPUT IMPEDANCE VS FREQUENCY

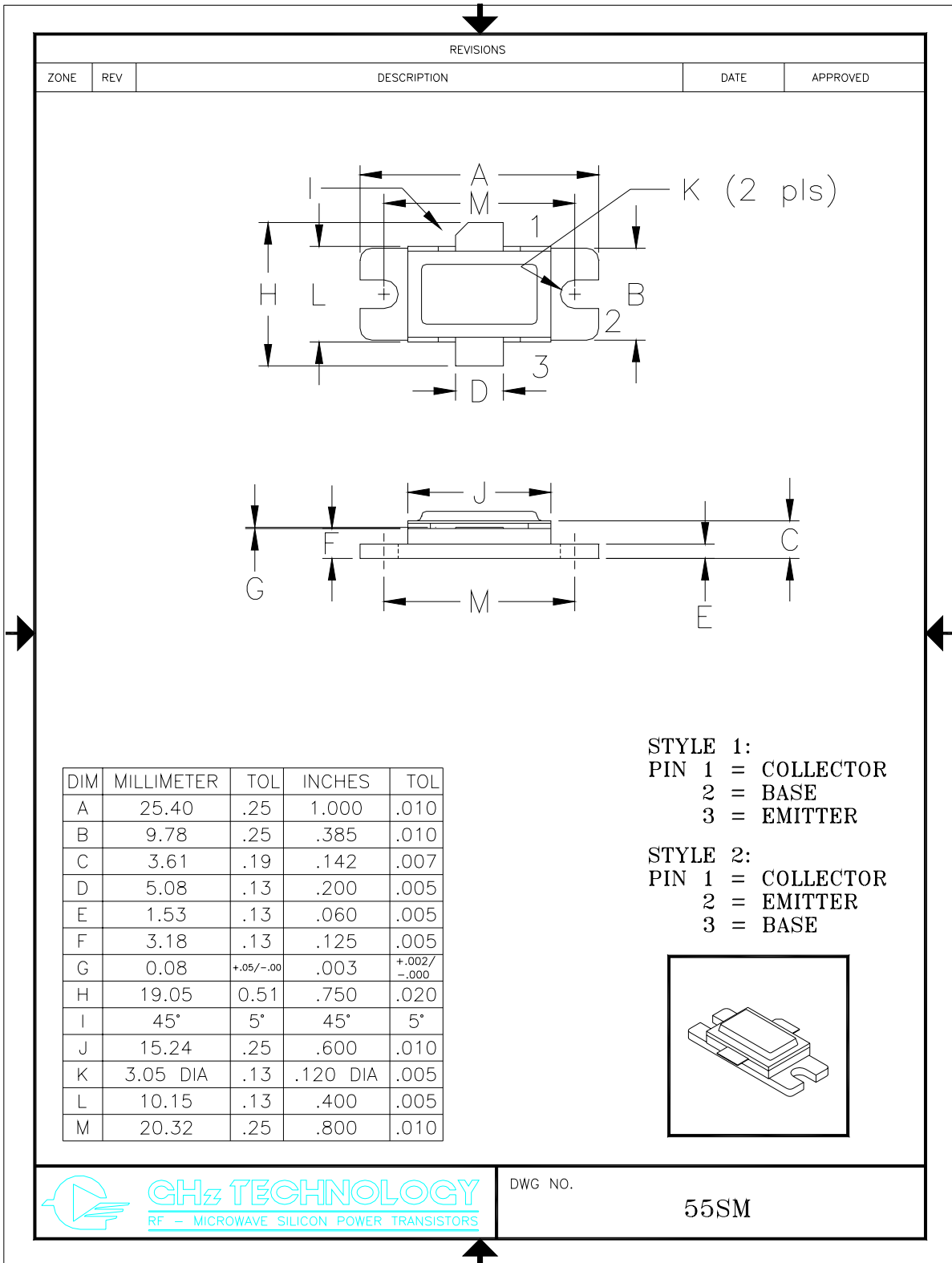
Vcc = 50 V, Pi = 65W, 32 us, 2%



SERIES LOAD IMPEDANCE VS FREQUENCY

Vcc = 50 V, Pin = 65 W, 32 us, 2%





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Datasheets for electronics components.