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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon N-Channel MOS FET



ADE-208-1333 (Z) 1st. Edition Mar. 2001

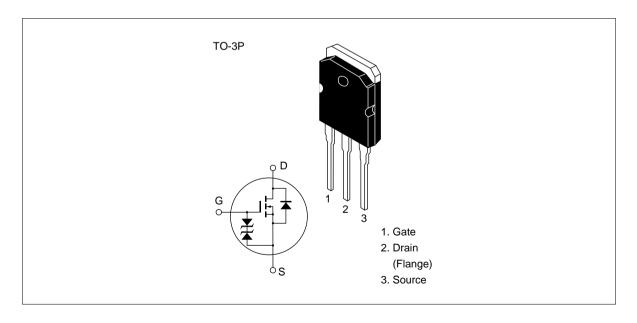
Application

High speed power switching

Features

- Low on–resistance
- High speed switching
- No secondary breakdown
- Suitable for Switching regulator

Outline



Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	1000	V
Gate to source voltage	$V_{\sf GSS}$	±30	V
Drain current	I _D	8	A
Drain peak current	l _{D(pulse)} *1	24	A
Body to drain diode reverse drain current	I _{DR}	8	A
Channel dissipation	Pch*2	150	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

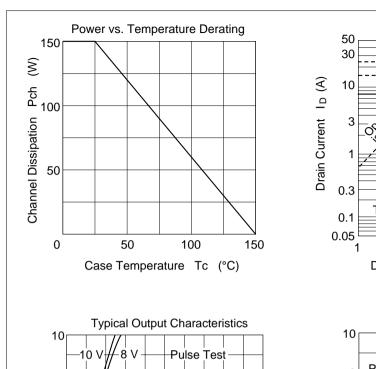
Notes 1. PW 10 µs, duty cycle 1 %

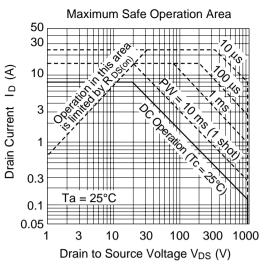
2. Value at Tc = 25°C

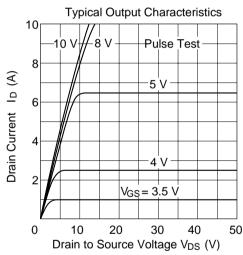
Electrical Characteristics ($Ta = 25^{\circ}C$)

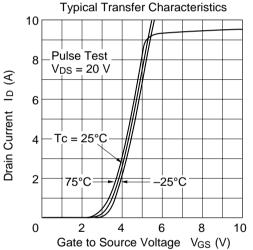
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	1000	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	250	μΑ	$V_{DS} = 800 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	_	3.0	V	$I_{D} = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R _{DS(on)}	_	1.2	1.6		$I_D = 4 A$ $V_{GS} = 10 V^{*1}$
Forward transfer admittance	y _{fs}	4	6	_	S	$I_D = 4 A$ $V_{DS} = 20 V^{*1}$
Input capacitance	Ciss	_	2690	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	920	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	375	_	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	35	_	ns	I _D = 4 A
Rise time	t _r	_	135	_	ns	V _{GS} = 10 V
Turn-off delay time	t _{d(off)}	_	300	_	ns	R _L = 7.5
Fall time	t _f	_	205	_	ns	
Body to drain diode forward voltage	V_{DF}	_	0.9	_	V	$I_F = 8 A, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	_	1600	_	μs	$I_F = 8 \text{ A}, V_{GS} = 0,$ $di_F / dt = 100 \text{ A} / \mu \text{s}$
·	t _{rr}	_	1600	_	μs	

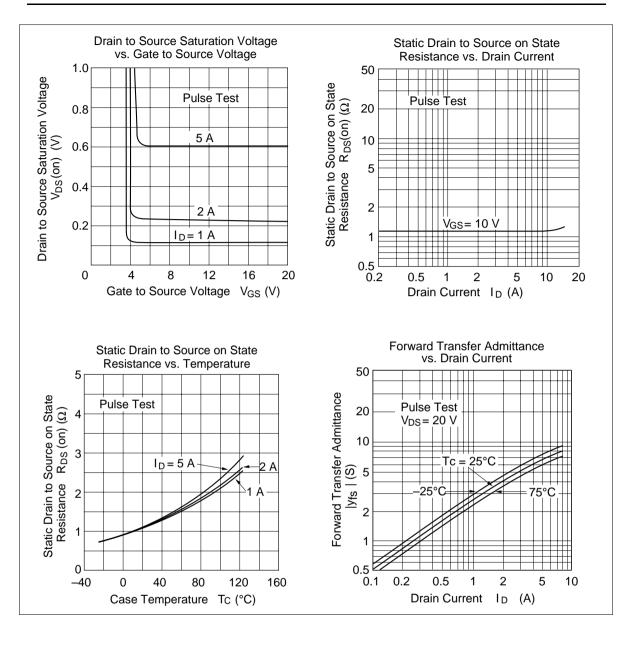
Note 1. Pulse Test

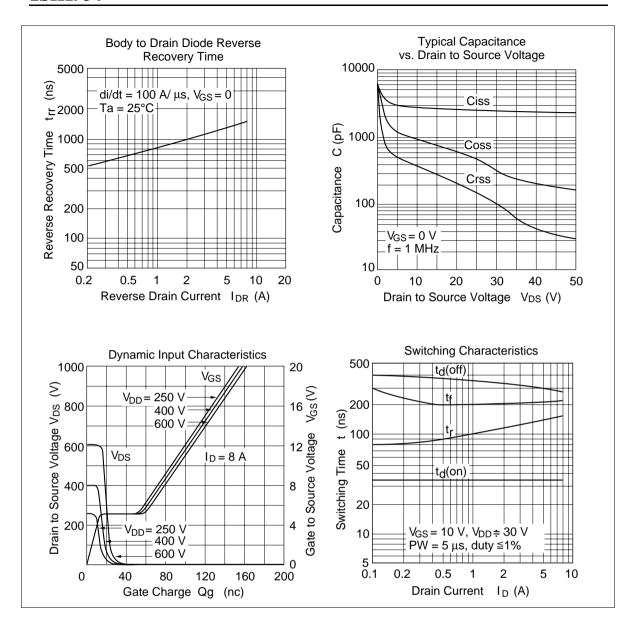


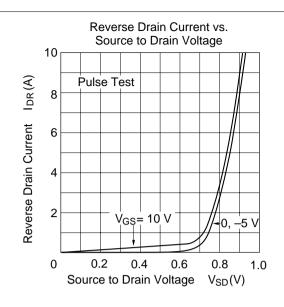


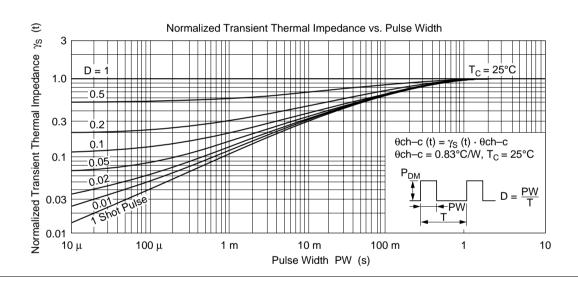


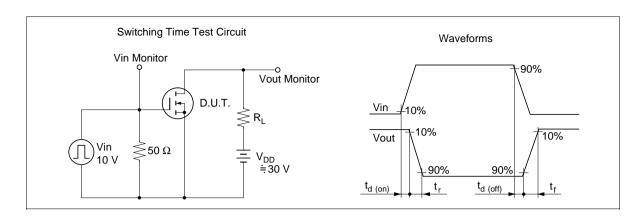




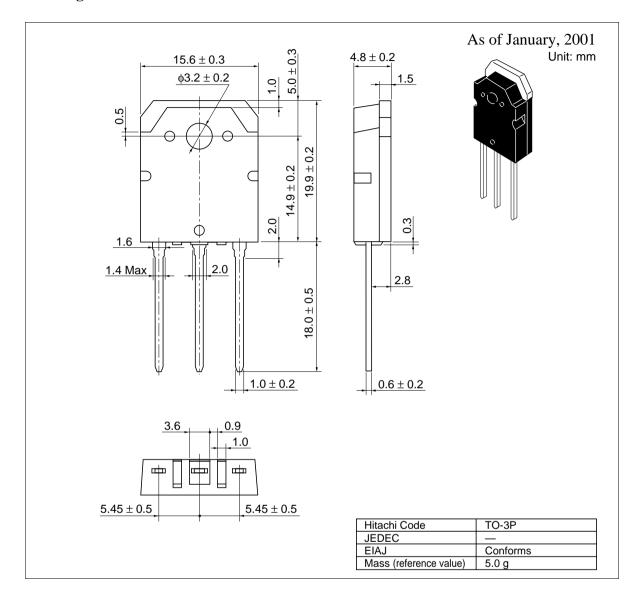








Package Dimensions



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