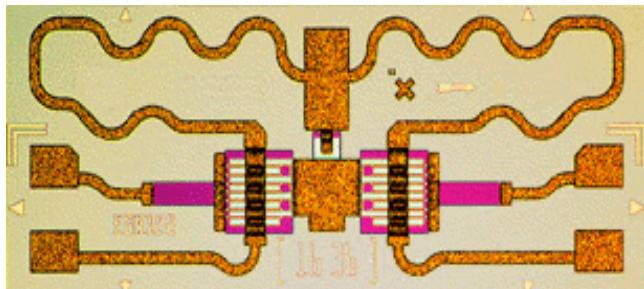


SPDT FET Switch**TGS8122-SCC****Key Features and Performance**

- 8 to 11 GHz Frequency Range
- 0.9 dB Typical Insertion Loss
- 40 dB Typical Isolation at 9 GHz
- 1.5:1 Typical Input SWR at Midband
- 1.4:1 Typical Output SWR at Midband
- Less Than 2 ns Rise/Fall Time
- $1.7018 \times 0.7112 \times 0.152 \text{ mm}$ ($0.067 \times 0.028 \times 0.006 \text{ in.}$)

Description

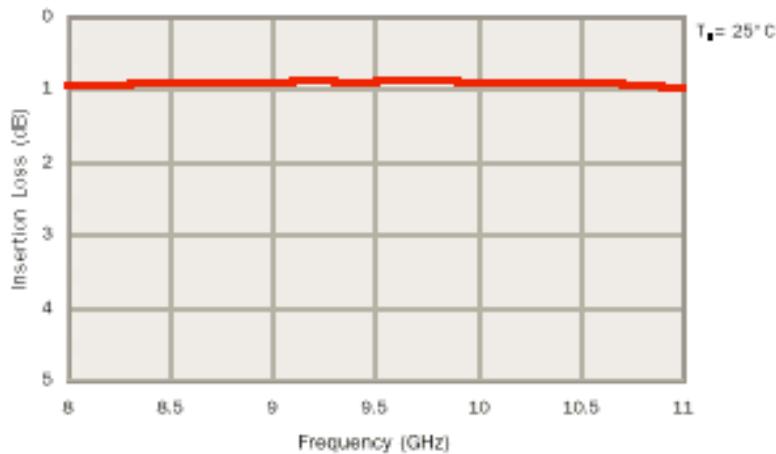
The TriQuint TGS8122-SCC is a monolithic single-pole, double-throw (SPDT) GaAs FET switch designed for 8 to 11 GHz. This device has low insertion loss, low current consumption of less than 50 mA with control voltages of -7 V and 0 V, and rise and fall times are less than 2 ns. Ground is provided to the circuitry through vias to the backside metallization.

This switch is ideal for use in high-speed switching radar and communication systems.

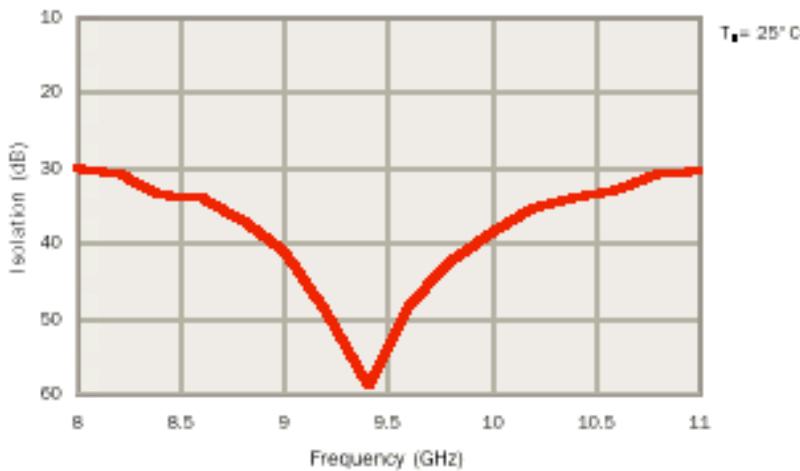
Bond pad and backside metallization is gold plated for compatibility with eutectic alloy attachment methods as well as the thermocompression and thermosonic wire-bonding processes.

The TGS8122-SCC is supplied in chip form and is readily assembled using automated equipment.

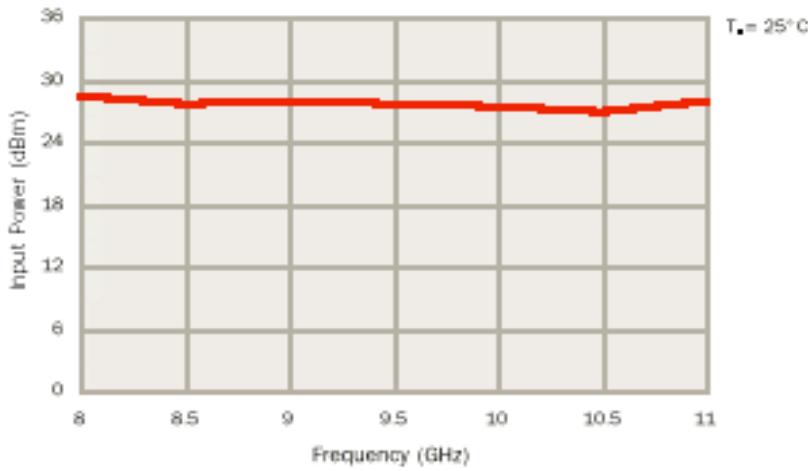
**TYPICAL
INSERTION LOSS**



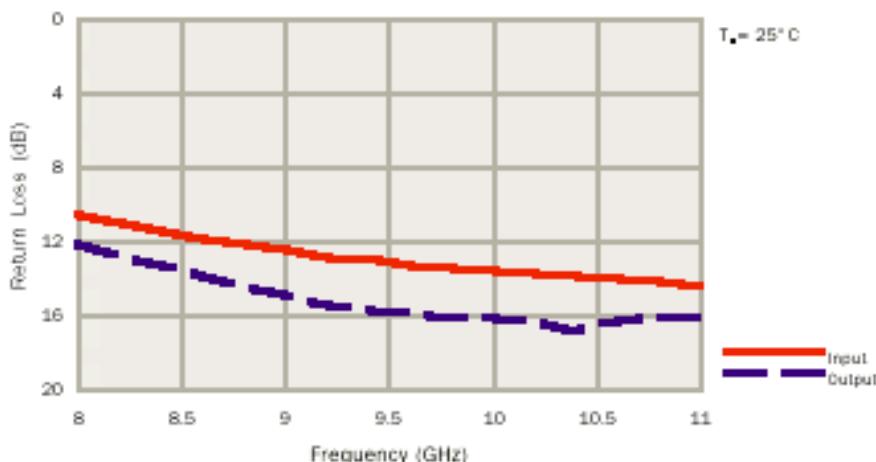
**TYPICAL
ISOLATION**



**TYPICAL
INPUT POWER
 $P_{1-\text{dB}}$**



**TYPICAL
RETURN LOSS**



**ABSOLUTE
MAXIMUM RATINGS**

Input continuous-wave power, P_{in} *	3 W
Control voltage range, $V_{ctrl,2}$	-10 V to 0 V
Operating channel temperature, T_{ch}^{**}	150°C
Mounting temperature (30 sec), T_M	320°C
Storage temperature range, T_{sta}	-65 to 150°C

Ratings over operating channel temperature range, T_{ch} (unless otherwise noted)

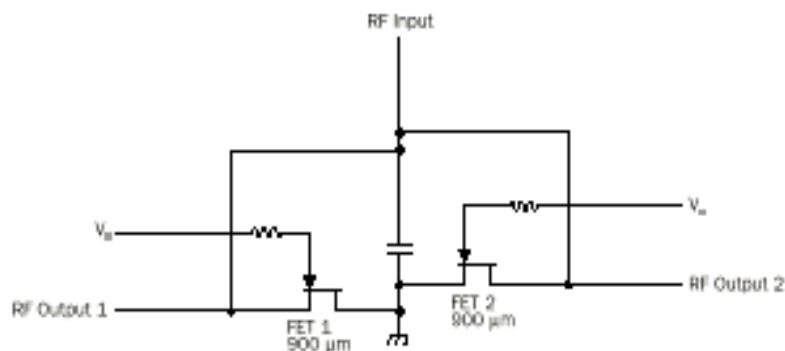
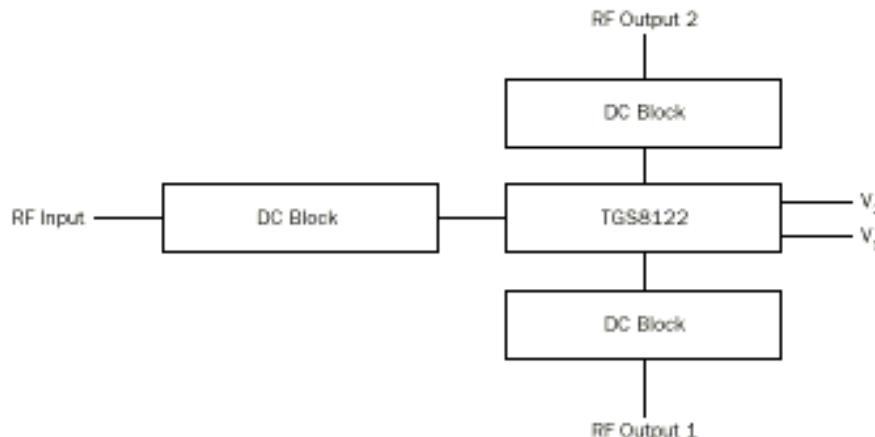
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "RF Characteristics" is not implied. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

* DC blocks are not provided at RF ports.

** Operating channel temperature directly affects the device MTTF. For maximum life, it is recommended that channel temperature be maintained at the lowest possible level.

RF CHARACTERISTICS

PARAMETER		TEST CONDITIONS	TYP	UNIT
IL	Insertion loss	f = 8 to 11 GHz	0.9	dB
ISO	Isolation	f = 8 GHz	30	dB
		f = 9 GHz	40	
		f = 10 GHz	42	
		f = 11 GHz	31	
SWR(in)	Input standing-wave ratio	f = 8 to 11 GHz	1.5:1	
SWR(out)	Output standing-wave ratio	f = 8 to 11 GHz	1.4:1	
P _{1dB(in)}	Input power at 1-dB gain compression	f = 8 to 11 GHz	27.5	dBm
t _r	Rise time, detected output voltage level	P _{IN} = 8 dBm at 10 GHz	<2	ns
t _f	Fall time, detected output voltage level			

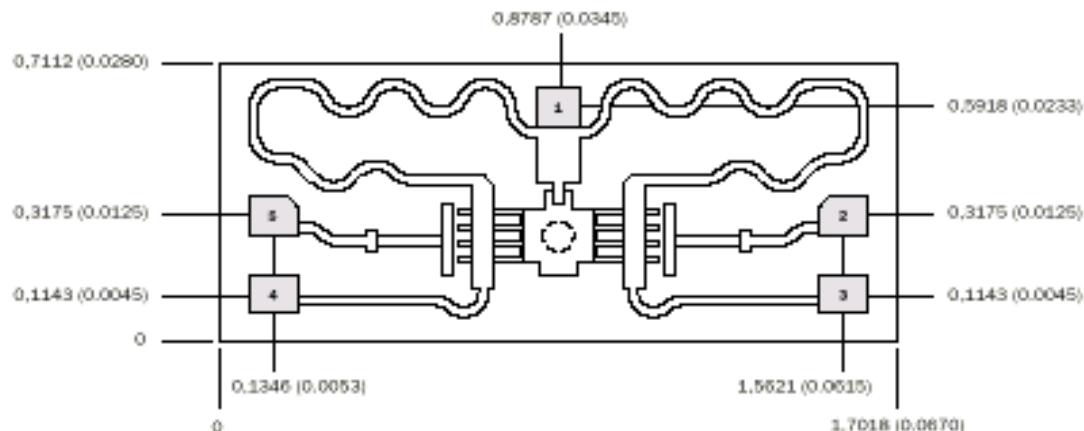
T_A = 25°C
EQUIVALENT SCHEMATIC

RECOMMENDED ASSEMBLY DIAGRAM


RF connections: bond using two 1-mil diameter, 20- to 25-mil-length gold wires at the RF Input and three 1-mil diameter, 20- to 25-mil-length gold wires at the RF Output port for optimum RF performance.

Low-loss path is RF Input to RF Output 1 for V₁ = -7 V and V₂ = 0. Low-loss path is RF Input to RF Output 2 for V₁ = 0 and V₂ = -7 V.

DC blocks are not provided at RF ports.

MECHANICAL DRAWING



Units: millimeters (inches)

Thickness: 0.1524 (0.006) (reference only)

Chip-edge-to-bond-pad dimensions are shown to center of bond pad.

Chip-size tolerance: ± 0.0508 (0.002)

Bond pad #1 (RF Input): 0.1118 x 0.0991 (0.0044 x 0.0039)

Bond pad #2 (V_2): 0.1219 x 0.0965 (0.0048 x 0.0038)

Bond pad #3 (RF Output 2): 0.1219 x 0.0965 (0.0048 x 0.0038)

Bond pad #4 (RF Output 1): 0.1219 x 0.0965 (0.0048 x 0.0038)

Bond pad #5 (V_1): 0.1219 x 0.0965 (0.0048 x 0.0038)

GaAs MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.