

N-CHANNEL 60V - 0.014 Ω - 35A DPAK STripFET™ II POWER MOSFET

TYPE	V _{DSS}	R _{DS(on)}	ID
STD35NF06L	60 V	< 0.017 Ω	35 A

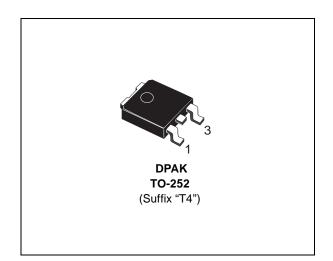
- TYPICAL $R_{DS}(on) = 0.014 \Omega$
- LOW THRESHOLD DRIVE
- GATE CHARGE MINIMIZED
- SURFACE-MOUNTING DPAK (TO-252) POWER PACKAGE IN TAPE & REEL (SUFFIX "T4")

DESCRIPTION

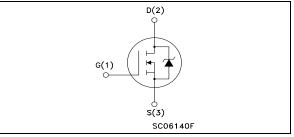
This Power MOSFET is the latest development of STMicroelectronis unique "Single Feature Size™" stripbased process. The resulting transistor shows extremely high packing density for low on-resistance, rugged avalanche characteristics and less critical alignment steps therefore a remarkable manufacturing reproducibility.

APPLICATIONS

- DC-AC CONVERTERS
- AUTOMOTIVE SWITCHING APPLICATION



INTERNAL SCHEMATIC DIAGRAM



Ordering Information

SALES TYPE	MARKING	PACKAGE	PACKAGING
STD35NF06LT4	D35NF06L	TO-252	TAPE & REEL

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source Voltage (V _{GS} = 0)	60	V
V _{DGR}	Drain-gate Voltage (R_{GS} = 20 k Ω)	60	V
V _{GS}	Gate- source Voltage	± 16	V
I _D	Drain Current (continuous) at T _C = 25°C	35	A
Ι _D	Drain Current (continuous) at T _C = 100°C	24.5	A
I _{DM} (●)	Drain Current (pulsed)	140	A
P _{tot}	Total Dissipation at $T_C = 25^{\circ}C$	80	W
	Derating Factor	0.67	W/°C
dv/dt (1)	Peak Diode Recovery voltage slope	5	V/ns
E _{AS} (2)	Single Pulse Avalanche Energy	280	mJ
T _{stg}	Storage Temperature	-55 to 175	°C
Tj	Operating Junction Temperature	-55 10 175	

(•) Pulse width limited by safe operating area.

(1) I_{SD} ≤35A, di/dt ≤100A/µs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{JMAX} (2) Starting T_j = 25 °C, I_D = 30A, V_{DD} =30V

November 2003

THERMAL DATA

Rthj-case Rthj-amb T _l	Thermal Resistance Junction-case Thermal Resistance Junction-ambient Maximum Lead Temperature For Soldering Purpose (1.6 mm from case, for 10 sec)	Max Max	1.88 100 275	°C/W °C/W °C	
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ELECTRICAL CHARACTERISTICS (T_{CASE} = 25 °C UNLESS OTHERWISE SPECIFIED) OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	$I_D = 250 \ \mu A, \ V_{GS} = 0$	60			V
IDSS	Zero Gate Voltage Drain Current (V _{GS} = 0)	V_{DS} = Max Rating V_{DS} = Max Rating T _C = 125°C			1 10	μΑ μΑ
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	$V_{GS} = \pm 16V$			±100	nA

ON (*)

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$	I _D = 250 μA	1			V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} = 10 V V _{GS} = 4.5 V	I _D = 17.5 A I _D = 17.5 A		0.014 0.016	0.017 0.020	Ω Ω

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
g _{fs} (*)	Forward Transconductance	V _{DS} = 15 V I _D = 17.5 A		28		S
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	$V_{DS} = 25V f = 1 MHz V_{GS} = 0$		1700 305 105		pF pF pF

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r	Turn-on Delay Time Rise Time	$ \begin{array}{ll} V_{DD} = 30 \ V & I_D = 27.5 \ A \\ R_G = 4.7 \ \Omega & V_{GS} = 4.5 \ V \\ (\text{Resistive Load, Figure 3}) \end{array} $		20 100		ns ns
Q _g Q _{gs} Q _{gd}	Total Gate Charge Gate-Source Charge Gate-Drain Charge	V _{DD} = 48 V I _D = 55 A V _{GS} =4.5 V		25 5 10	33	nC nC nC

SWITCHING OFF

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
t _{d(off)} t _f	Turn-off Delay Time Fall Time	V_{DD} = 30 V R _G = 4.7 Ω (Resistive Load, Fi	I _D =27.5 A V _{GS} = 4.5 V igure 3)		40 20		ns ns

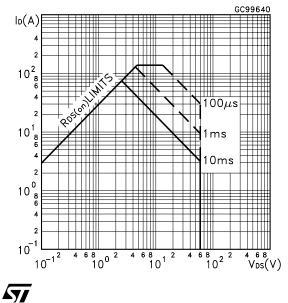
SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
I _{SD} I _{SDM} (●)	Source-drain Current Source-drain Current (pulsed)					35 140	A A
V _{SD} (*)	Forward On Voltage	I _{SD} = 35 A	$V_{GS} = 0$			1.5	V
t _{rr} Q _{rr} I _{RRM}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	I _{SD} = 35 A V _{DD} = 30 V (see test circu	di/dt = 100A/µs T _j = 150°C iit, Figure 5)		80 200 5		ns nC A

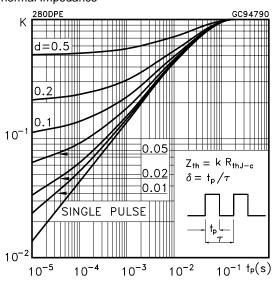
(*)Pulsed: Pulse duration = $300 \ \mu$ s, duty cycle 1.5 %.

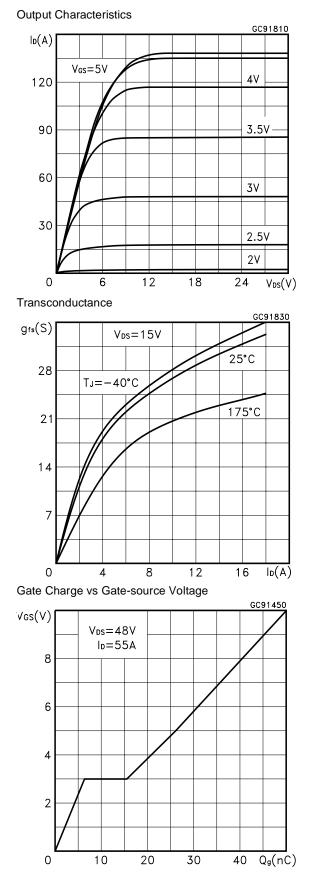
(•)Pulse width limited by safe operating area.

Safe Operating Area

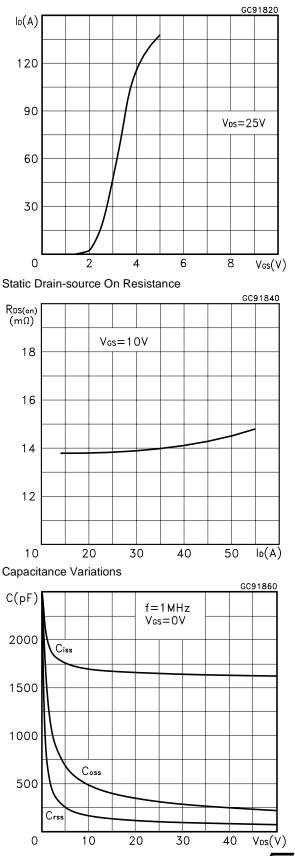


Thermal Impedance



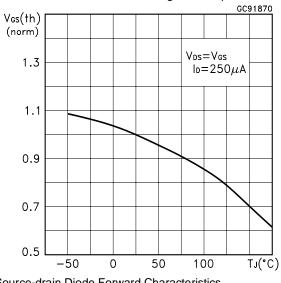






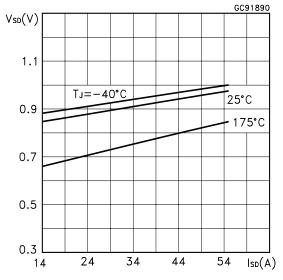
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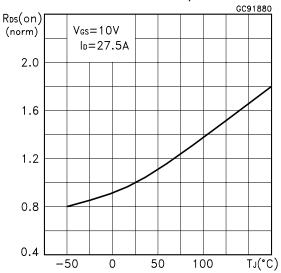
Normalized Gate Threshold Voltage vs Temperature

Source-drain Diode Forward Characteristics



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Normalized on Resistance vs Temperature



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Fig. 1: Unclamped Inductive Load Test Circuit

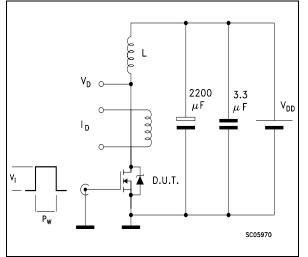


Fig. 3: Switching Times Test Circuits For Resistive Load

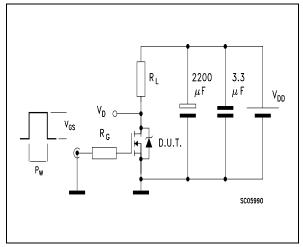


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times

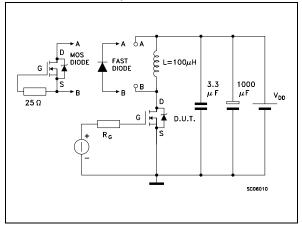


Fig. 2: Unclamped Inductive Waveform

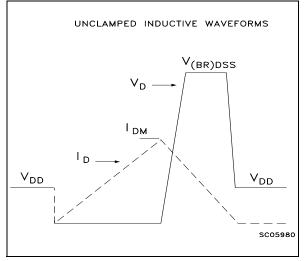
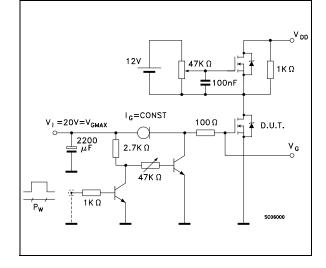


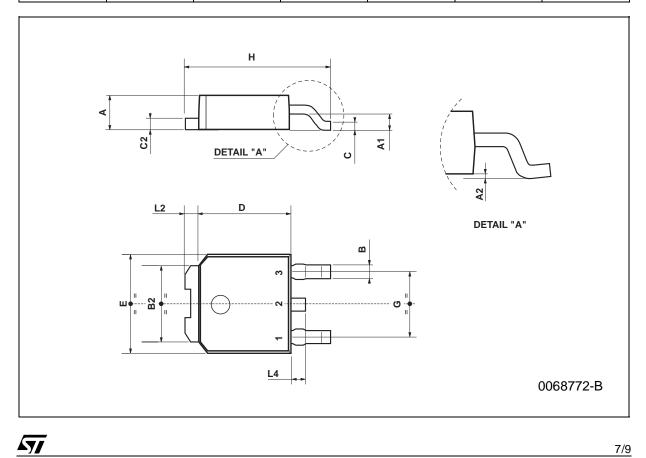
Fig. 4: Gate Charge test Circuit



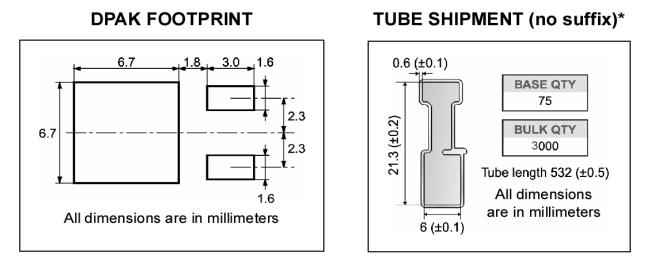
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TO-252 (DPAK) MECHANICAL DATA

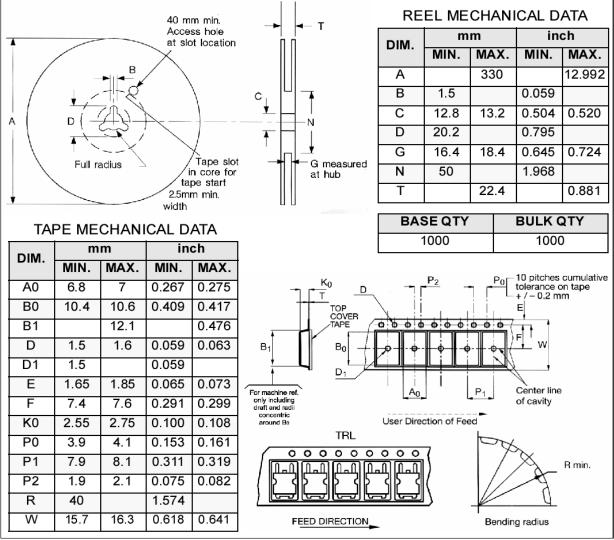
DIM.		mm			inch	
Dini.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A2	0.03		0.23	0.001		0.009
В	0.64		0.9	0.025		0.035
B2	5.2		5.4	0.204		0.212
С	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
Е	6.4		6.6	0.252		0.260
G	4.4		4.6	0.173		0.181
Н	9.35		10.1	0.368		0.397
L2		0.8			0.031	
L4	0.6		1	0.023		0.039



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TAPE AND REEL SHIPMENT (suffix "T4")*



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*on sales type

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