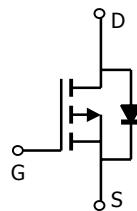
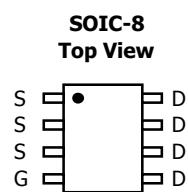




AO4413

P-Channel Enhancement Mode Field Effect Transistor

General Description	Features
<p>The AO4413 uses advanced trench technology to provide excellent $R_{DS(ON)}$, and ultra-low low gate charge with a 25V gate rating. This device is suitable for use as a load switch or in PWM applications.</p>	$V_{DS} (V) = -30V$ $I_D = -15A$ $R_{DS(ON)} < 7m\Omega (V_{GS} = -20V)$ $R_{DS(ON)} < 8.5m\Omega (V_{GS} = -10V)$



Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 25	V
Continuous Drain Current ^A	I_D	-15	A
$T_A=70^\circ C$		-12.8	
Pulsed Drain Current ^B	I_{DM}	-80	
Power Dissipation ^A	P_D	3	W
$T_A=70^\circ C$		2.1	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	26	40	°C/W
Steady-State		50	75	°C/W
Maximum Junction-to-Lead ^C	$R_{\theta JL}$	14	24	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units	
STATIC PARAMETERS							
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=-250\mu\text{A}, V_{GS}=0\text{V}$	-30			V	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-24\text{V}, V_{GS}=0\text{V}$	$T_J=55^\circ\text{C}$	-1	-5	μA	
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=\pm 25\text{V}$			± 100	nA	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu\text{A}$	-1.5	-2.6	-3.5	V	
$I_{\text{D(ON)}}$	On state drain current	$V_{GS}=-10\text{V}, V_{DS}=-5\text{V}$	60			A	
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{GS}=-20\text{V}, I_D=-15\text{A}$		5.8	7	$\text{m}\Omega$	
		$T_J=125^\circ\text{C}$	7.2	8.7			
				8.5			
V_{SD}	Diode Forward Voltage	$V_{DS}=-10\text{V}, I_D=-15\text{A}$		7.2		$\text{m}\Omega$	
		$V_{GS}=-6\text{V}, I_D=-10\text{A}$		10.4			
					-4.2	A	
DYNAMIC PARAMETERS							
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=-15\text{V}, f=1\text{MHz}$		5034		pF	
C_{oss}	Output Capacitance			1076		pF	
C_{rss}	Reverse Transfer Capacitance			829		pF	
R_g	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$		2.5		Ω	
SWITCHING PARAMETERS							
Q_g	Total Gate Charge	$V_{GS}=-10\text{V}, V_{DS}=-15\text{V}, I_D=-15\text{A}$		78		nC	
Q_{gs}	Gate Source Charge			15.2		nC	
Q_{gd}	Gate Drain Charge			23.6		nC	
$t_{\text{D(on)}}$	Turn-On Delay Time	$V_{GS}=-10\text{V}, V_{DS}=-15\text{V}, R_L=1.0\Omega, R_{\text{GEN}}=3\Omega$		17.5		ns	
t_r	Turn-On Rise Time			19.5		ns	
$t_{\text{D(off)}}$	Turn-Off Delay Time			49		ns	
t_f	Turn-Off Fall Time			30.5		ns	
t_{rr}	Body Diode Reverse Recovery Time	$I_F=-15\text{A}, dI/dt=100\text{A}/\mu\text{s}$		43		ns	
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=-15\text{A}, dI/dt=100\text{A}/\mu\text{s}$		38		nC	

A: The value of R_{0JA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user's specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C. The R_{0JA} is the sum of the thermal impedance from junction to lead R_{0JL} and lead to ambient.

D. The static characteristics in Figures 1 to 6,12,14 are obtained using 80 μs pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The SOA curve provides a single pulse rating.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

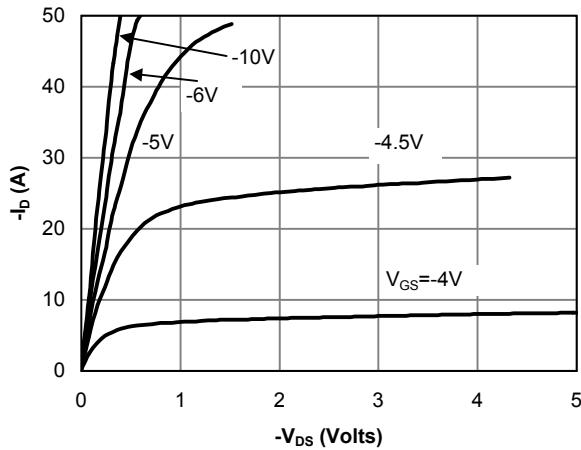


Fig 1: On-Region Characteristics

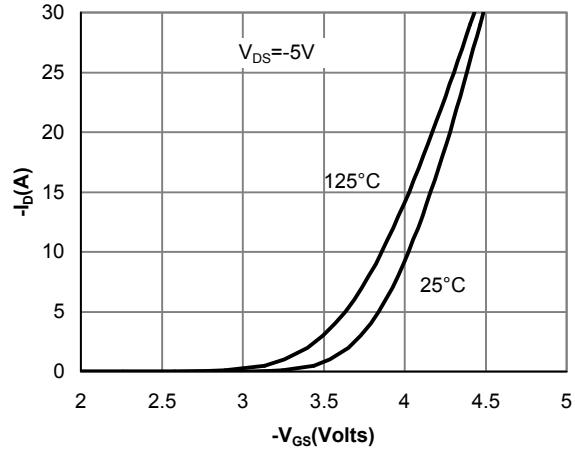


Figure 2: Transfer Characteristics

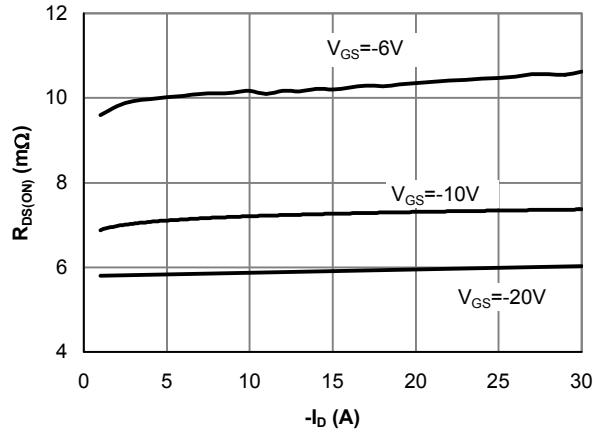


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

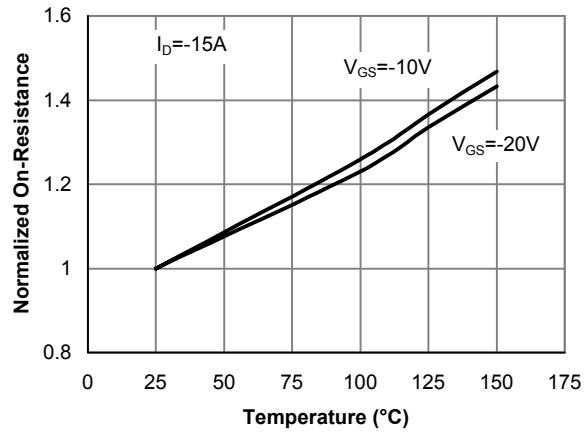


Figure 4: On-Resistance vs. Junction Temperature

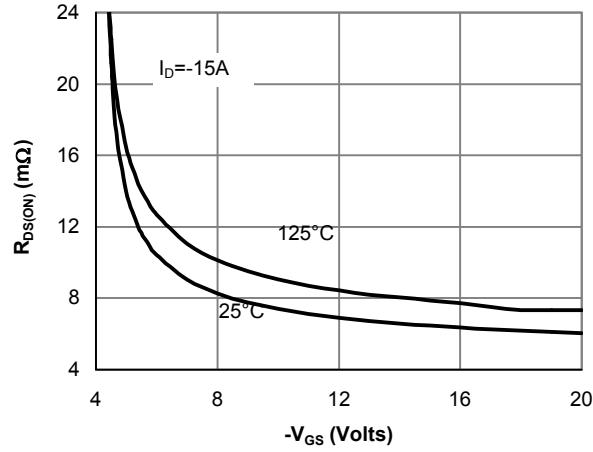


Figure 5: On-Resistance vs. Gate-Source Voltage

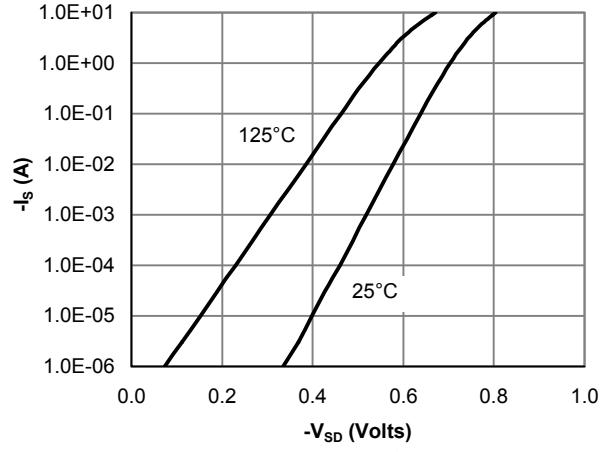


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

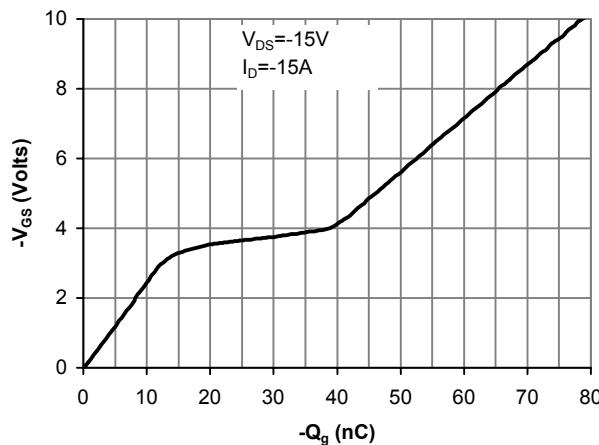


Figure 7: Gate-Charge Characteristics

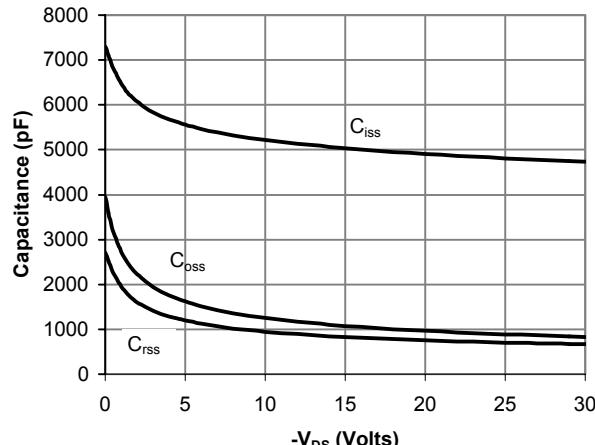


Figure 8: Capacitance Characteristics

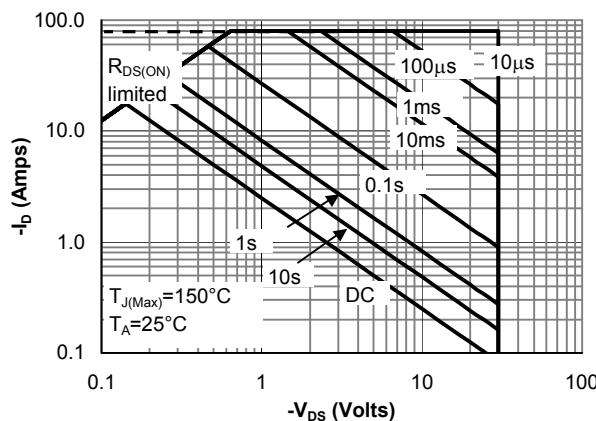


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

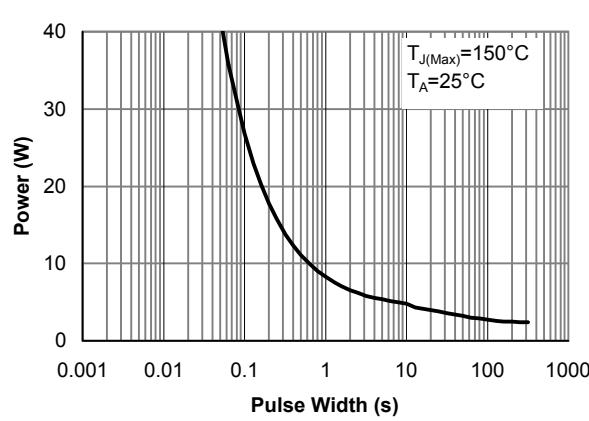


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

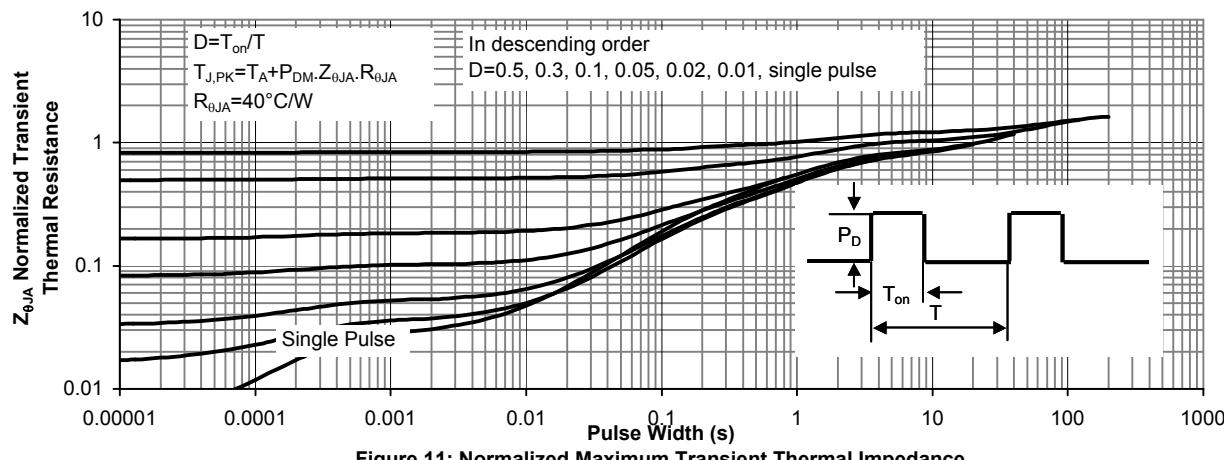
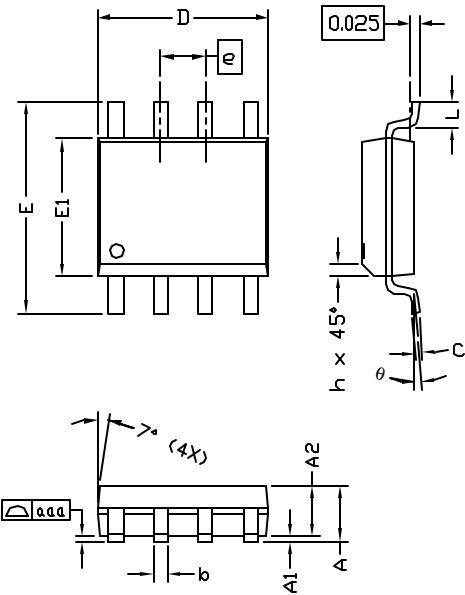


Figure 11: Normalized Maximum Transient Thermal Impedance



ALPHA & OMEGA
SEMICONDUCTOR, INC.

SOP-8 Package Data



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.45	1.50	1.55	0.057	0.059	0.061
A1	0.00	—	0.10	0.000	—	0.004
A2	—	1.45	—	—	0.057	—
b	0.33	—	0.51	0.013	—	0.020
c	0.19	—	0.25	0.007	—	0.010
D	4.80	—	5.00	0.189	—	0.197
E1	3.80	—	4.00	0.150	—	0.157
e	1.27 BSC			0.050 BSC		
E	5.80	—	6.20	0.228	—	0.244
h	0.25	—	0.50	0.010	—	0.020
L	0.40	—	1.27	0.016	—	0.050
aaa	—	—	0.10	—	—	0.004
θ	0°	—	8°	0°	—	8°

NOTE:

1. LEAD FINISH: 150 MICROINCHES (3.8 μ m) MIN.
THICKNESS OF Tin/Lead (SOLDER) PLATED ON LEAD
2. TOLERANCE ± 0.10 mm (4 mil) UNLESS OTHERWISE SPECIFIED
3. COPLANARITY : 0.10 mm
4. DIMENSION L IS MEASURED IN GAGE PLANE

PACKAGE MARKING DESCRIPTION

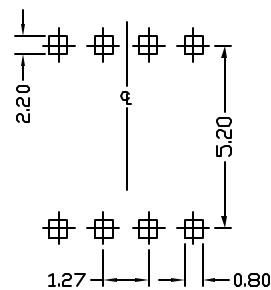


NOTE:
LOGO - AOS LOGO
4413 - PART NUMBER CODE.
F - FAB LOCATION
A - ASSEMBLY LOCATION
Y - YEAR CODE
W - WEEK CODE.
LC - ASSEMBLY LOT CODE

SOP-8 PART NO. CODE

PART NO.	CODE
AO4413	4413

RECOMMENDED LAND PATTERN



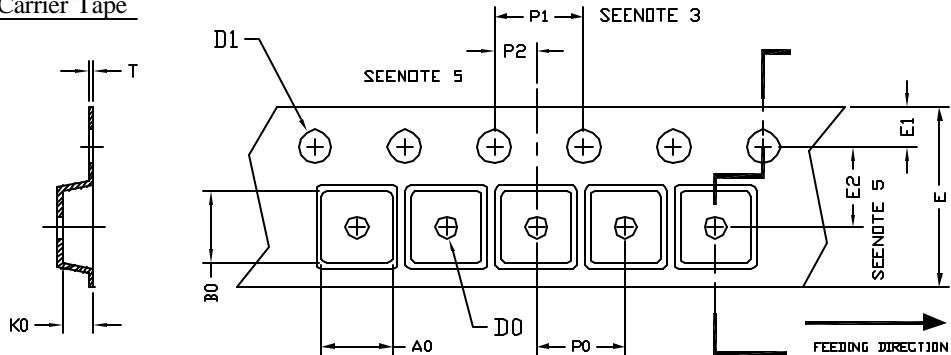
UNIT: mm



ALPHA & OMEGA
SEMICONDUCTOR, INC.

SO-8 Tape and Reel Data

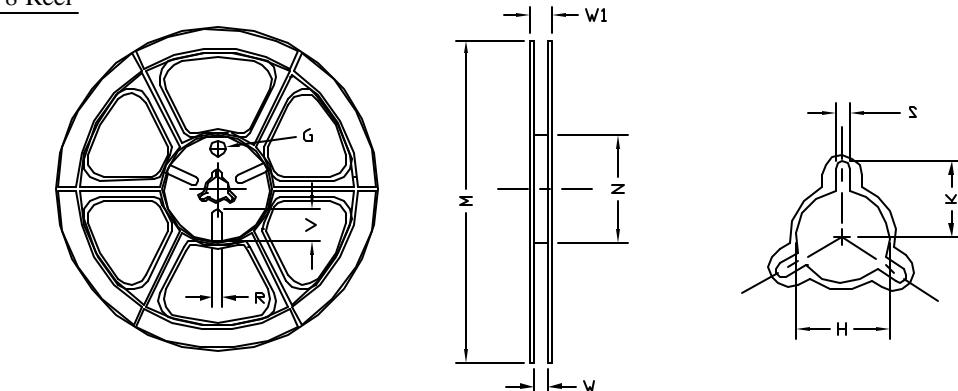
SO-8 Carrier Tape



UNIT: MM

PACKAGE	A_0	B_0	K_0	D_0	D_1	E	E_1	E_2	P_0	P_1	P_2	T
SO-8 (12 mm)	6.40 ± 0.10	52.0 ± 0.10	2.10 ± 0.10	16.0 ± 0.10	1.50 ± 0.10	12.00 ± 0.30	1.75 ± 0.10	5.50 ± 0.05	8.00 ± 0.10	4.00 ± 0.10	2.00 ± 0.05	0.25 ± 0.05

SO-8 Reel



UNIT: MM

TAPE SIZE	REEL SIZE	M	N	W	W_1	H	K	S	G	R	V
12 mm	$\phi 330$	$\phi 330.00$ ± 0.50	$\phi 97.00$ ± 0.10	13.00 ± 0.30	17.40 ± 1.00	$\phi 13.00$ $+0.50$ -0.20	10.60	2.00 ± 0.50	---	---	---

SO-8 Tape

Leader / Trailer
& Orientation

